# Contents

1 Introduction 1  
1.1 Introduction ........................................... 1  
1.2 Overview .................................................. 3  

2 Structure 5  
2.1 Conventions ................................................... 5  
2.2 Values .......................................................... 7  
2.3 Types .......................................................... 9  
2.4 Instructions ..................................................... 12  
2.5 Modules ......................................................... 20  

3 Validation 25  
3.1 Conventions ..................................................... 25  
3.2 Types .......................................................... 28  
3.3 Instructions ..................................................... 31  
3.4 Modules ......................................................... 47  

4 Execution 55  
4.1 Conventions ..................................................... 55  
4.2 Runtime Structure ............................................. 57  
4.3 Numerics ........................................................ 64  
4.4 Instructions ..................................................... 87  
4.5 Modules ......................................................... 120  

5 Binary Format 131  
5.1 Conventions ..................................................... 131  
5.2 Values .......................................................... 133  
5.3 Types .......................................................... 134  
5.4 Instructions ..................................................... 136  
5.5 Modules ......................................................... 147  

6 Text Format 155  
6.1 Conventions ..................................................... 155  
6.2 Lexical Format .................................................. 157  
6.3 Values .......................................................... 159  
6.4 Types .......................................................... 161  
6.5 Instructions ..................................................... 162  
6.6 Modules ......................................................... 175  

7 Appendix 183  
7.1 Embedding ....................................................... 183
1.1 Introduction

WebAssembly (abbreviated Wasm\(^2\)) is a *safe, portable, low-level code format* designed for efficient execution and compact representation. Its main goal is to enable high performance applications on the Web, but it does not make any Web-specific assumptions or provide Web-specific features, so it can be employed in other environments as well.

WebAssembly is an open standard developed by a [W3C Community Group]\(^1\).

This document describes version 2.0 (Draft 2022-04-16) of the *core* WebAssembly standard. It is intended that it will be superseded by new incremental releases with additional features in the future.

1.1.1 Design Goals

The design goals of WebAssembly are the following:

- **Fast, safe, and portable semantics:**
  - **Fast:** executes with near native code performance, taking advantage of capabilities common to all contemporary hardware.
  - **Safe:** code is validated and executes in a memory-safe\(^3\), sandboxed environment preventing data corruption or security breaches.
  - **Well-defined:** fully and precisely defines valid programs and their behavior in a way that is easy to reason about informally and formally.
  - **Hardware-independent:** can be compiled on all modern architectures, desktop or mobile devices and embedded systems alike.
  - **Language-independent:** does not privilege any particular language, programming model, or object model.
  - **Platform-independent:** can be embedded in browsers, run as a stand-alone VM, or integrated in other environments.

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\(^2\) A contraction of “WebAssembly”, not an acronym, hence not using all-caps.

\(^1\) [https://www.w3.org/community/webassembly/](https://www.w3.org/community/webassembly/)

\(^3\) No program can break WebAssembly’s memory model. Of course, it cannot guarantee that an unsafe language compiling to WebAssembly does not corrupt its own memory layout, e.g. inside WebAssembly’s linear memory.
– **Open**: programs can interoperate with their environment in a simple and universal manner.

  **Efficient and portable representation:**

  – **Compact**: has a binary format that is fast to transmit by being smaller than typical text or native code formats.
  – **Modular**: programs can be split up in smaller parts that can be transmitted, cached, and consumed separately.
  – **Efficient**: can be decoded, validated, and compiled in a fast single pass, equally with either just-in-time (JIT) or ahead-of-time (AOT) compilation.
  – **Streamable**: allows decoding, validation, and compilation to begin as soon as possible, before all data has been seen.
  – **Parallelizable**: allows decoding, validation, and compilation to be split into many independent parallel tasks.
  – **Portable**: makes no architectural assumptions that are not broadly supported across modern hardware.

WebAssembly code is also intended to be easy to inspect and debug, especially in environments like web browsers, but such features are beyond the scope of this specification.

### 1.1.2 Scope

At its core, WebAssembly is a **virtual instruction set architecture (virtual ISA)**. As such, it has many use cases and can be embedded in many different environments. To encompass their variety and enable maximum reuse, the WebAssembly specification is split and layered into several documents.

This document is concerned with the core ISA layer of WebAssembly. It defines the instruction set, binary encoding, validation, and execution semantics, as well as a textual representation. It does not, however, define how WebAssembly programs can interact with a specific environment they execute in, nor how they are invoked from such an environment.

Instead, this specification is complemented by additional documents defining interfaces to specific embedding environments such as the Web. These will each define a WebAssembly **application programming interface (API)** suitable for a given environment.

### 1.1.3 Security Considerations

WebAssembly provides no ambient access to the computing environment in which code is executed. Any interaction with the environment, such as I/O, access to resources, or operating system calls, can only be performed by invoking functions provided by the embedder and imported into a WebAssembly module. An embedder can establish security policies suitable for a respective environment by controlling or limiting which functional capabilities it makes available for import. Such considerations are an embedder’s responsibility and the subject of API definitions for a specific environment.

Because WebAssembly is designed to be translated into machine code running directly on the host’s hardware, it is potentially vulnerable to side channel attacks on the hardware level. In environments where this is a concern, an embedder may have to put suitable mitigations into place to isolate WebAssembly computations.
1.1.4 Dependencies

WebAssembly depends on two existing standards:

- IEEE 754-2019\(^6\), for the representation of floating-point data and the semantics of respective numeric operations.
- Unicode\(^5\), for the representation of import/export names and the text format.

However, to make this specification self-contained, relevant aspects of the aforementioned standards are defined and formalized as part of this specification, such as the binary representation and rounding of floating-point values, and the value range and UTF-8 encoding of Unicode characters.

Note: The aforementioned standards are the authoritative source of all respective definitions. Formalizations given in this specification are intended to match these definitions. Any discrepancy in the syntax or semantics described is to be considered an error.

1.2 Overview

1.2.1 Concepts

WebAssembly encodes a low-level, assembly-like programming language. This language is structured around the following concepts.

**Values** WebAssembly provides only four basic number types. These are integers and IEEE 754-2019\(^6\) numbers, each in 32 and 64 bit width. 32 bit integers also serve as Booleans and as memory addresses. The usual operations on these types are available, including the full matrix of conversions between them. There is no distinction between signed and unsigned integer types. Instead, integers are interpreted by respective operations as either unsigned or signed in two’s complement representation.

In addition to these basic number types, there is a single 128 bit wide vector type representing different types of packed data. The supported representations are 4 32-bit, or 2 64-bit IEEE 754-2019\(^7\) numbers, or different widths of packed integer values specifically 2 64-bit integers, 4 32-bit integers, 8 16-bit integers, or 16 8-bit integers.

Finally, values can consist of opaque references that represent pointers towards different sorts of entities. Unlike with other types, their size or representation is not observable.

**Instructions** The computational model of WebAssembly is based on a stack machine. Code consists of sequences of instructions that are executed in order. Instructions manipulate values on an implicit operand stack\(^8\) and fall into two main categories. Simple instructions perform basic operations on data. They pop arguments from the operand stack and push results back to it. Control instructions alter control flow. Control flow is structured, meaning it is expressed with well-nested constructs such as blocks, loops, and conditionals. Branches can only target such constructs.

**Traps** Under some conditions, certain instructions may produce a trap, which immediately aborts execution. Traps cannot be handled by WebAssembly code, but are reported to the outside environment, where they typically can be caught.

**Functions** Code is organized into separate functions. Each function takes a sequence of values as parameters and returns a sequence of values as results. Functions can call each other, including recursively, resulting in an implicit call stack that cannot be accessed directly. Functions may also declare mutable local variables that are usable as virtual registers.

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\(^4\) https://ieeexplore.ieee.org/document/8766229  
\(^5\) https://www.unicode.org/versions/latest/  
\(^6\) https://ieeexplore.ieee.org/document/8766229  
\(^7\) https://ieeexplore.ieee.org/document/8766229  
\(^8\) In practice, implementations need not maintain an actual operand stack. Instead, the stack can be viewed as a set of anonymous registers that are implicitly referenced by instructions. The type system ensures that the stack height, and thus any referenced register, is always known statically.
Tables  A table is an array of opaque values of a particular element type. It allows programs to select such values indirectly through a dynamic index operand. Currently, the only available element type is an untyped function reference. Thereby, a program can call functions indirectly through a dynamic index into a table. For example, this allows emulating function pointers by way of table indices.

Linear Memory  A linear memory is a contiguous, mutable array of raw bytes. Such a memory is created with an initial size but can be grown dynamically. A program can load and store values from/to a linear memory at any byte address (including unaligned). Integer loads and stores can specify a storage size which is smaller than the size of the respective value type. A trap occurs if an access is not within the bounds of the current memory size.

Modules  A WebAssembly binary takes the form of a module that contains definitions for functions, tables, and linear memories, as well as mutable or immutable global variables. Definitions can also be imported, specifying a module/name pair and a suitable type. Each definition can optionally be exported under one or more names. In addition to definitions, modules can define initialization data for their memories or tables that takes the form of segments copied to given offsets. They can also define a start function that is automatically executed.

Embedder  A WebAssembly implementation will typically be embedded into a host environment. This environment defines how loading of modules is initiated, how imports are provided (including host-side definitions), and how exports can be accessed. However, the details of any particular embedding are beyond the scope of this specification, and will instead be provided by complementary, environment-specific API definitions.

1.2.2 Semantic Phases

Conceptually, the semantics of WebAssembly is divided into three phases. For each part of the language, the specification specifies each of them.

Decoding  WebAssembly modules are distributed in a binary format. Decoding processes that format and converts it into an internal representation of a module. In this specification, this representation is modelled by abstract syntax, but a real implementation could compile directly to machine code instead.

Validation  A decoded module has to be valid. Validation checks a number of well-formedness conditions to guarantee that the module is meaningful and safe. In particular, it performs type checking of functions and the instruction sequences in their bodies, ensuring for example that the operand stack is used consistently.

Execution  Finally, a valid module can be executed. Execution can be further divided into two phases:

   Instantiation.  A module instance is the dynamic representation of a module, complete with its own state and execution stack. Instantiation executes the module body itself, given definitions for all its imports. It initializes globals, memories and tables and invokes the module’s start function if defined. It returns the instances of the module’s exports.

   Invocation.  Once instantiated, further WebAssembly computations can be initiated by invoking an exported function on a module instance. Given the required arguments, that executes the respective function and returns its results.

Instantiation and invocation are operations within the embedding environment.
2.1 Conventions

WebAssembly is a programming language that has multiple concrete representations (its binary format and the text format). Both map to a common structure. For conciseness, this structure is described in the form of an abstract syntax. All parts of this specification are defined in terms of this abstract syntax.

2.1.1 Grammar Notation

The following conventions are adopted in defining grammar rules for abstract syntax.

- Terminal symbols (atoms) are written in sans-serif font: i32, end.
- Nonterminal symbols are written in italic font: valtype, instr.
- $A^n$ is a sequence of $n \geq 0$ iterations of $A$.
- $A^*$ is a possibly empty sequence of iterations of $A$. (This is a shorthand for $A^n$ used where $n$ is not relevant.)
- $A^+$ is a non-empty sequence of iterations of $A$. (This is a shorthand for $A^n$ where $n \geq 1$.)
- $A^?$ is an optional occurrence of $A$. (This is a shorthand for $A^n$ where $n \leq 1$.)
- Productions are written $sym ::= A_1 | \ldots | A_n$.
- Large productions may be split into multiple definitions, indicated by ending the first one with explicit ellipses, $sym ::= A_1 | \ldots$, and starting continuations with ellipses, $sym ::= \ldots | A_2$.
- Some productions are augmented with side conditions in parentheses, “(if condition)”, that provide a shorthand for a combinatorial expansion of the production into many separate cases.
- If the same meta variable or non-terminal symbol appears multiple times in a production, then all those occurrences must have the same instantiation. (This is a shorthand for a side condition requiring multiple different variables to be equal.)
2.1.2 Auxiliary Notation

When dealing with syntactic constructs the following notation is also used:

- \( \epsilon \) denotes the empty sequence.
- \( |s| \) denotes the length of a sequence \( s \).
- \( s[i] \) denotes the \( i \)-th element of a sequence \( s \), starting from 0.
- \( s[i : n] \) denotes the sub-sequence \( s[i] \ldots s[i + n - 1] \) of a sequence \( s \).
- \( s \) with \([i] = A \) denotes the same sequence as \( s \), except that the \( i \)-th element is replaced with \( A \).
- \( s \) with \([i : n] = A^n \) denotes the same sequence as \( s \), except that the sub-sequence \( s[i : n] \) is replaced with \( A^n \).
- \( \text{concat}(s^*) \) denotes the flat sequence formed by concatenating all sequences \( s_i \) in \( s^* \).

Moreover, the following conventions are employed:

- The notation \( x^n \), where \( x \) is a non-terminal symbol, is treated as a meta variable ranging over respective sequences of \( x \) (similarly for \( x^*, x^+, x^? \)).
- When given a sequence \( x^n \), then the occurrences of \( x \) in a sequence written \( (A_1 x A_2)^n \) are assumed to be in point-wise correspondence with \( x^n \) (similarly for \( x^*, x^+, x^? \)). This implicitly expresses a form of mapping syntactic constructions over a sequence.

Productions of the following form are interpreted as records that map a fixed set of fields \( \text{field}_i \) to “values” \( A_i \), respectively:

\[
 r ::= \{ \text{field}_1 A_1, \text{field}_2 A_2, \ldots \}
\]

The following notation is adopted for manipulating such records:

- \( r.\text{field} \) denotes the contents of the field component of \( r \).
- \( r \) with \( \text{field} = A \) denotes the same record as \( r \), except that the contents of the field component is replaced with \( A \).
- \( r_1 \oplus r_2 \) denotes the composition of two records with the same fields of sequences by appending each sequence point-wise:

\[
\{\text{field}_1 A_1^*, \text{field}_2 A_2^*, \ldots\} \oplus \{\text{field}_1 B_1^*, \text{field}_2 B_2^*, \ldots\} = \{\text{field}_1 A_1^* B_1^*, \text{field}_2 A_2^* B_2^*, \ldots\}
\]

- \( \bigoplus r^* \) denotes the composition of a sequence of records, respectively; if the sequence is empty, then all fields of the resulting record are empty.

The update notation for sequences and records generalizes recursively to nested components accessed by “paths” \( \text{pth} ::= ([\ldots] \mid \cdot \text{field})^+ \):

- \( s \) with \([i] \text{ pth} = A \) is short for \( s \) with \([i] = (s[i] \text{ with pth} = A) \).
- \( r \) with \( \text{field} \text{ pth} = A \) is short for \( r \) with \( \text{field} = (r.\text{field} \text{ with pth} = A) \).

where \( r \) with \( .\text{field} = A \) is shortened to \( r \) with \( \text{field} = A \).

2.1.3 Vectors

Vectors are bounded sequences of the form \( A^n \) (or \( A^* \)), where the \( A \) can either be values or complex constructions. A vector can have at most \( 2^{32} - 1 \) elements.

\[
\text{vec}(A) ::= A^n \quad (\text{if } n < 2^{32})
\]
2.2 Values

WebAssembly programs operate on primitive numeric values. Moreover, in the definition of programs, immutable sequences of values occur to represent more complex data, such as text strings or other vectors.

2.2.1 Bytes

The simplest form of value are raw uninterpreted bytes. In the abstract syntax they are represented as hexadecimal literals.

\[
\text{byte} ::= \text{0x00} | \ldots | \text{0xFF}
\]

Conventions

- The meta variable \( b \) ranges over bytes.
- Bytes are sometimes interpreted as natural numbers \( n < 256 \).

2.2.2 Integers

Different classes of integers with different value ranges are distinguished by their bit width \( N \) and by whether they are unsigned or signed.

\[
\begin{align*}
\text{u}N & ::= 0 | 1 | \ldots | 2^N - 1 \\
\text{s}N & ::= -2^{N-1} | \ldots | -1 | 0 | 1 | \ldots | 2^{N-1} - 1 \\
\text{i}N & ::= \text{u}N
\end{align*}
\]

The latter class defines uninterpreted integers, whose signedness interpretation can vary depending on context. In the abstract syntax, they are represented as unsigned values. However, some operations convert them to signed based on a two’s complement interpretation.

Note: The main integer types occurring in this specification are \( \text{u}32, \text{u}64, \text{s}32, \text{s}64, \text{i}8, \text{i}16, \text{i}32, \text{i}64 \). However, other sizes occur as auxiliary constructions, e.g., in the definition of floating-point numbers.

Conventions

- The meta variables \( m, n, i \) range over integers.
- Numbers may be denoted by simple arithmetics, as in the grammar above. In order to distinguish arithmetics like \( 2^N \) from sequences like \( (1)^N \), the latter is distinguished with parentheses.

2.2.3 Floating-Point

Floating-point data represents 32 or 64 bit values that correspond to the respective binary formats of the IEEE 754-2019 standard (Section 3.3).

Every value has a sign and a magnitude. Magnitudes can either be expressed as normal numbers of the form \( m_0.m_1m_2 \ldots m_M \cdot 2^e \), where \( e \) is the exponent and \( m \) is the significand whose most significant bit \( m_0 \) is 1, or as a subnormal number where the exponent is fixed to the smallest possible value and \( m_0 \) is 0; among the subnormals are positive and negative zero values. Since the significands are binary values, normals are represented in the form \( (1 + m \cdot 2^{-M}) \cdot 2^e \), where \( M \) is the bit width of \( m \); similarly for subnormals.

\[9\] https://ieeexplore.ieee.org/document/8766229

2.2. Values
Possible magnitudes also include the special values \( \infty \) (infinity) and \( \text{nan} \) (NaN, not a number). NaN values have a \textit{payload} that describes the mantissa bits in the underlying \textit{binary representation}. No distinction is made between signalling and quiet NaNs.

\[
f N \ ::= \ +f N\text{mag} \mid -f N\text{mag} \\
f N\text{mag} \ ::= \ (1 + uM \cdot 2^{-M}) \cdot 2^e \quad (\text{if } -2^{E-1} + 2 \leq e \leq 2^{E-1} - 1) \\
\mid \quad (0 + uM \cdot 2^{-M}) \cdot 2^e \quad (\text{if } e = -2^{E-1} + 2) \\
\mid \quad \infty \\
\mid \quad \text{nan}(n) \quad (\text{if } 1 \leq n < 2^M)
\]

where \( M = \text{signif}(N) \) and \( E = \text{expon}(N) \) with

\[
\text{signif}(32) = 23 \quad \text{expon}(32) = 8 \\
\text{signif}(64) = 52 \quad \text{expon}(64) = 11
\]

A \textit{canonical NaN} is a floating-point value \( \pm \text{nan}(\text{canon}_N) \) where \( \text{canon}_N \) is a payload whose most significant bit is 1 while all others are 0:

\[
\text{canon}_N = 2^{\text{signif}(N)-1}
\]

An \textit{arithmetic NaN} is a floating-point value \( \pm \text{nan}(n) \) with \( n \geq \text{canon}_N \), such that the most significant bit is 1 while all others are arbitrary.

\textbf{Note:} In the abstract syntax, subnormals are distinguished by the leading 0 of the significand. The exponent of subnormals has the same value as the smallest possible exponent of a normal number. Only in the \textit{binary representation} the exponent of a subnormal is encoded differently than the exponent of any normal number.

\textbf{Conventions}

- The meta variable \( z \) ranges over floating-point values where clear from context.

\textbf{2.2.4 Vectors}

\textit{Numeric vectors} are 128-bit values that are processed by vector instructions (also known as \textit{SIMD} instructions, single instruction multiple data). They are represented in the abstract syntax using \textit{i128}. The interpretation of lane types (\textit{integer} or \textit{floating-point} numbers) and lane sizes are determined by the specific instruction operating on them.

\textbf{2.2.5 Names}

\textit{Names} are sequences of \textit{characters}, which are \textit{scalar values} as defined by \textit{Unicode}\(^\text{10}\) (Section 2.4).

\[
\text{name} \ ::= \ \text{char}\^* \quad (\text{if } |\text{utf8(char}\^*)| < 2^{32}) \\
\text{char} \ ::= \ U+00 \mid \ldots \mid U+D7FF \mid U+E000 \mid \ldots \mid U+10FFFF
\]

Due to the limitations of the \textit{binary format}, the length of a name is bounded by the length of its \textit{UTF-8} encoding.

\(^{10}\) https://www.unicode.org/versions/latest/
Convention

• Characters (Unicode scalar values) are sometimes used interchangeably with natural numbers $n < 1114112$.

2.3 Types

Various entities in WebAssembly are classified by types. Types are checked during validation, instantiation, and possibly execution.

2.3.1 Number Types

Number types classify numeric values.

\[
\text{numtype ::= i32 | i64 | f32 | f64}
\]

The types \(i32\) and \(i64\) classify 32 and 64 bit integers, respectively. Integers are not inherently signed or unsigned, their interpretation is determined by individual operations.

The types \(f32\) and \(f64\) classify 32 and 64 bit floating-point data, respectively. They correspond to the respective binary floating-point representations, also known as single and double precision, as defined by the IEEE 754-2019\footnote{https://ieeexplore.ieee.org/document/8766229} standard (Section 3.3).

Number types are transparent, meaning that their bit patterns can be observed. Values of number type can be stored in memories.

Conventions

• The notation $|t|$ denotes the bit width of a number type $t$. That is, $|i32| = |f32| = 32$ and $|i64| = |f64| = 64$.

2.3.2 Vector Types

Vector types classify vectors of numeric values processed by vector instructions (also known as SIMD instructions, single instruction multiple data).

\[
\text{vectype ::= v128}
\]

The type \(v128\) corresponds to a 128 bit vector of packed integer or floating-point data. The packed data can be interpreted as signed or unsigned integers, single or double precision floating-point values, or a single 128 bit type. The interpretation is determined by individual operations.

Vector types, like number types are transparent, meaning that their bit patterns can be observed. Values of vector type can be stored in memories.

Conventions

• The notation $|t|$ for bit width extends to vector types as well, that is, $|v128| = 128$. 

\[\text{11 https://ieeexplore.ieee.org/document/8766229}\]
2.3.3 Reference Types

Reference types classify first-class references to objects in the runtime store.

\[
\text{reftype} ::= \text{funcref} \mid \text{externref}
\]

The type \text{funcref} denotes the infinite union of all references to functions, regardless of their function types.

The type \text{externref} denotes the infinite union of all references to objects owned by the embedder and that can be passed into WebAssembly under this type.

Reference types are opaque, meaning that neither their size nor their bit pattern can be observed. Values of reference type can be stored in tables.

2.3.4 Value Types

Value types classify the individual values that WebAssembly code can compute with and the values that a variable accepts. They are either number types, vector types, or reference types.

\[
\text{valtype} ::= \text{numtype} \mid \text{vectype} \mid \text{reftype}
\]

Conventions

- The meta variable \( t \) ranges over value types or subclasses thereof where clear from context.

2.3.5 Result Types

Result types classify the result of executing instructions or functions, which is a sequence of values, written with brackets.

\[
\text{resulttype} ::= [\text{vec(valtype)}]
\]

2.3.6 Function Types

Function types classify the signature of functions, mapping a vector of parameters to a vector of results. They are also used to classify the inputs and outputs of instructions.

\[
\text{functype} ::= \text{resulttype} \to \text{resulttype}
\]

2.3.7 Limits

Limits classify the size range of resizeable storage associated with memory types and table types.

\[
\text{limits} ::= \{\text{min u32}, \text{max u32}\}
\]

If no maximum is given, the respective storage can grow to any size.
2.3.8 Memory Types

Memory types classify linear memories and their size range.

\[
\text{memtype} ::= \text{limits}
\]

The limits constrain the minimum and optionally the maximum size of a memory. The limits are given in units of page size.

2.3.9 Table Types

Table types classify tables over elements of reference type within a size range.

\[
\text{tabletype} ::= \text{limits reftype}
\]

Like memories, tables are constrained by limits for their minimum and optionally maximum size. The limits are given in numbers of entries.

**Note:** In future versions of WebAssembly, additional element types may be introduced.

2.3.10 Global Types

Global types classify global variables, which hold a value and can either be mutable or immutable.

\[
\text{globaltype} ::= \text{mut valtype}
\]

\[
\text{mut} ::= \text{const} | \text{var}
\]

2.3.11 External Types

External types classify imports and external values with their respective types.

\[
\text{externtype} ::= \text{func functype} | \text{table tabletype} | \text{mem memtype} | \text{global globaltype}
\]

**Conventions**

The following auxiliary notation is defined for sequences of external types. It filters out entries of a specific kind in an order-preserving fashion:

- \(\text{funcs}(\text{externtype}^*) = [\text{functype} | (\text{func functype}) \in \text{externtype}^*] \)
- \(\text{tables}(\text{externtype}^*) = [\text{tabletype} | (\text{table tabletype}) \in \text{externtype}^*] \)
- \(\text{mems}(\text{externtype}^*) = [\text{memtype} | (\text{mem memtype}) \in \text{externtype}^*] \)
- \(\text{globals}(\text{externtype}^*) = [\text{globaltype} | (\text{global globaltype}) \in \text{externtype}^*] \)
2.4 Instructions

WebAssembly code consists of sequences of instructions. Its computational model is based on a stack machine in that instructions manipulate values on an implicit operand stack, consuming (popping) argument values and producing or returning (pushing) result values.

In addition to dynamic operands from the stack, some instructions also have static immediate arguments, typically indices or type annotations, which are part of the instruction itself.

Some instructions are structured in that they bracket nested sequences of instructions.

The following sections group instructions into a number of different categories.

2.4.1 Numeric Instructions

Numeric instructions provide basic operations over numeric values of specific type. These operations closely match respective operations available in hardware.

\[
\begin{align*}
\text{nn, mm} & ::= 32 \mid 64 \\
\text{sx} & ::= u \mid s \\
\text{instr} & ::= \text{inn.const inn} \mid \text{fnn.const fnn} \\
& \quad \mid \text{inn.iunop} \mid \text{fnn.funop} \\
& \quad \mid \text{inn.ibinop} \mid \text{fnn.fbinop} \\
& \quad \mid \text{inn.itestop} \\
& \quad \mid \text{inn.irelop} \mid \text{fnn.frelop} \\
& \quad \mid \text{inn.extend8_s} \mid \text{inn.extend16_s} \mid \text{i64.extend32_s} \\
& \quad \mid \text{i32.wrap_i64} \mid \text{i64.extend_i32_sx} \mid \text{inn.trunc_fmm_sx} \\
& \quad \mid \text{f32.demote_f64} \mid \text{f64.promote_f32} \mid \text{fnn.convert_imm_sx} \\
& \quad \mid \text{inn.reinterpret_fnn} \mid \text{fnn.reinterpret_inn} \\
\text{iunop} & ::= \text{clz} \mid \text{ctz} \mid \text{popcnt} \\
\text{ibinop} & ::= \text{add} \mid \text{sub} \mid \text{mul} \mid \text{div_sx} \mid \text{rem_sx} \\
& \quad \mid \text{and} \mid \text{or} \mid \text{xor} \mid \text{shl} \mid \text{shr_sx} \mid \text{rotl} \mid \text{rotr} \\
\text{funop} & ::= \text{abs} \mid \text{neg} \mid \text{sqrt} \mid \text{ceil} \mid \text{floor} \mid \text{trunc} \mid \text{nearest} \\
\text{fbinop} & ::= \text{add} \mid \text{sub} \mid \text{mul} \mid \text{div} \mid \text{min} \mid \text{max} \mid \text{copysign} \\
\text{itestop} & ::= \text{eqz} \\
\text{irelop} & ::= \text{eq} \mid \text{ne} \mid \text{lt_sx} \mid \text{gt_sx} \mid \text{le_sx} \mid \text{ge_sx} \\
\text{frelop} & ::= \text{eq} \mid \text{ne} \mid \text{lt} \mid \text{gt} \mid \text{le} \mid \text{ge}
\end{align*}
\]

Numeric instructions are divided by number type. For each type, several subcategories can be distinguished:

- **Constants**: return a static constant.
- **Unary Operations**: consume one operand and produce one result of the respective type.
- **Binary Operations**: consume two operands and produce one result of the respective type.
- **Tests**: consume one operand of the respective type and produce a Boolean integer result.
- **Comparisons**: consume two operands of the respective type and produce a Boolean integer result.
- **Conversions**: consume a value of one type and produce a result of another (the source type of the conversion is the one after the "_").

Some integer instructions come in two flavors, where a signedness annotation sx distinguishes whether the operands are to be interpreted as unsigned or signed integers. For the other integer instructions, the use of two’s complement for the signed interpretation means that they behave the same regardless of signedness.
Conventions

Occasionally, it is convenient to group operators together according to the following grammar shorthands:

\[
\begin{align*}
\text{unop} & ::= \text{iunop} | \text{funop} | \text{extend} \cdot N \cdot s \\
\text{binop} & ::= \text{ibinop} | \text{fbinop} \\
\text{testop} & ::= \text{ittestop} \\
\text{relop} & ::= \text{irelop} | \text{frelop} \\
\text{cvtop} & ::= \text{wrap} | \text{extend} | \text{trunc} | \text{trunc\_sat} | \text{convert} | \text{demote} | \text{promote} | \text{reinterpret}
\end{align*}
\]

2.4. Instructions
2.4.2 Vector Instructions

Vector instructions (also known as SIMD instructions, single data multiple value) provide basic operations over values of vector type.

$$\text{ishape} ::= \text{v8}$$

$$\text{fshape} ::= \text{i16x8} | \text{i32x4} | \text{i64x2}$$

$$\text{shape} ::= \text{ishape} | \text{fshape}$$

$$\text{half} ::= \text{low} | \text{high}$$

$$\text{laneidx} ::= \text{v8}$$

$$\text{instr} ::= \ldots$$

$$\text{v128.const i128}$$

$$\text{v128.vunop}$$

$$\text{v128.vbinop}$$

$$\text{v128.vternop}$$

$$\text{v128.vtestop}$$

$$\text{i8x16.shuffle laneidx}$$

$$\text{i8x16.swizzle}$$

$$\text{shape}.\text{splat}$$

$$\text{i8x16.extract_lane_sx laneidx} | \text{i16x8.extract_lane_sx laneidx}$$

$$\text{i32x4.extract_lane_sx laneidx} | \text{i64x2.extract_lane_sx laneidx}$$

$$\text{fshape}.\text{extract_lane_sx laneidx}$$

$$\text{shape}.\text{replace_lane laneidx}$$

$$\text{i8x16.virelop} | \text{i16x8.virelop} | \text{i32x4.virelop}$$

$$\text{i64x2.eq} | \text{i64x2.ne} | \text{i64x2.lt_s} | \text{i64x2.gt_s} | \text{i64x2.le_s} | \text{i64x2.ge_s}$$

$$\text{fshape}.\text{vfrelop}$$

$$\text{ishape}.\text{viunop} | \text{i8x16.popcnt}$$

$$\text{i16x8.q15mulr_sat_s}$$

$$\text{i32x4.dot_i16x8_s}$$

$$\text{fshape}.\text{vfunop}$$

$$\text{ishape}.\text{vtestop}$$

$$\text{ishape}.\text{vatown}$$

$$\text{i8x16.narrow_i16x8_sx} | \text{i16x8.narrow_i32x4_sx}$$

$$\text{i16x8.extend_half_i8x16_sx} | \text{i32x4.extend_half_i16x8_sx}$$

$$\text{i64x2.extend_half_i32x4_sx}$$

$$\text{ishape}.\text{vishiftop}$$

$$\text{ishape}.\text{vbinop}$$

$$\text{i8x16.viminmaxop} | \text{i16x8.viminmaxop} | \text{i32x4.viminmaxop}$$

$$\text{i8x16.visatbinop} | \text{i16x8.visatbinop}$$

$$\text{i16x8.mul} | \text{i32x4.mul} | \text{i64x2.mul}$$

$$\text{i8x16.avgr_u} | \text{i16x8.avgr_u}$$

$$\text{i16x8.extmul_half_i8x16_sx} | \text{i32x4.extmul_half_i16x8_sx} | \text{i64x2.extmul_half_i32x4_sx}$$

$$\text{i16x8.extadd_pairwise_i8x16_sx} | \text{i32x4.extadd_pairwise_i16x8_sx}$$

$$\text{fshape}.\text{vbinop}$$

$$\text{i32x4.trunc_sat_f32x4_sx} | \text{i32x4.trunc_sat_f64x2_sx_zero}$$

$$\text{f32x4.convert_i32x4_sx} | \text{f32x4.deMOTE_f64x2_zero}$$

$$\text{f64x2.convert_low_i32x4_sx} | \text{f64x2.promote_low_f32x4}$$

$$\ldots$$

$$\text{vunop} ::= \text{not}$$

$$\text{vbinop} ::= \text{and} | \text{andnot} | \text{or} | \text{xor}$$

$$\text{vternop} ::= \text{bitselect}$$

$$\text{vtestop} ::= \text{any_true}$$

$$\text{virelop} ::= \text{eq} | \text{ne} | \text{lt_sx} | \text{gt_sx} | \text{le_sx} | \text{ge_sx}$$

$$\text{vfrelop} ::= \text{eq} | \text{ne} | \text{lt} | \text{gt} | \text{le} | \text{ge}$$

$$\text{viunop} ::= \text{abs} | \text{neg}$$

$$\text{vbinop} ::= \text{add} | \text{sub}$$

$$\text{viminmaxop} ::= \text{min_sx} | \text{max_sx}$$

$$\text{visatbinop} ::= \text{add_sat_sx} | \text{sub_sat_sx}$$

$$\text{vishiftop} ::= \text{shl} | \text{shr_sx}$$

$$\text{vfunop} ::= \text{abs} | \text{neg} | \text{sqrt} | \text{ceil} | \text{floor} | \text{trunc} | \text{nearest}$$

$$\text{vfunop} ::= \text{add} | \text{sub} | \text{mul} | \text{div} | \text{min} | \text{max} | \text{pmin} | \text{pmax}$$
Vector instructions have a naming convention involving a prefix that determines how their operands will be interpreted. This prefix describes the shape of the operand, written \(txN\), and consisting of a packed numeric type \(t\) and the number of lanes \(N\) of that type. Operations are performed point-wise on the values of each lane.

**Note:** For example, the shape \(i32x4\) interprets the operand as four \(i32\) values, packed into an \(i128\). The bitwidth of the numeric type \(t\) times \(N\) always is 128.

Instructions prefixed with \(v128\) do not involve a specific interpretation, and treat the \(v128\) as an \(i128\) value or a vector of 128 individual bits.

Vector instructions can be grouped into several subcategories:

- **Constants**: return a static constant.
- **Unary Operations**: consume one \(v128\) operand and produce one \(v128\) result.
- **Binary Operations**: consume two \(v128\) operands and produce one \(v128\) result.
- **Ternary Operations**: consume three \(v128\) operands and produce one \(v128\) result.
- **Tests**: consume one \(v128\) operand and produce a Boolean integer result.
- **Shifts**: consume a \(v128\) operand and a \(i32\) operand, producing one \(v128\) result.
- **Splats**: consume a value of numeric type and produce a \(v128\) result of a specified shape.
- **Extract lanes**: consume a \(v128\) operand and return the numeric value in a given lane.
- **Replace lanes**: consume a \(v128\) operand and a numeric value for a given lane, and produce a \(v128\) result.

Some vector instructions have a signedness annotation \(sx\) which distinguishes whether the elements in the operands are to be interpreted as unsigned or signed integers. For the other vector instructions, the use of two’s complement for the signed interpretation means that they behave the same regardless of signedness.

**Conventions**

Occasionally, it is convenient to group operators together according to the following grammar shorthands:

- \(vu\)n\(op\) ::= \(vi\)n\(op\) | \(vf\)n\(op\) | \(popcnt\)
- \(vb\)in\(op\) ::= \(vi\)bin\(op\) | \(vf\)bin\(op\)
- \(v\)min\(ax\)op | \(vi\)sat\(bin\)op
- \(mul\) | \(avgr\)\(_u\) | \(q15mulr\)\(_sat\)\(_s\)
- \(v\)test\(op\) ::= \(vi\)test\(op\)
- \(v\)re\(lop\) ::= \(vire\)lop | \(vf\)re\(lop\)
- \(v\)c\(v\)top ::= \(extend\) | \(trunc\)\(_sat\) | \(convert\) | \(demote\) | \(promote\)

**2.4.3 Reference Instructions**

Instructions in this group are concerned with accessing references.

- \(instr\ ::= \ldots\)
- \(ref\)null \(re\)type
- \(ref\).\(is\)\(_null\)
- \(ref\).\(func\)\(_funcidx\)

These instructions produce a null value, check for a null value, or produce a reference to a given function, respectively.
2.4.4 Parametric Instructions

Instructions in this group can operate on operands of any value type.

\[
\text{instr} ::= \ldots \\
| \text{drop} \\
| \text{select (valtype)}^? 
\]

The \text{drop} instruction simply throws away a single operand.

The \text{select} instruction selects one of its first two operands based on whether its third operand is zero or not. It may include a value type determining the type of these operands. If missing, the operands must be of numeric type.

\textbf{Note:} In future versions of WebAssembly, the type annotation on \text{select} may allow for more than a single value being selected at the same time.

2.4.5 Variable Instructions

Variable instructions are concerned with access to local or global variables.

\[
\text{instr} ::= \ldots \\
| \text{local.get localidx} \\
| \text{local.set localidx} \\
| \text{local.tee localidx} \\
| \text{global.get globalidx} \\
| \text{global.set globalidx} 
\]

These instructions get or set the values of variables, respectively. The \text{local.tee} instruction is like \text{local.set} but also returns its argument.

2.4.6 Table Instructions

Instructions in this group are concerned with tables table.

\[
\text{instr} ::= \ldots \\
| \text{table.get tableidx} \\
| \text{table.set tableidx} \\
| \text{table.size tableidx} \\
| \text{table.grow tableidx} \\
| \text{table.fill tableidx} \\
| \text{table.copy tableidx tableidx} \\
| \text{table.init tableidx elemidx} \\
| \text{elem.drop elemidx} 
\]

The \text{table.get} and \text{table.set} instructions load or store an element in a table, respectively.

The \text{table.size} instruction returns the current size of a table. The \text{table.grow} instruction grows table by a given delta and returns the previous size, or \(-1\) if enough space cannot be allocated. It also takes an initialization value for the newly allocated entries.

The \text{table.fill} instruction sets all entries in a range to a given value.

The \text{table.copy} instruction copies elements from a source table region to a possibly overlapping destination region; the first index denotes the destination. The \text{table.init} instruction copies elements from a passive element segment into a table. The \text{elem.drop} instruction prevents further use of a passive element segment. This instruction is intended to be used as an optimization hint. After an element segment is dropped its elements can no longer be retrieved, so the memory used by this segment may be freed.

An additional instruction that accesses a table is the control instruction \text{call_indirect}.
2.4.7 Memory Instructions

Instructions in this group are concerned with linear memory.

\[
\begin{align*}
\text{memory} & ::= \{\text{offset} \, \text{u32}, \text{align} \, \text{u32}\} \\
\text{ww} & ::= 8 \mid 16 \mid 32 \mid 64 \\
\text{instr} & ::= \ldots \\
& \quad | \text{inn.} \text{load memory} \mid \text{fnn.} \text{load memory} \\
& \quad | \text{inn.} \text{store memory} \mid \text{fnn.} \text{store memory} \\
& \quad | \text{inn.} \text{load8}_s z \text{ memory} \mid \text{inn.} \text{load16}_s z \text{ memory} \mid \text{i64.} \text{load32}_s z \text{ memory} \\
& \quad | \text{inn.} \text{store8 memory} \mid \text{inn.} \text{store16 memory} \mid \text{i64.} \text{store32 memory} \\
& \quad | \text{v128.} \text{load8x8}_s z \text{ memory} \mid \text{v128.} \text{load16x4}_s z \text{ memory} \mid \text{v128.} \text{load32x2}_s z \text{ memory} \\
& \quad | \text{v128.} \text{load32}_s z \text{ memory} \mid \text{v128.} \text{load64}_s z \text{ memory} \\
& \quad | \text{v128.} \text{loadww}_s z \text{ lane memory laneidx} \mid \text{v128.} \text{storeww}_s z \text{ lane memory laneidx} \\
& \quad | \text{memory.size} \\
& \quad | \text{memory.grow} \\
& \quad | \text{memory.fill} \\
& \quad | \text{memory.copy} \\
& \quad | \text{memory.init dataidx} \\
& \quad | \text{data.drop dataidx}
\end{align*}
\]

Memory is accessed with load and store instructions for the different number types. They all take a memory immediate that contains an address offset and the expected alignment (expressed as the exponent of a power of 2). Integer loads and stores can optionally specify a storage size that is smaller than the bit width of the respective value type. In the case of loads, a sign extension mode \(sx\) is then required to select appropriate behavior.

Vector loads can specify a shape that is half the bit width of \(v128\). Each lane is half its usual size, and the sign extension mode \(sx\) then specifies how the smaller lane is extended to the larger lane. Alternatively, vector loads can perform a splat, such that only a single lane of the specified storage size is loaded, and the result is duplicated to all lanes.

The static address offset is added to the dynamic address operand, yielding a 33 bit effective address that is the zero-based index at which the memory is accessed. All values are read and written in little endian order. A trap results if any of the accessed memory bytes lies outside the address range implied by the memory’s current size.

**Note:** Future version of WebAssembly might provide memory instructions with 64 bit address ranges.

The memory.size instruction returns the current size of a memory. The memory.grow instruction grows memory by a given delta and returns the previous size, or \(-1\) if enough memory cannot be allocated. Both instructions operate in units of page size.

The memory.fill instruction sets all values in a region to a given byte. The memory.copy instruction copies data from a source memory region to a possibly overlapping destination region. The memory.init instruction copies data from a passive data segment into a memory. The data.drop instruction prevents further use of a passive data segment. This instruction is intended to be used as an optimization hint. After a data segment is dropped its data can no longer be retrieved, so the memory used by this segment may be freed.

**Note:** In the current version of WebAssembly, all memory instructions implicitly operate on memory index 0. This restriction may be lifted in future versions.

2.4.8 Control Instructions

Instructions in this group affect the flow of control.

```
blocktype ::= typeidx | valtype?
instr ::= ...
nop
unreachable
block blocktype instr* end
loop blocktype instr* end
if blocktype instr* else instr* end
br labelidx
br_if labelidx
br_table vec(labelidx) labelidx
return
call funcidx
call_indirect tableidx typeidx
```

The `nop` instruction does nothing.

The `unreachable` instruction causes an unconditional `trap`.

The `block`, `loop` and `if` instructions are `structured` instructions. They bracket nested sequences of instructions, called `blocks`, terminated with, or separated by, `end` or `else` pseudo-instructions. As the grammar prescribes, they must be well-nested.

A structured instruction can consume `input` and produce `output` on the operand stack according to its annotated `block type`. It is given either as a `type index` that refers to a suitable `function type`, or as an optional `value type` inline, which is a shorthand for the function type `id -> [valtype?]`.

Each structured control instruction introduces an implicit `label`. Labels are targets for branch instructions that reference them with `label indices`. Unlike with other `index spaces`, indexing of labels is relative by nesting depth, that is, label 0 refers to the innermost structured control instruction enclosing the referring branch instruction, while increasing indices refer to those farther out. Consequently, labels can only be referenced from within the associated structured control instruction. This also implies that branches can only be directed outwards, “breaking” from the block of the control construct they target. The exact effect depends on that control construct. In case of `block` or `if` it is a `forward jump`, resuming execution after the matching `end`. In case of `loop` it is a `backward jump` to the beginning of the loop.

**Note:** This enforces `structured control flow`. Intuitively, a branch targeting a `block` or `if` behaves like a `break` statement in most C-like languages, while a branch targeting a `loop` behaves like a `continue` statement.

Branch instructions come in several flavors: `br` performs an unconditional branch, `br_if` performs a conditional branch, and `br_table` performs an indirect branch through an operand indexing into the label vector that is an immediate to the instruction, or to a default target if the operand is out of bounds. The `return` instruction is a shortcut for an unconditional branch to the outermost block, which implicitly is the body of the current function. Taking a branch `unwinds` the operand stack up to the height where the targeted structured control instruction was entered. However, branches may additionally consume operands themselves, which they push back on the operand stack after unwinding. Forward branches require operands according to the output of the targeted block’s type, i.e., represent the values produced by the terminated block. Backward branches require operands according to the input of the targeted block’s type, i.e., represent the values consumed by the restarted block.

The `call` instruction invokes another `function`, consuming the necessary arguments from the stack and returning the result values of the call. The `call_indirect` instruction calls a function indirectly through an operand indexing into a `table` that is denoted by a `table index` and must have type `funcref`. Since it may contain functions of heterogeneous type, the callee is dynamically checked against the `function type` indexed by the instruction’s second immediate, and the call is aborted with a `trap` if it does not match.
2.4.9 Expressions

Function bodies, initialization values for globals, and offsets of element or data segments are given as expressions, which are sequences of instructions terminated by an end marker.

\[ expr ::= instr^* \text{ end} \]

In some places, validation restricts expressions to be constant, which limits the set of allowable instructions.

2.5 Modules

WebAssembly programs are organized into modules, which are the unit of deployment, loading, and compilation. A module collects definitions for types, functions, tables, memories, and globals. In addition, it can declare imports and exports and provide initialization in the form of data and element segments, or a start function.

\[ \text{module} ::= \{ \text{types vec}\{\text{functype}\}, \text{funcs vec}\{\text{func}\}, \text{tables vec}\{\text{table}\}, \text{mems vec}\{\text{mem}\}, \text{globals vec}\{\text{global}\}, \text{elems vec}\{\text{elem}\}, \text{datas vec}\{\text{data}\}, \text{start start?}, \text{imports vec}\{\text{import}\}, \text{exports vec}\{\text{export}\} \} \]

Each of the vectors – and thus the entire module – may be empty.

2.5.1 Indices

Definitions are referenced with zero-based indices. Each class of definition has its own index space, as distinguished by the following classes.

\[ \text{typeid} ::= \text{u32} \]
\[ \text{funcidx} ::= \text{u32} \]
\[ \text{tableidx} ::= \text{u32} \]
\[ \text{memidx} ::= \text{u32} \]
\[ \text{globalidx} ::= \text{u32} \]
\[ \text{elemidx} ::= \text{u32} \]
\[ \text{dataidx} ::= \text{u32} \]
\[ \text{localidx} ::= \text{u32} \]
\[ \text{labelidx} ::= \text{u32} \]

The index space for functions, tables, memories and globals includes respective imports declared in the same module. The indices of these imports precede the indices of other definitions in the same index space.

Element indices reference element segments and data indices reference data segments.

The index space for locals is only accessible inside a function and includes the parameters of that function, which precede the local variables.

Label indices reference structured control instructions inside an instruction sequence.
Conventions

- The meta variable $l$ ranges over label indices.
- The meta variables $x, y$ range over indices in any of the other index spaces.
- The notation $\text{idx}(A)$ denotes the set of indices from index space $\text{idx}$ occurring free in $A$. We sometimes reinterpret this set as the vector of its elements.

**Note:** For example, if $\text{instr}^*$ is $(\text{data.drop } x)(\text{memory.init } y)$, then $\text{dataidx}((\text{instr}^*)) = \{x, y\}$, or equivalently, the vector $x \ y$.

### 2.5.2 Types

The *types* component of a module defines a vector of *function types*.

All function types used in a module must be defined in this component. They are referenced by *type indices*.

**Note:** Future versions of WebAssembly may add additional forms of type definitions.

### 2.5.3 Functions

The *funcs* component of a module defines a vector of *functions* with the following structure:

$$
\text{func ::= } \{ \text{type typeidx, locals vec(valtype), body expr} \}
$$

The *type* of a function declares its signature by reference to a *type* defined in the module. The parameters of the function are referenced through 0-based *local indices* in the function’s body; they are mutable.

The *locals* declare a vector of mutable local variables and their types. These variables are referenced through *local indices* in the function’s body. The index of the first local is the smallest index not referencing a parameter.

The *body* is an *instruction* sequence that upon termination must produce a stack matching the function type’s *result type*.

Functions are referenced through *function indices*, starting with the smallest index not referencing a function import.

### 2.5.4 Tables

The *tables* component of a module defines a vector of *tables* described by their *table type*:

$$
\text{table ::= } \{ \text{type tabletype} \}
$$

A table is a vector of opaque values of a particular *reference type*. The *min size* in the *limits* of the table type specifies the initial size of that table, while its *max*, if present, restricts the size to which it can grow later.

Tables can be initialized through *element segments*.

Tables are referenced through *table indices*, starting with the smallest index not referencing a table import. Most constructs implicitly reference table index 0.
2.5.5 Memories

The \texttt{mems} component of a module defines a vector of \textit{linear memories} (or \textit{memories} for short) as described by their \textit{memory type}:

\[
\text{mem} ::= \{\text{type memtype}\}
\]

A memory is a vector of raw uninterpreted bytes. The \textit{min size} in the \textit{limits} of the memory type specifies the initial size of that memory, while its \textit{max}, if present, restricts the size to which it can grow later. Both are in units of \textit{page size}.

Memories can be initialized through \textit{data segments}.

Memories are referenced through \textit{memory indices}, starting with the smallest index not referencing a memory import. Most constructs implicitly reference memory index 0.

\textbf{Note:} In the current version of WebAssembly, at most one memory may be defined or imported in a single module, and \textit{all} constructs implicitly reference this memory 0. This restriction may be lifted in future versions.

2.5.6 Globals

The \texttt{globals} component of a module defines a vector of \textit{global variables} (or \textit{globals} for short):

\[
\text{global} ::= \{\text{type globaltype}, \text{init expr}\}
\]

Each global stores a single value of the given \textit{global type}. Its \textit{type} also specifies whether a global is immutable or mutable. Moreover, each global is initialized with a \textit{init} value given by a \textit{constant initializer expression}.

Globals are referenced through \textit{global indices}, starting with the smallest index not referencing a global import.

2.5.7 Element Segments

The initial contents of a table is uninitialized. \textit{Element segments} can be used to initialize a subrange of a table from a static \textit{vector} of elements.

The \texttt{elems} component of a module defines a vector of element segments. Each element segment defines a \textit{reference type} and a corresponding list of \textit{constant element expressions}.

Element segments have a mode that identifies them as either \textit{passive}, \textit{active}, or \textit{declarative}. A passive element segment’s elements can be copied to a table using the \texttt{table.init} instruction. An active element segment copies its elements into a table during \textit{instantiation}, as specified by a \textit{table index} and a \textit{constant expression} defining an offset into that table. A declarative element segment is not available at runtime but merely serves to forward-declare references that are formed in code with instructions like \texttt{ref.func}.

\[
\text{elem} ::= \{\text{type ref\texttt{type}}, \text{init vec(expr)}, \text{mode elemmode}\}
\]

\[
\text{elemmode} ::= \text{passive} \\
| \quad \text{active} \{\text{table tableidx, offset expr}\} \\
| \quad \text{declarative}
\]

The \textit{offset} is given by a \textit{constant expression}.

Element segments are referenced through \textit{element indices}.

\textbf{Note:} In the current version of WebAssembly, only tables of element type \texttt{funcref} can be initialized with an element segment. This limitation may be lifted in the future.
2.5.8 Data Segments

The initial contents of a memory are zero bytes. Data segments can be used to initialize a range of memory from a static vector of bytes.

The `data` component of a module defines a vector of data segments.

Like element segments, data segments have a mode that identifies them as either passive or active. A passive data segment’s contents can be copied into a memory using the `memory.init` instruction. An active data segment copies its contents into a memory during instantiation, as specified by a `memory index` and a constant expression defining an offset into that memory.

\[
\text{data} ::= \{\text{init vec(byte), mode datamode}\}
\]

\[
\text{datamode} ::= \text{passive} \mid \text{active \{memory memidx, offset expr\}}
\]

Data segments are referenced through `data indices`.

**Note:** In the current version of WebAssembly, at most one memory is allowed in a module. Consequently, the only valid `memidx` is 0.

2.5.9 Start Function

The `start` component of a module declares the `function index` of a start function that is automatically invoked when the module is instantiated, after tables and memories have been initialized.

\[
\text{start} ::= \{\text{func funcidx}\}
\]

**Note:** The start function is intended for initializing the state of a module. The module and its exports are not accessible before this initialization has completed.

2.5.10 Exports

The `exports` component of a module defines a set of exports that become accessible to the host environment once the module has been instantiated.

\[
\text{export} ::= \{\text{name name, desc exportdesc}\}
\]

\[
\text{exportdesc} ::= \text{func funcidx} \mid \text{table tableidx} \mid \text{mem memidx} \mid \text{global globalidx}
\]

Each export is labeled by a unique `name`. Exportable definitions are functions, tables, memories, and globals, which are referenced through a respective descriptor.

**Conventions**

The following auxiliary notation is defined for sequences of exports, filtering out indices of a specific kind in an order-preserving fashion:

- `funcs(export*) = [funcidx | func funcidx ∈ (export.desc)\ast]`
- `tables(export*) = [tableidx | table tableidx ∈ (export.desc)\ast]`
- `mems(export*) = [memidx | mem memidx ∈ (export.desc)\ast]`
- `globals(export*) = [globalidx | global globalidx ∈ (export.desc)\ast]`
2.5.11 Imports

The imports component of a module defines a set of imports that are required for instantiation.

\[
\text{import} ::= \{\text{module name}, \text{name name, desc importdesc}\}
\]

\[
\text{importdesc} ::= \text{func typeidz}
| \text{table tabletype}
| \text{mem memtype}
| \text{global globaltype}
\]

Each import is labeled by a two-level name space, consisting of a module name and a name for an entity within that module. Importable definitions are functions, tables, memories, and globals. Each import is specified by a descriptor with a respective type that a definition provided during instantiation is required to match.

Every import defines an index in the respective index space. In each index space, the indices of imports go before the first index of any definition contained in the module itself.

Note: Unlike export names, import names are not necessarily unique. It is possible to import the same module/name pair multiple times; such imports may even have different type descriptions, including different kinds of entities. A module with such imports can still be instantiated depending on the specifics of how an embedder allows resolving and supplying imports. However, embedders are not required to support such overloading, and a WebAssembly module itself cannot implement an overloaded name.
CHAPTER 3

Validation

3.1 Conventions

Validation checks that a WebAssembly module is well-formed. Only valid modules can be instantiated.

Validity is defined by a type system over the abstract syntax of a module and its contents. For each piece of abstract syntax, there is a typing rule that specifies the constraints that apply to it. All rules are given in two equivalent forms:

1. In prose, describing the meaning in intuitive form.
2. In formal notation, describing the rule in mathematical form.\(^{13}\)

Note: The prose and formal rules are equivalent, so that understanding of the formal notation is not required to read this specification. The formalism offers a more concise description in notation that is used widely in programming languages semantics and is readily amenable to mathematical proof.

In both cases, the rules are formulated in a declarative manner. That is, they only formulate the constraints, they do not define an algorithm. The skeleton of a sound and complete algorithm for type-checking instruction sequences according to this specification is provided in the appendix.

3.1.1 Contexts

Validity of an individual definition is specified relative to a context, which collects relevant information about the surrounding module and the definitions in scope:

- **Types**: the list of types defined in the current module.
- **Functions**: the list of functions declared in the current module, represented by their function type.
- **Tables**: the list of tables declared in the current module, represented by their table type.
- **Memories**: the list of memories declared in the current module, represented by their memory type.
- **Globals**: the list of globals declared in the current module, represented by their global type.


\(^{14}\) https://dl.acm.org/citation.cfm?doid=3062341.3062363
• **Element Segments**: the list of element segments declared in the current module, represented by their element type.

• **Data Segments**: the list of data segments declared in the current module, each represented by an ok entry.

• **Locals**: the list of locals declared in the current function (including parameters), represented by their value type.

• **Labels**: the stack of labels accessible from the current position, represented by their result type.

• **Return**: the return type of the current function, represented as an optional result type that is absent when no return is allowed, as in free-standing expressions.

• **References**: the list of function indices that occur in the module outside functions and can hence be used to form references inside them.

In other words, a context contains a sequence of suitable types for each index space, describing each defined entry in that space. Locals, labels and return type are only used for validating instructions in function bodies, and are left empty elsewhere. The label stack is the only part of the context that changes as validation of an instruction sequence proceeds.

More concretely, contexts are defined as records $C$ with abstract syntax:

$$C ::= \{ \text{types } \text{functype}^*, \text{funcs } \text{functype}^*, \text{tables } \text{tabletype}^*, \text{mems } \text{memtype}^*, \text{globals } \text{globaltype}^*, \text{elems } \text{reftype}^*, \text{datas } \text{ok}^*, \text{locals } \text{valtype}^*, \text{labels } \text{resulttype}^*, \text{return } \text{resulttype}^?, \text{refs } \text{funcidx}^* \}$$

In addition to field access written $C$.field the following notation is adopted for manipulating contexts:

• When spelling out a context, empty fields are omitted.

• $C, \text{field } A^*$ denotes the same context as $C$ but with the elements $A^*$ prepended to its field component sequence.

**Note:** We use indexing notation like $C.labels[i]$ to look up indices in their respective index space in the context. Context extension notation $C, \text{field } A$ is primarily used to locally extend relative index spaces, such as label indices. Accordingly, the notation is defined to append at the front of the respective sequence, introducing a new relative index 0 and shifting the existing ones.

### 3.1.2 Prose Notation

Validation is specified by stylised rules for each relevant part of the abstract syntax. The rules not only state constraints defining when a phrase is valid, they also classify it with a type. The following conventions are adopted in stating these rules.

• A phrase $A$ is said to be “valid with type $T$” if and only if all constraints expressed by the respective rules are met. The form of $T$ depends on what $A$ is.

**Note:** For example, if $A$ is a function, then $T$ is a function type; for an $A$ that is a global, $T$ is a global type; and so on.

• The rules implicitly assume a given context $C$. 

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Chapter 3. Validation
• In some places, this context is locally extended to a context \( C' \) with additional entries. The formulation "Under context \( C' \), ... statement ..." is adopted to express that the following statement must apply under the assumptions embodied in the extended context.

### 3.1.3 Formal Notation

**Note:** This section gives a brief explanation of the notation for specifying typing rules formally. For the interested reader, a more thorough introduction can be found in respective textbooks.\(^{15}\)

The proposition that a phrase \( A \) has a respective type \( T \) is written \( A : T \). In general, however, typing is dependent on a context \( C \). To express this explicitly, the complete form is a *judgement* \( C \vdash A : T \), which says that \( A : T \) holds under the assumptions encoded in \( C \).

The formal typing rules use a standard approach for specifying type systems, rendering them into *deduction rules*. Every rule has the following general form:

\[
\frac{\text{premise}_1 \quad \text{premise}_2 \quad \ldots \quad \text{premise}_n}{\text{conclusion}}
\]

Such a rule is read as a big implication: if all premises hold, then the conclusion holds. Some rules have no premises; they are *axioms* whose conclusion holds unconditionally. The conclusion always is a judgement \( C \vdash A : T \), and there is one respective rule for each relevant construct \( A \) of the abstract syntax.

**Note:** For example, the typing rule for the \( \mathbf{i32}.\mathbf{add} \) instruction can be given as an axiom:

\[
C \vdash \mathbf{i32}.\mathbf{add} : \mathbf{[i32 \ i32]} \rightarrow \mathbf{i32}
\]

The instruction is always valid with type \( \mathbf{[i32 \ i32]} \rightarrow \mathbf{i32} \) (saying that it consumes two \( \mathbf{i32} \) values and produces one), independent of any side conditions.

An instruction like \( \mathbf{local}.\mathbf{get} \) can be typed as follows:

\[
C, \mathbf{locals}[x] = t \quad \Rightarrow \quad C \vdash \mathbf{local} \mathbf{get} x : \mathbf{[]} \rightarrow \mathbf{[t]}
\]

Here, the premise enforces that the immediate *local index* \( x \) exists in the context. The instruction produces a value of its respective type \( t \) (and does not consume any values). If \( C, \mathbf{locals}[x] \) does not exist then the premise does not hold, and the instruction is ill-typed.

Finally, a *structured* instruction requires a recursive rule, where the premise is itself a typing judgement:

\[
C \vdash \mathbf{blocktype} : [t_1] \rightarrow [t'_2] \quad \frac{C, \mathbf{label}[t'_2] \vdash \mathbf{instr}^* : [t_1] \rightarrow [t_2]}{C \vdash \mathbf{block} \mathbf{blocktype} \ \mathbf{instr}^* \ \mathbf{end} : [t_1] \rightarrow [t'_2]}
\]

A *block* instruction is only valid when the instruction sequence in its body is. Moreover, the result type must match the block’s annotation \( \mathbf{blocktype} \). If so, then the *block* instruction has the same type as the body. Inside the body an additional label of the corresponding result type is available, which is expressed by extending the context \( C \) with the additional label information for the premise.

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\(^{15}\) For example: Benjamin Pierce. *Types and Programming Languages*. The MIT Press 2002

\(^{16}\) https://www.cis.upenn.edu/~bcpierce/tapl/
3.2 Types

Most types are universally valid. However, restrictions apply to limits, which must be checked during validation. Moreover, block types are converted to plain function types for ease of processing.

3.2.1 Limits

Limits must have meaningful bounds that are within a given range.

\{\text{min } n, \text{max } m\}

- The value of \(n\) must not be larger than \(k\).
- If the maximum \(m\) is not empty, then:
  - Its value must not be larger than \(k\).
  - Its value must not be smaller than \(n\).
- Then the limit is valid within range \(k\).

\[
\frac{n \leq k}{\frac{(m \leq k)}{(n \leq m)}}
\] \(\vdash \{\text{min } n, \text{max } m\} : k\)

3.2.2 Block Types

Block types may be expressed in one of two forms, both of which are converted to plain function types by the following rules.

\text{typeidx}

- The type \(C\.\text{types}[\text{typeidx}]\) must be defined in the context.
- Then the block type is valid as function type \(C\.\text{types}[\text{typeidx}]\).

\[
C\.\text{types}[\text{typeidx}] = \text{functype}
\]

\[
C \vdash \text{typeidx} : \text{functype}
\]

\text{[valtype^n]}

- The block type is valid as function type \(\text{[]} \to \text{[valtype^n]}\).

\[
C \vdash \text{[valtype^n]} : \text{[]} \to \text{[valtype^n]}
\]

3.2.3 Function Types

Function types are always valid.
\([ t_1^r ] \rightarrow [ t_2^r ] \)

- The function type is valid.

\[ \vdash [ t_1^r ] \rightarrow [ t_2^r ] \text{ ok} \]

### 3.2.4 Table Types

**limits reftype**

- The limits \( limits \) must be \textit{valid} within range \( 2^{32} - 1 \).
- Then the table type is valid.

\[ \vdash limits : 2^{32} - 1 \]
\[ \vdash limits \text{ reftype ok} \]

### 3.2.5 Memory Types

**limits**

- The limits \( limits \) must be \textit{valid} within range \( 2^{16} \).
- Then the memory type is valid.

\[ \vdash limits : 2^{16} \]
\[ \vdash limits \text{ ok} \]

### 3.2.6 Global Types

**mut valtype**

- The global type is valid.

\[ \vdash mut \text{ valtype ok} \]

### 3.2.7 External Types

**func functype**

- The \textit{function type} \( functype \) must be \textit{valid}.
- Then the external type is valid.

\[ \vdash functype \text{ ok} \]
\[ \vdash func \text{ functype ok} \]
### 3.2.8 Import Subtyping

When instantiating a module, external values must be provided whose types are matched against the respective external types classifying each import. In some cases, this allows for a simple form of subtyping, as defined here.

#### Limits

Limits \( \{ \min n_1, \max m_1 \} \) match limits \( \{ \min n_2, \max m_2 \} \) if and only if:

- \( n_1 \) is larger than or equal to \( n_2 \).
- Either:
  - \( m_2 \) is empty.
- Or:
  - Both \( m_1 \) and \( m_2 \) are non-empty.
  - \( m_1 \) is smaller than or equal to \( m_2 \).

\[
\begin{align*}
\vdash \{ \min n_1, \max m_1 \} & \leq \{ \min n_2, \max m_2 \} & \vdash \{ \min n_1, \max m_1 \} & \leq \{ \min n_2, \max m_2 \}
\end{align*}
\]
Functions

An external type `func functype_1` matches `func functype_2` if and only if:

- Both `functype_1` and `functype_2` are the same.

\[ \vdash \text{func functype} \leq \text{func functype} \]

Tables

An external type `table (limits_1 reftype_1)` matches `table (limits_2 reftype_2)` if and only if:

- Limits `limits_1` match `limits_2`.
- Both `reftype_1` and `reftype_2` are the same.

\[ \vdash \text{limits} \leq \text{limits} \]
\[ \vdash \text{table (limits_1 reftype)} \leq \text{table (limits_2 reftype)} \]

Memories

An external type `mem limits_1` matches `mem limits_2` if and only if:

- Limits `limits_1` match `limits_2`.

\[ \vdash \text{limits} \leq \text{limits} \]
\[ \vdash \text{mem limits_1} \leq \text{mem limits_2} \]

Globals

An external type `global globaltype_1` matches `global globaltype_2` if and only if:

- Both `globaltype_1` and `globaltype_2` are the same.

\[ \vdash \text{global globaltype} \leq \text{global globaltype} \]

3.3 Instructions

Instructions are classified by stack types \([t_1^*] \rightarrow [t_2^*]\) that describe how instructions manipulate the operand stack.

\[
\text{stacktype} ::= [\text{opdtype}^*] \rightarrow [\text{opdtype}^*] \\
\text{opdtype} ::= \text{valtype} | \perp
\]

The types describe the required input stack with operand types \(t_1^*\) that an instruction pops off and the provided output stack with result values of types \(t_2^*\) that it pushes back. Stack types are akin to function types, except that they allow individual operands to be classified as \(\perp\) (bottom), indicating that the type is unconstrained. As an auxiliary notion, an operand type \(t_1\) matches another operand type \(t_2\), if \(t_1\) is either \(\perp\) or equal to \(t_2\). This is extended to stack types in a point-wise manner.

\[ \vdash \perp \leq t \quad \vdash \perp \leq t \]
\[
\begin{align*}
(t \leq t')^* &
\vdash [t^*] \leq [t'^*]
\end{align*}
\]

**Note:** For example, the instruction `i32.add` has type `[i32 i32] \to [i32]`, consuming two `i32` values and producing one.

Typing extends to *instruction sequences* `instr^*`. Such a sequence has a *function type* `[t_1^*] \to [t_2^*]` if the accumulative effect of executing the instructions is consuming values of types `t_1^*` off the operand stack and pushing new values of types `t_2^*`.

For some instructions, the typing rules do not fully constrain the type, and therefore allow for multiple types. Such instructions are called *polymorphic*. Two degrees of polymorphism can be distinguished:

- **value-polymorphic**: the *value type* `t` of one or several individual operands is unconstrained. That is the case for all *parametric instructions* like `drop` and `select`.

- **stack-polymorphic**: the entire (or most of the) *function type* `[t_1^*] \to [t_2^*]` of the instruction is unconstrained. That is the case for all *control instructions* that perform an *unconditional control transfer*, such as `unreachable`, `br`, `br_table`, and `return`.

In both cases, the unconstrained types or type sequences can be chosen arbitrarily, as long as they meet the constraints imposed for the surrounding parts of the program.

**Note:** For example, the `select` instruction is valid with type `[t t i32] \to [t]`, for any possible *number type* `t`. Consequently, both instruction sequences

\[
(i32\text{-}const\ 1) \ (i32\text{-}const\ 2) \ (i32\text{-}const\ 3) \ \text{select}
\]

and

\[
(f64\text{-}const\ 1.0) \ (f64\text{-}const\ 2.0) \ (i32\text{-}const\ 3) \ \text{select}
\]

are valid, with `t` in the typing of `select` being instantiated to `i32` or `f64`, respectively.

The `unreachable` instruction is valid with type `[t_1^*] \to [t_2^*]` for any possible sequences of value types `t_1^*` and `t_2^*`. Consequently,

\[
\text{unreachable \ i32.add}
\]

is valid by assuming type `[] \to [i32 i32]` for the `unreachable` instruction. In contrast,

\[
\text{unreachable \ (i64\text{-}const\ 0) \ i32.add}
\]

is invalid, because there is no possible type to pick for the `unreachable` instruction that would make the sequence well-typed.

The *Appendix* describes a type checking *algorithm* that efficiently implements validation of instruction sequences as prescribed by the rules given here.

### 3.3.1 Numeric Instructions

\(t\text{-}const\ c\)

- The instruction is valid with type `[] \to [t]`.

\[
C \vdash t\text{-}const\ c : [] \to [t]
\]
t.unop

- The instruction is valid with type \([t] \rightarrow [t]\).

\[ C \vdash t.\text{unop} : [t] \rightarrow [t] \]

t.binop

- The instruction is valid with type \([t \, t] \rightarrow [t]\).

\[ C \vdash t.\text{binop} : [t \, t] \rightarrow [t] \]

t.testop

- The instruction is valid with type \([t] \rightarrow [i32]\).

\[ C \vdash t.\text{testop} : [t] \rightarrow [i32] \]

t.relop

- The instruction is valid with type \([t \, t] \rightarrow [i32]\).

\[ C \vdash t.\text{relop} : [t \, t] \rightarrow [i32] \]

t2.cvtop_t1_sx7

- The instruction is valid with type \([t_1] \rightarrow [t_2]\).

\[ C \vdash t_2.\text{cvtop}_7_{t1} : [t_1] \rightarrow [t_2] \]

### 3.3.2 Reference Instructions

ref.null \( t \)

- The instruction is valid with type \([\ ] \rightarrow [t]\).

\[ C \vdash \text{ref.null } t : [\ ] \rightarrow [t] \]

**Note:** In future versions of WebAssembly, there may be reference types for which no null reference is allowed.

ref.is_null

- The instruction is valid with type \([t] \rightarrow [i32]\), for any reference type \( t \).

\[ t = \text{reftype} \]

\[ C \vdash \text{ref.is_null} : [t] \rightarrow [i32] \]
ref.func \( x \)

- The function \( C.funcs[x] \) must be defined in the context.
- The function index \( x \) must be contained in \( C.refs \).
- The instruction is valid with type \([\cdot] \rightarrow [\text{funcref}]\).

\[
\frac{C.funcs[x] = \text{functype} \quad x \in C.refs}{C \vdash \text{ref.func } x : [\cdot] \rightarrow [\text{funcref}]}
\]

### 3.3.3 Vector Instructions

Vector instructions can have a prefix to describe the shape of the operand. Packed numeric types, \( \text{i8} \) and \( \text{i16} \), are not value type, we define an auxiliary function to map such packed types into value types:

- \( \text{unpacked(i8\times16)} = \text{i32} \)
- \( \text{unpacked(i16\times8)} = \text{i32} \)
- \( \text{unpacked(t\times N)} = t \)

We also define an auxiliary function to get number of packed numeric types in a \( \text{v128}, \) dimension:

\[
\text{dim(t\times N)} = N
\]

\( \text{v128.const } c \)

- The instruction is valid with type \([\cdot] \rightarrow [\text{v128}]\).

\[
C \vdash \text{v128.const } c : [\cdot] \rightarrow [\text{v128}]
\]

\( \text{v128.vvunop} \)

- The instruction is valid with type \([\text{v128}] \rightarrow [\text{v128}]\).

\[
C \vdash \text{v128.vvunop} : [\text{v128}] \rightarrow [\text{v128}]
\]

\( \text{v128.vvbinop} \)

- The instruction is valid with type \([\text{v128 v128}] \rightarrow [\text{v128}]\).

\[
C \vdash \text{v128.vvbinop} : [\text{v128 v128}] \rightarrow [\text{v128}]
\]

\( \text{v128.vvternop} \)

- The instruction is valid with type \([\text{v128 v128 v128}] \rightarrow [\text{v128}]\).

\[
C \vdash \text{v128.vvternop} : [\text{v128 v128 v128}] \rightarrow [\text{v128}]
\]
v128.vvtestop

- The instruction is valid with type \([v128] \rightarrow [i32]\).

\[ C \vdash v128.vvtestop : [v128] \rightarrow [i32] \]

i8x16.swizzle

- The instruction is valid with type \([v128 v128] \rightarrow [v128]\).

\[ C \vdash i8x16.swizzle : [v128 v128] \rightarrow [v128] \]

i8x16.shuffle laneidx^{16}

- For all \(laneidx_i\), in \(laneidx^{16}\), \(laneidx_i\) must be smaller than 32.
- The instruction is valid with type \([v128 v128] \rightarrow [v128]\).

\[(laneidx < 32)^{16} \]

\[ C \vdash i8x16.shuffle laneidx^{16} : [v128 v128] \rightarrow [v128] \]

shape.splat

- Let \(t\) be \(\text{unpacked}(shape)\).
- The instruction is valid with type \([t] \rightarrow [v128]\).

\[ C \vdash shape.splat : [\text{unpacked}(shape)] \rightarrow [v128] \]

shape.extract_lane_{sx}^{7} laneidx

- The lane index \(laneidx\) must be smaller than \(\text{dim}(shape)\).
- The instruction is valid with type \([v128] \rightarrow [\text{unpacked}(shape)]\).

\[ laneidx < \text{dim}(shape) \]

\[ C \vdash txN.extract_lane_{sx}^{7} laneidx : [v128] \rightarrow [\text{unpacked}(shape)] \]

shape.replace_lane laneidx

- The lane index \(laneidx\) must be smaller than \(\text{dim}(shape)\).
- Let \(t\) be \(\text{unpacked}(shape)\).
- The instruction is valid with type \([v128 t] \rightarrow [v128]\).

\[ laneidx < \text{dim}(shape) \]

\[ C \vdash shape.replace_lane laneidx : [v128 \text{unpacked}(shape)] \rightarrow [v128] \]
shape.vunop

- The instruction is valid with type \([v128] \rightarrow [v128]\).

\[ C \vdash \text{shape.vunop} : [v128] \rightarrow [v128] \]

shape.ebinop

- The instruction is valid with type \([v128 \ v128] \rightarrow [v128]\).

\[ C \vdash \text{shape.ebinop} : [v128 \ v128] \rightarrow [v128] \]

shape.vrelop

- The instruction is valid with type \([v128 \ v128] \rightarrow [v128]\).

\[ C \vdash \text{shape.vrelop} : [v128 \ v128] \rightarrow [v128] \]

ishape.vishiftop

- The instruction is valid with type \([v128 \ i32] \rightarrow [v128]\).

\[ C \vdash \text{ishape.vishiftop} : [v128 \ i32] \rightarrow [v128] \]

shape.veltostop

- The instruction is valid with type \([v128] \rightarrow [i32]\).

\[ C \vdash \text{shape.veltostop} : [v128] \rightarrow [i32] \]

shape.vcvtop_half?_shape_sx?_zero?

- The instruction is valid with type \([v128] \rightarrow [v128]\).

\[ C \vdash \text{shape.vcvtop_half?_shape_sx?_zero?} : [v128] \rightarrow [v128] \]

ishape1.narrow_ishape2_sx

- The instruction is valid with type \([v128 \ v128] \rightarrow [v128]\).

\[ C \vdash \text{ishape1.narrow_ishape2_sx} : [v128 \ v128] \rightarrow [v128] \]

ishape.bitmask

- The instruction is valid with type \([v128] \rightarrow [i32]\).

\[ C \vdash \text{ishape.bitmask} : [v128] \rightarrow [i32] \]
ishape_1.dot_ishape_2_s

- The instruction is valid with type \([v128 \ v128] \rightarrow [v128]\).

\[ C \vdash ishape_1.dot_ishape_2_s : [v128 \ v128] \rightarrow [v128] \]

ishape_1.extmul_half_ishape_2_sx

- The instruction is valid with type \([v128 \ v128] \rightarrow [v128]\).

\[ C \vdash ishape_1.extmul_half_ishape_2_sx : [v128 \ v128] \rightarrow [v128] \]

ishape_1.extadd_pairwise_ishape_2_sx

- The instruction is valid with type \([v128] \rightarrow [v128]\).

\[ C \vdash ishape_1.extadd_pairwise_ishape_2_sx : [v128] \rightarrow [v128] \]

### 3.3.4 Parametric Instructions

**drop**

- The instruction is valid with type \([t] \rightarrow []\), for any value type \(t\).

\[ C \vdash drop : [t] \rightarrow [] \]

**Note:** Both drop and select without annotation are value-polymorphic instructions.

**select** \((t^*)\)

- If \(t^*\) is present, then:
  - The length of \(t^*\) must be 1.
  - Then the instruction is valid with type \([t^* \ t^* \ i32] \rightarrow [t^*]\).

- Else:
  - The instruction is valid with type \([t \ t \ i32] \rightarrow [t]\), for any operand type \(t\) that matches some number type or vector type.

\[ C \vdash select t : [t \ t \ i32] \rightarrow [t] \quad \vdash t \leq \text{numtype} \quad \vdash t \leq \text{vectype} \]

**Note:** In future versions of WebAssembly, select may allow more than one value per choice.
3.3.5 Variable Instructions

local.get $x$

- The local $C.\text{locals}[x]$ must be defined in the context.
- Let $t$ be the value type $C.\text{locals}[x]$.
- Then the instruction is valid with type $[] \rightarrow [t]$.

$$\frac{C.\text{locals}[x] = t}{C \vdash \text{local.get } x : [] \rightarrow [t]}$$

local.set $x$

- The local $C.\text{locals}[x]$ must be defined in the context.
- Let $t$ be the value type $C.\text{locals}[x]$.
- Then the instruction is valid with type $[t] \rightarrow []$.

$$\frac{C.\text{locals}[x] = t}{C \vdash \text{local.set } x : [t] \rightarrow []}$$

local.tee $x$

- The local $C.\text{locals}[x]$ must be defined in the context.
- Let $t$ be the value type $C.\text{locals}[x]$.
- Then the instruction is valid with type $[t] \rightarrow [t]$.

$$\frac{C.\text{locals}[x] = t}{C \vdash \text{local.tee } x : [t] \rightarrow [t]}$$

global.get $x$

- The global $C.\text{globals}[x]$ must be defined in the context.
- Let $\text{mut } t$ be the global type $C.\text{globals}[x]$.
- Then the instruction is valid with type $[] \rightarrow [t]$.

$$\frac{C.\text{globals}[x] = \text{mut } t}{C \vdash \text{global.get } x : [] \rightarrow [t]}$$

global.set $x$

- The global $C.\text{globals}[x]$ must be defined in the context.
- Let $\text{mut } t$ be the global type $C.\text{globals}[x]$.
- The mutability $\text{mut}$ must be $\text{var}$.
- Then the instruction is valid with type $[t] \rightarrow []$.

$$\frac{C.\text{globals}[x] = \text{var } t}{C \vdash \text{global.set } x : [t] \rightarrow []}$$
3.3.6 Table Instructions

table.get x

• The table C.tables[x] must be defined in the context.
• Let limits t be the table type C.tables[x].
• Then the instruction is valid with type [i32] → [t].

\[
C \vdash \text{table.get } x : [i32] \rightarrow [t]
\]

table.set x

• The table C.tables[x] must be defined in the context.
• Let limits t be the table type C.tables[x].
• Then the instruction is valid with type [i32 t] → [].

\[
C \vdash \text{table.set } x : [i32 t] \rightarrow []
\]

table.size x

• The table C.tables[x] must be defined in the context.
• Then the instruction is valid with type [] → [i32].

\[
C \vdash \text{table.size } x : [] \rightarrow [i32]
\]

table.grow x

• The table C.tables[x] must be defined in the context.
• Let limits t be the table type C.tables[x].
• Then the instruction is valid with type [t i32] → [i32].

\[
C \vdash \text{table.grow } x : [t i32] \rightarrow [i32]
\]

table.fill x

• The table C.tables[x] must be defined in the context.
• Let limits t be the table type C.tables[x].
• Then the instruction is valid with type [i32 t i32] → [].

\[
C \vdash \text{table.fill } x : [i32 t i32] \rightarrow []
\]
table.copy \( x \ y \)

- The table \( C \cdot \text{tables}[x] \) must be defined in the context.
- Let \( \text{limits}_1 \ t_1 \) be the \text{table type} \( C \cdot \text{tables}[x] \).
- The table \( C \cdot \text{tables}[y] \) must be defined in the context.
- Let \( \text{limits}_2 \ t_2 \) be the \text{table type} \( C \cdot \text{tables}[y] \).
- The \text{reference type} \( t_1 \) must be the same as \( t_2 \).
- Then the instruction is valid with type \([i32\ i32\ i32] \rightarrow []\).

\[
C \cdot \text{tables}[x] = \text{limits}_1 \ t_1 \quad C \cdot \text{tables}[x] = \text{limits}_2 \ t_2
\]

\[
C \vdash \text{table.copy} \ x \ y : [i32\ i32\ i32] \rightarrow []
\]

table.init \( x \ y \)

- The table \( C \cdot \text{tables}[x] \) must be defined in the context.
- Let \( \text{limits} \ t_1 \) be the \text{table type} \( C \cdot \text{tables}[x] \).
- The element segment \( C \cdot \text{elems}[y] \) must be defined in the context.
- Let \( t_2 \) be the \text{reference type} \( C \cdot \text{elems}[y] \).
- The \text{reference type} \( t_1 \) must be the same as \( t_2 \).
- Then the instruction is valid with type \([i32\ i32\ i32] \rightarrow []\).

\[
C \cdot \text{tables}[x] = \text{limits}_1 \ t_1 \quad C \cdot \text{elems}[y] = t_2
\]

\[
C \vdash \text{table.init} \ x \ y : [i32\ i32\ i32] \rightarrow []
\]

elem.drop \( x \)

- The element segment \( C \cdot \text{elems}[x] \) must be defined in the context.
- Then the instruction is valid with type \([],\rightarrow []\).

\[
C \cdot \text{elems}[x] = t
\]

\[
C \vdash \text{elem.drop} \ x : [], \rightarrow []
\]

### 3.3.7 Memory Instructions

\( t \cdot \text{load} \ memory \)

- The memory \( C \cdot \text{mems}[0] \) must be defined in the context.
- The alignment \( 2^{ \text{mem.type} \cdot \text{align}} \) must not be larger than the \text{bit width} of \( t \) divided by 8.
- Then the instruction is valid with type \([i32] \rightarrow [t]\).

\[
C \cdot \text{mems}[0] = \text{mem.type} \quad 2^{ \text{mem.type} \cdot \text{align}} \leq |t|/8
\]

\[
C \vdash t \cdot \text{load} \ memory : [i32] \rightarrow [t]
\]
### t.loadN\_sx memory

- The memory `C.mems[0]` must be defined in the context.
- The alignment `2^{\text{memory.align}}` must not be larger than \( N/8 \).
- Then the instruction is valid with type `[i32] \rightarrow [t]`.

\[
\frac{C.mems[0] = \text{memtype}}{2^{\text{memory.align}} \leq N/8}
\]

### t.store memory

- The memory `C.mems[0]` must be defined in the context.
- The alignment `2^{\text{memory.align}}` must not be larger than the \textit{bit width} of \( t \) divided by 8.
- Then the instruction is valid with type `[i32] t \rightarrow []`.

\[
\frac{C.mems[0] = \text{memtype}}{2^{\text{memory.align}} \leq \lvert t \rvert/8}
\]

### t.storeN memory

- The memory `C.mems[0]` must be defined in the context.
- The alignment `2^{\text{memory.align}}` must not be larger than \( N/8 \).
- Then the instruction is valid with type `[i32] t \rightarrow []`.

\[
\frac{C.mems[0] = \text{memtype}}{2^{\text{memory.align}} \leq N/8}
\]

### v128.loadN\_sx memory

- The memory `C.mems[0]` must be defined in the context.
- The alignment `2^{\text{memory.align}}` must not be larger than \( N/8 \cdot M \).
- Then the instruction is valid with type `[i32] \rightarrow [v128]`.

\[
\frac{C.mems[0] = \text{memtype}}{2^{\text{memory.align}} \leq N/8 \cdot M}
\]

### v128.loadN\_splat memory

- The memory `C.mems[0]` must be defined in the context.
- The alignment `2^{\text{memory.align}}` must not be larger than \( N/8 \).
- Then the instruction is valid with type `[i32] \rightarrow [v128]`.

\[
\frac{C.mems[0] = \text{memtype}}{2^{\text{memory.align}} \leq N/8}
\]
v128.load.N_zero memarg

- The memory $C\.mems[0]$ must be defined in the context.
- The alignment $2^{\text{memarg.align}}$ must not be larger than $N/8$.
- Then the instruction is valid with type $\text{[i32} \rightarrow \text{v128]}$.

$$C\.mems[0] = \text{memtype} \quad 2^{\text{memarg.align}} \leq \frac{N}{8}$$

$$C \vdash \text{v128.load.N_zero memarg} : \text{[i32} \rightarrow \text{v128]}$$

v128.load.N_lane memory laneidx

- The lane index $\text{laneidx}$ must be smaller than $128/N$.
- The memory $C\.mems[0]$ must be defined in the context.
- The alignment $2^{\text{memory.align}}$ must not be larger than $N/8$.
- Then the instruction is valid with type $\text{[i32 v128} \rightarrow \text{v128]}$.

$$\text{laneidx} < \frac{128}{N} \quad C\.mems[0] = \text{memtype} \quad 2^{\text{memory.align}} < \frac{N}{8}$$

$$C \vdash \text{v128.load.N_lane memory laneidx} : \text{[i32 v128} \rightarrow \text{v128]}$$

v128.store.N_lane memory laneidx

- The lane index $\text{laneidx}$ must be smaller than $128/N$.
- The memory $C\.mems[0]$ must be defined in the context.
- The alignment $2^{\text{memory.align}}$ must not be larger than $N/8$.
- Then the instruction is valid with type $\text{[i32 v128} \rightarrow \text{v128]}$.

$$\text{laneidx} < \frac{128}{N} \quad C\.mems[0] = \text{memtype} \quad 2^{\text{memory.align}} < \frac{N}{8}$$

$$C \vdash \text{v128.store.N_lane memory laneidx} : \text{[i32 v128} \rightarrow \text{v128]}$$

memory.size

- The memory $C\.mems[0]$ must be defined in the context.
- Then the instruction is valid with type $\text{[]} \rightarrow \text{[i32]}$.

$$C\.mems[0] = \text{memtype}$$

$$C \vdash \text{memory.size} : \text{[]} \rightarrow \text{[i32]}$$

memory.grow

- The memory $C\.mems[0]$ must be defined in the context.
- Then the instruction is valid with type $\text{[i32} \rightarrow \text{[i32]}$.

$$C\.mems[0] = \text{memtype}$$

$$C \vdash \text{memory.grow} : \text{[i32} \rightarrow \text{[i32]}$$
memory.fill

- The memory $C.mems[0]$ must be defined in the context.
- Then the instruction is valid with type $[i32 i32 i32] \to []$.

$$C.mems[0] = \text{memtype} \quad \frac{}{C \vdash \text{memory.fill} : [i32 i32 i32] \to []}$$

memory.copy

- The memory $C.mems[0]$ must be defined in the context.
- Then the instruction is valid with type $[i32 i32 i32] \to []$.

$$C.mems[0] = \text{memtype} \quad \frac{}{C \vdash \text{memory.copy} : [i32 i32 i32] \to []}$$

memory.init $x$

- The memory $C.mems[0]$ must be defined in the context.
- The data segment $C.datas[x]$ must be defined in the context.
- Then the instruction is valid with type $[i32 i32 i32] \to []$.

$$C.mems[0] = \text{memtype} \quad C.datas[x] = \text{ok} \quad \frac{}{C \vdash \text{memory.init } x : [i32 i32 i32] \to []}$$

data.drop $x$

- The data segment $C.datas[x]$ must be defined in the context.
- Then the instruction is valid with type $[] \to []$.

$$C.datas[x] = \text{ok} \quad \frac{}{C \vdash \text{data.drop } x : [] \to []}$$

### 3.3.8 Control Instructions

nop

- The instruction is valid with type $[] \to []$.

$$\frac{}{C \vdash \text{nop} : [] \to []}$$

unreachable

- The instruction is valid with type $[t^1_1] \to [t^2_2]$, for any sequences of value types $t^1_1$ and $t^2_2$.

$$\frac{}{C \vdash \text{unreachable} : [t^1_1] \to [t^2_2]}$$

**Note:** The unreachable instruction is stack-polymorphic.
The block type must be valid as some function type \([t_1^*] \rightarrow [t_2^*]\).

Let \(C'\) be the same context as \(C\), but with the result type \([t_2^*]\) prepended to the labels vector.

Under context \(C'\), the instruction sequence \(instr^*\) must be valid with type \([t_1^*] \rightarrow [t_2^*]\).

Then the compound instruction is valid with type \([t_1] \rightarrow [t_2]\).

\[
\begin{align*}
\frac{C \vdash \text{blocktype} : [t_1^*] \rightarrow [t_2^*] \quad C, \text{labels} [t_2^*] \vdash instr^* : [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{block type instr^* end} : [t_1] \rightarrow [t_2]} \end{align*}
\]

**Note:** The notation \(C, \text{labels} [t^*]\) inserts the new label type at index 0, shifting all others.

The block type must be valid as some function type \([t_1^*] \rightarrow [t_2^*]\).

Let \(C'\) be the same context as \(C\), but with the result type \([t_1^*]\) prepended to the labels vector.

Under context \(C'\), the instruction sequence \(instr^*\) must be valid with type \([t_1^*] \rightarrow [t_2^*]\).

Then the compound instruction is valid with type \([t_1] \rightarrow [t_2]\).

\[
\begin{align*}
\frac{C \vdash \text{blocktype} : [t_1^*] \rightarrow [t_2^*] \quad C, \text{labels} [t_1^*] \vdash instr^* : [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{loop blocktype instr^* end} : [t_1] \rightarrow [t_2]} \end{align*}
\]

**Note:** The notation \(C, \text{labels} [t^*]\) inserts the new label type at index 0, shifting all others.

The block type \(instr_1^*\) must be valid as some function type \([t_1^*] \rightarrow [t_2^*]\).

Let \(C'\) be the same context as \(C\), but with the result type \([t_1^*] \rightarrow [t_2^*]\) prepended to the labels vector.

Under context \(C'\), the instruction sequence \(instr_1^*\) must be valid with type \([t_1^*] \rightarrow [t_2^*]\).

Under context \(C'\), the instruction sequence \(instr_2^*\) must be valid with type \([t_1^*] \rightarrow [t_2^*]\).

Then the compound instruction is valid with type \([t_1^*] \rightarrow [t_2^*]\).

\[
\begin{align*}
\frac{C \vdash \text{blocktype} : [t_1^*] \rightarrow [t_2^*] \quad C, \text{labels} [t_2^*] \vdash instr_1^* : [t_1^*] \rightarrow [t_2^*] \quad C, \text{labels} [t_1^*] \vdash instr_2^* : [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{if blocktype instr_1^* else instr_2^* end} : [t_1^*] \rightarrow [t_2^*]} \end{align*}
\]

**Note:** The notation \(C, \text{labels} [t^*]\) inserts the new label type at index 0, shifting all others.

The label \(C, \text{labels}[l]\) must be defined in the context.

Let \([l^*]\) be the result type \(C, \text{labels}[l]\).

Then the instruction is valid with type \([t_1^* l^*] \rightarrow [t_2^*]\), for any sequences of value types \(t_1^*\) and \(t_2^*\).
\[
C.\text{labels}[l] = [t^*] \\
C \vdash \text{br } l : [t^*_1 \ t^*] \rightarrow [t^*_2] 
\]

**Note:** The *label index* space in the *context* \(C\) contains the most recent label first, so that \(C.\text{labels}[l]\) performs a relative lookup as expected.

The br instruction is *stack-polymorphic*.

**br_if \(l\)**

- The label \(C.\text{labels}[l]\) must be defined in the context.
- Let \([t^*]\) be the *result type* \(C.\text{labels}[l]\).
- Then the instruction is valid with type \([t^* \rightarrow i32] \rightarrow [t^*]\).

\[
C.\text{labels}[l] = [t^*] \\
C \vdash \text{br_if } l : [t^* \rightarrow i32] \rightarrow [t^*] 
\]

**Note:** The *label index* space in the *context* \(C\) contains the most recent label first, so that \(C.\text{labels}[l]\) performs a relative lookup as expected.

The br_table instruction is *stack-polymorphic*.

**br_table \(l^* \ l_N\)**

- The label \(C.\text{labels}[l_N]\) must be defined in the context.
- For all \(l_i\) in \(l^*\), the label \(C.\text{labels}[l_i]\) must be defined in the context.
- There must be a *result type* \([t^*]\), such that:
  - For each *operand type* \(t_j\) in \(t^*\) and corresponding type \(t'_{Nj}\) in \(C.\text{labels}[l_N]\), \(t_j\) matches \(t'_{Nj}\).
  - For all \(l_i\) in \(l^*\), and for each *operand type* \(t_j\) in \(t^*\) and corresponding type \(t'_{ij}\) in \(C.\text{labels}[l_i]\), \(t_j\) matches \(t'_{ij}\).
- Then the instruction is valid with type \([t^*_1 \ t^*_i \rightarrow i32] \rightarrow [t^*_2]\), for any sequences of *value types* \(t^*_1\) and \(t^*_2\).

\[
C \vdash \text{br_table } l^* \ l_N : [t^*_1 \ t^*_i \rightarrow i32] \rightarrow [t^*_2] 
\]

**Note:** The *label index* space in the *context* \(C\) contains the most recent label first, so that \(C.\text{labels}[l_i]\) performs a relative lookup as expected.

The br_table instruction is *stack-polymorphic*.

**return**

- The return type \(C.\text{return}\) must not be absent in the context.
- Let \([t^*]\) be the *result type* of \(C.\text{return}\).
- Then the instruction is valid with type \([t^*_1 \ t^*] \rightarrow [t^*_2]\), for any sequences of *value types* \(t^*_1\) and \(t^*_2\).

\[
C.\text{return} = [t^*] \\
C \vdash \text{return} : [t^*_1 \ t^*] \rightarrow [t^*_2] 
\]

**Note:** The return instruction is *stack-polymorphic*.

### 3.3. Instructions
Call $x$

- The function $C.funcs[x]$ must be defined in the context.
- Then the instruction is valid with type $C.funcs[x]$.
\[
C.funcs[x] = [t^*_1] \rightarrow [t^*_2] \quad C \vdash \text{call } x : [t^*_1] \rightarrow [t^*_2]
\]

call\_indirect $x$ $y$

- The table $C.tables[x]$ must be defined in the context.
- Let $limits$ be the table type $C.tables[x]$.
- The reference type $t$ must be $\text{funcref}$.
- The type $C.types[y]$ must be defined in the context.
- Let $[t^*_1] \rightarrow [t^*_2]$ be the function type $C.types[y]$.
- Then the instruction is valid with type $[t^*_1 \text{i32}] \rightarrow [t^*_2]$.
\[
C.tables[x] = \text{limits} \quad \text{funcref} \quad C.types[y] = [t^*_1] \rightarrow [t^*_2] \\
C \vdash \text{call\_indirect } x \ y : [t^*_1 \text{i32}] \rightarrow [t^*_2]
\]

3.3.9 Instruction Sequences

Typing of instruction sequences is defined recursively.

Empty Instruction Sequence: $\epsilon$

- The empty instruction sequence is valid with type $[t^*] \rightarrow [t^*]$, for any sequence of operand types $t^*$.
\[
C \vdash \epsilon : [t^*] \rightarrow [t^*]
\]

Non-empty Instruction Sequence: $\text{instr}^* \text{ instr}_N$

- The instruction sequence $\text{instr}^*$ must be valid with type $[t^*_1] \rightarrow [t^*_2]$, for some sequences of value types $t^*_1$ and $t^*_2$.
- The instruction $\text{instr}_N$ must be valid with type $[t^*] \rightarrow [t^*_3]$, for some sequences of value types $t^*$ and $t^*_3$.
- There must be a sequence of value types $t^*_0$, such that $t^*_2 = t^*_0 \ t^*_0$ where the type sequence $t^*_0$ is as long as $t^*$.
- For each operand type $t^*_i$ in $t^*_0$ and corresponding type $t_i$ in $t^*$, $t^*_i$ matches $t_i$.
- Then the combined instruction sequence is valid with type $[t^*_1] \rightarrow [t^*_0 \ t^*_0]$.
\[
C \vdash \text{instr}^* : [t^*_1] \rightarrow [t^*_0 \ t^*_0] \quad C \vdash [t^*_0 \ t^*_0] \leq [t^*] \quad C \vdash \text{instr}_N : [t^*] \rightarrow [t^*_3] \\
C \vdash \text{instr}^* \text{ instr}_N : [t^*_1] \rightarrow [t^*_0 \ t^*_0]
\]
3.3.10 Expressions

Expressions `expr` are classified by `result types` of the form `[t*]`.

`instr* end`

- The instruction sequence `instr*` must be `valid` with some `stack type` `[] → [t'/*]`.
- For each `operand type` `t'_i` in `t'/*` and corresponding `value type` `t_i` in `t*`, `t'_i` matches `t_i`.
- Then the expression is valid with `result type` `[t*]`.

\[
\frac{\mathcal{C} \vdash instr^* : [] \rightarrow [t'/*]}{\mathcal{C} \vdash instr^* \text{ end} : [t*]} \quad [t'/*] \leq [t*]
\]

Constant Expressions

- In a `constant` expression `instr* end` all instructions in `instr*` must be constant.
- A constant instruction `instr` must be:
  - either of the form `t.const c`,
  - or of the form `ref.null`,
  - or of the form `ref.func x`,
  - or of the form `global.get x`, in which case `C.globals[x]` must be a `global type` of the form `const t`.

\[
(C \vdash instr \text{ const})^* \quad \frac{\mathcal{C} \vdash instr \text{ const}^*}{\mathcal{C} \vdash instr \text{ const} \text{ end const}} \\
\frac{\mathcal{C} \vdash t.\text{const} c \text{ const}}{C.globals[x] = \text{const } t} \\
\frac{\mathcal{C} \vdash ref.null \text{ const}}{C.globals[x] = \text{const } t} \\
\frac{\mathcal{C} \vdash ref.func x \text{ const}}{C.globals[x] = \text{const } t} \\
\]

**Note:** Currently, constant expressions occurring as initializers of `globals` are further constrained in that contained `global.get` instructions are only allowed to refer to `imported` `globals`. This is enforced in the `validation rule for modules` by constraining the context `C` accordingly.

The definition of constant expression may be extended in future versions of WebAssembly.

3.4 Modules

Modules are valid when all the components they contain are valid. Furthermore, most definitions are themselves classified with a suitable type.
3.4.1 Functions

Functions `func` are classified by `function types` of the form `[t^*_1] → [t^*_2]`.

`{ type x, locals t^*, body expr }`

- The type `C.types[x]` must be defined in the context.
- Let `[t^*_1] → [t^*_2]` be the function type `C.types[x]`.
- Let `C'` be the same `context` as `C`, but with:
  - `locals` set to the sequence of `value types` `t^*_1 t^*`, concatenating parameters and locals,
  - `labels` set to the singular sequence containing only `result type` `[t^*_2]`.
  - `return` set to the `result type` `[t^*_2]`.
- Under the context `C'`, the expression `expr` must be valid with type `[t^*_2]`.
- Then the function definition is valid with type `[t^*_1] → [t^*_2]`.

\[ C.types[x] = [t^*_1] → [t^*_2] \]
\[ C, locals t^*_1 t^*, labels [t^*_2], return [t^*_2] ⊢ expr : [t^*_2] \]
\[ C' ⊢ \{ type x, locals t^*, body expr \} : [t^*_1] → [t^*_2] \]

3.4.2 Tables

Tables `table` are classified by `table types`.

`{ type tabletype }`

- The `table type` `tabletype` must be `valid`.
- Then the table definition is valid with type `tabletype`.

\[ ⊢ tabletype \text{ ok} \]
\[ C ⊢ \{ type tabletype \} : tabletype \]

3.4.3 Memories

Memories `mem` are classified by `memory types`.

`{ type memtype }`

- The `memory type` `memtype` must be `valid`.
- Then the memory definition is valid with type `memtype`.

\[ ⊢ memtype \text{ ok} \]
\[ C ⊢ \{ type memtype \} : memtype \]
3.4.4 Globals

Globals global are classified by global types of the form mut t.

\{\text{type mut t, init expr}\}

- The global type mut t must be valid.
- The expression expr must be valid with result type \([t]\).
- The expression expr must be constant.
- Then the global definition is valid with type mut t.

\[\frac{\vdash \text{mut t ok}}{\vdash \text{mut t : mut t}}\]

3.4.5 Element Segments

Element segments elem are classified by the reference type of their elements.

\{\text{type t, init e*, mode elemmode}\}

- For each \(e_i\) in \(e^*\),
  - The expression \(e_i\) must be valid.
  - The expression \(e_i\) must be constant.
- The element mode elemmode must be valid with reference type t.
- Then the element segment is valid with reference type t.

\[\frac{(C \vdash e \text{ ok})^*}{C \vdash \{\text{type t, init e*, mode elemmode}\} : t}\]

passive

- The element mode is valid with any reference type.

\[C \vdash \text{passive : reftype}\]

active \{table x, offset expr\}

- The table C.tables[x] must be defined in the context.
- Let limits t be the table type C.tables[x].
- The expression expr must be valid with result type \([i32]\).
- The expression expr must be constant.
- Then the element mode is valid with reference type t.

\[C \vdash \text{active \{table x, offset expr\} : t}\]
declarative  

- The element mode is valid with any \textit{reference type}.

\[
C \vdash \text{declarative} : \text{reftype}
\]

### 3.4.6 Data Segments

Data segments \textit{data} are not classified by any type but merely checked for well-formedness.

\{\text{init } b^*, \text{mode } \text{datamode}\}  

- The data mode \textit{datamode} must be valid.
- Then the data segment is valid.

\[
C \vdash \text{datamode} \text{ ok} \\
C \vdash \{\text{init } b^*, \text{mode } \text{datamode}\} \text{ ok}
\]

\textbf{passive}  

- The data mode is valid.

\[
C \vdash \text{passive ok}
\]

\textbf{active } \{\text{memory } x, \text{offset } expr\}  

- The memory $C.\text{mems}[x]$ must be defined in the context.
- The expression \textit{expr} must be \textit{valid} with \textit{result type} \[i32\].
- The expression \textit{expr} must be \textit{constant}.
- Then the data mode is valid.

\[
C.\text{mems}[x] = limits \\
C \vdash \text{expr} : [i32] \\
C \vdash \text{expr const} \\
C \vdash \text{active } \{\text{memory } x, \text{offset } expr\} \text{ ok}
\]

### 3.4.7 Start Function

Start function declarations \textit{start} are not classified by any type.

\{\text{func } x\}  

- The function $C.\text{funcs}[x]$ must be defined in the context.
- The type of $C.\text{funcs}[x]$ must be \[\] \[\rightarrow \] .
- Then the start function is valid.

\[
C.\text{funcs}[x] = [] \rightarrow [] \\
C \vdash \{\text{func } x\} \text{ ok}
\]
3.4.8 Exports

Exports `export` and export descriptions `exportdesc` are classified by their `external type`.

\{ name name, desc exportdesc \}

- The export description `exportdesc` must be valid with `external type externtype`.
- Then the export is valid with `external type externtype`.

\[ C \vdash exportdesc : externtype \]
\[ C \vdash \{ name name, desc exportdesc \} : externtype \]

`func x`

- The function `C.funcs[x]` must be defined in the context.
- Then the export description is valid with `external type func C.funcs[x]`.

\[ C.funcs[x] = functype \]
\[ C \vdash func x : func functype \]

`table x`

- The table `C.tables[x]` must be defined in the context.
- Then the export description is valid with `external type table C.tables[x]`.

\[ C.tables[x] = tabletype \]
\[ C \vdash table x : table tabletype \]

`mem x`

- The memory `C.mems[x]` must be defined in the context.
- Then the export description is valid with `external type mem C.mems[x]`.

\[ C.mems[x] = memtype \]
\[ C \vdash mem x : mem memtype \]

`global x`

- The global `C.globals[x]` must be defined in the context.
- Then the export description is valid with `external type global C.globals[x]`.

\[ C.globals[x] = globaltype \]
\[ C \vdash global x : global globaltype \]
3.4.9 Imports

Imports `import` and import descriptions `importdesc` are classified by `external types`.

```
{module name_1, name name_2, desc importdesc}
```

- The import description `importdesc` must be valid with type `externetype`.
- Then the import is valid with type `externetype`.

\[
C \vdash importdesc : externetype
\]

\[
C \vdash \{module name_1, name name_2, desc importdesc\} : externetype
\]

`func x`

- The function `C.types[x]` must be defined in the context.
- Let \([t_1^*] \rightarrow [t_2^*]\) be the function type `C.types[x]`.
- Then the import description is valid with type `func [t_1^*] \rightarrow [t_2^*]`.

\[
C \vdash func x : func [t_1^*] \rightarrow [t_2^*]
\]

`table tabletype`

- The table type `tabletype` must be `valid`.
- Then the import description is valid with type `table tabletype`.

\[
C \vdash tabletabletype : tabletabletype
\]

`mem memtype`

- The memory type `memtype` must be `valid`.
- Then the import description is valid with type `mem memtype`.

\[
C \vdash memmemtype : memmemtype
\]

`global globaltype`

- The global type `globaltype` must be `valid`.
- Then the import description is valid with type `global globaltype`.

\[
C \vdash globalglobaltype : globalglobaltype
\]
3.4.10 Modules

Modules are classified by their mapping from the external types of their imports to those of their exports.

A module is entirely closed, that is, its components can only refer to definitions that appear in the module itself. Consequently, no initial context is required. Instead, the context $C$ for validation of the module’s content is constructed from the definitions in the module.

- Let $\textit{module}$ be the module to validate.
- Let $C$ be a context where:
  - $C.\textit{types}$ is $\textit{module}.\textit{types}$,
  - $C.\textit{funcs}$ is $\textit{funcs}(it^*)$ concatenated with $ft^*$, with the import’s external types $it^*$ and the internal function types $ft^*$ as determined below,
  - $C.\textit{tables}$ is $\textit{tables}(it^*)$ concatenated with $tt^*$, with the import’s external types $it^*$ and the internal table types $tt^*$ as determined below,
  - $C.\textit{mems}$ is $\textit{mems}(it^*)$ concatenated with $mt^*$, with the import’s external types $it^*$ and the internal memory types $mt^*$ as determined below,
  - $C.\textit{globals}$ is $\textit{globals}(it^*)$ concatenated with $gt^*$, with the import’s external types $it^*$ and the internal global types $gt^*$ as determined below,
  - $C.\textit{elems}$ is $rt^*$ as determined below,
  - $C.\textit{datas}$ is $\textit{ok}^n$, where $n$ is the length of the vector $\textit{module}.\textit{datas}$,
  - $C.\textit{locals}$ is empty,
  - $C.\textit{labels}$ is empty,
  - $C.\textit{return}$ is empty,
  - $C.\textit{refs}$ is the set $\textit{funcidx}(\textit{module} \text{ with } \textit{funcs} = \epsilon \text{ with } \textit{start} = \epsilon)$, i.e., the set of function indices occurring in the module, except in its functions or start function.
- Let $C'$ be the context where:
  - $C'.\textit{globals}$ is the sequence $\textit{globals}(it^*)$,
  - $C'.\textit{funcs}$ is the same as $C.\textit{funcs}$,
  - $C'.\textit{refs}$ is the same as $C.\textit{refs}$,
  - all other fields are empty.
- Under the context $C$:
  - For each $\textit{functype}_i$ in $\textit{module}.\textit{types}$, the function type $\textit{functype}_i$ must be valid.
  - For each $\textit{func}_i$ in $\textit{module}.\textit{funcs}$, the definition $\textit{func}_i$ must be valid with a function type $ft_i$.
  - For each $\textit{table}_i$ in $\textit{module}.\textit{tables}$, the definition $\textit{table}_i$ must be valid with a table type $tt_i$.
  - For each $\textit{mem}_i$ in $\textit{module}.\textit{mems}$, the definition $\textit{mem}_i$ must be valid with a memory type $mt_i$.
  - For each $\textit{global}_i$ in $\textit{module}.\textit{globals}$:
    * Under the context $C'$, the definition $\textit{global}_i$ must be valid with a global type $gt_i$.
      - For each $\textit{elem}_i$ in $\textit{module}.\textit{elems}$, the segment $\textit{elem}_i$ must be valid with reference type $rt_i$.
      - For each $\textit{data}_i$ in $\textit{module}.\textit{datas}$, the segment $\textit{data}_i$ must be valid.
      - If $\textit{module}.\textit{start}$ is non-empty, then $\textit{module}.\textit{start}$ must be valid.
      - For each $\textit{import}_i$ in $\textit{module}.\textit{imports}$, the segment $\textit{import}_i$ must be valid with an external type $it_i$.
      - For each $\textit{export}_i$ in $\textit{module}.\textit{exports}$, the segment $\textit{export}_i$ must be valid with external type $et_i$.
- The length of $C.\textit{mems}$ must not be larger than 1.
• All export names `export_i.name` must be different.
• Let `ft` be the concatenation of the internal `function types` `ft_i`, in index order.
• Let `tt` be the concatenation of the internal `table types` `tt_i`, in index order.
• Let `mt` be the concatenation of the internal `memory types` `mt_i`, in index order.
• Let `gt` be the concatenation of the internal `global types` `gt_i`, in index order.
• Let `rt` be the concatenation of the internal `reference types` `rt_i`, in index order.
• Let `it` be the concatenation of the internal `external types` `it_i` of the imports, in index order.
• Let `et` be the concatenation of the internal `external types` `et_i` of the exports, in index order.

Then the module is valid with `external types` `it` → `et`.

\[ (\vdash \text{type ok})^* (C \vdash \text{func} : ft)^* (C \vdash \text{table} : tt)^* (C \vdash \text{mem} : mt)^* (C' \vdash \text{global} : gt)^* (C \vdash \text{elem} : rt)^* (C \vdash \text{data ok})^* (C \vdash \text{start ok})^* (C \vdash \text{import} : it)^* (C \vdash \text{export} : et)^* \]

\[ ift = \text{funcidx}(\text{module with func} = \epsilon \text{ with start} = \epsilon) \]

\[ itt = \text{tableidx}(\text{module with table} = \epsilon) \]

\[ imt = \text{memidx}(\text{module with mem} = \epsilon) \]

\[ igt = \text{globals}(\text{module with global} = \epsilon) \]

\[ x^* = \text{funcidx}(\text{module with func} = \epsilon \text{ with start} = \epsilon) \]

\[ C = \{ \text{types} type^*, \text{funcs} if^* ft^*, \text{tables} itt^* tt^*, \text{mems} imt^* mt^*, \text{globals} igt^* gt^*, \text{elems} rt^*, \text{datas} ok^*, \text{refs} x^* \} \]

\[ C' = \{ \text{globals} igt^*, \text{funcs} (C.\text{funcs}), \text{refs} (C.\text{refs}) \} \]

\[ \vdash \text{module} : \text{module with imports} = \epsilon \]

Note: Most definitions in a module – particularly functions – are mutually recursive. Consequently, the definition of the context `C` in this rule is recursive: it depends on the outcome of validation of the function, table, memory, and global definitions contained in the module, which itself depends on `C`. However, this recursion is just a specification device. All types needed to construct `C` can easily be determined from a simple pre-pass over the module that does not perform any actual validation.

Globals, however, are not recursive. The effect of defining the limited context `C'` for validating the module’s globals is that their initialization expressions can only access functions and imported globals and nothing else.

Note: The restriction on the number of memories may be lifted in future versions of WebAssembly.
4.1 Conventions

WebAssembly code is executed when instantiating a module or invoking an exported function on the resulting module instance.

Execution behavior is defined in terms of an abstract machine that models the program state. It includes a stack, which records operand values and control constructs, and an abstract store containing global state.

For each instruction, there is a rule that specifies the effect of its execution on the program state. Furthermore, there are rules describing the instantiation of a module. As with validation, all rules are given in two equivalent forms:

1. In prose, describing the execution in intuitive form.
2. In formal notation, describing the rule in mathematical form.\(^{17}\)

Note: As with validation, the prose and formal rules are equivalent, so that understanding of the formal notation is not required to read this specification. The formalism offers a more concise description in notation that is used widely in programming languages semantics and is readily amenable to mathematical proof.

4.1.1 Prose Notation

Execution is specified by stylised, step-wise rules for each instruction of the abstract syntax. The following conventions are adopted in stating these rules.

- The execution rules implicitly assume a given store \(S\).
- The execution rules also assume the presence of an implicit stack that is modified by pushing or popping values, labels, and frames.
- Certain rules require the stack to contain at least one frame. The most recent frame is referred to as the current frame.


\(^{18}\) https://dl.acm.org/citation.cfm?doid=3062341.3062363
Both the store and the current frame are mutated by replacing some of their components. Such replacement is assumed to apply globally.

The execution of an instruction may trap, in which case the entire computation is aborted and no further modifications to the store are performed by it. (Other computations can still be initiated afterwards.)

The execution of an instruction may also end in a jump to a designated target, which defines the next instruction to execute.

Execution can enter and exit instruction sequences that form blocks.

Instruction sequences are implicitly executed in order, unless a trap or jump occurs.

In various places the rules contain assertions expressing crucial invariants about the program state.

### 4.1.2 Formal Notation

**Note:** This section gives a brief explanation of the notation for specifying execution formally. For the interested reader, a more thorough introduction can be found in respective text books.\(^{19}\)

The formal execution rules use a standard approach for specifying operational semantics, rendering them into reduction rules. Every rule has the following general form:

\[
\text{configuration} \overset{}{\rightarrow} \text{configuration}
\]

A configuration is a syntactic description of a program state. Each rule specifies one step of execution. As long as there is at most one reduction rule applicable to a given configuration, reduction — and thereby execution — is deterministic. WebAssembly has only very few exceptions to this, which are noted explicitly in this specification.

For WebAssembly, a configuration typically is a tuple \((S; F; \text{instr}^*)\) consisting of the current store \(S\), the call frame \(F\) of the current function, and the sequence of instructions that is to be executed. (A more precise definition is given later.)

To avoid unnecessary clutter, the store \(S\) and the frame \(F\) are omitted from reduction rules that do not touch them.

There is no separate representation of the stack. Instead, it is conveniently represented as part of the configuration’s instruction sequence. In particular, values are defined to coincide with const instructions, and a sequence of const instructions can be interpreted as an operand “stack” that grows to the right.

**Note:** For example, the reduction rule for the \(\text{i32.add}\) instruction can be given as follows:

\[
(\text{i32.const } n_1) \ (\text{i32.const } n_2) \ \text{i32.add} \quad \overset{}{\rightarrow} \quad (\text{i32.const } (n_1 + n_2) \mod 2^{32})
\]

Per this rule, two const instructions and the add instruction itself are removed from the instruction stream and replaced with one new const instruction. This can be interpreted as popping two value off the stack and pushing the result.

When no result is produced, an instruction reduces to the empty sequence:

\[
\text{nop} \overset{}{\rightarrow} \epsilon
\]

Labels and frames are similarly defined to be part of an instruction sequence.

The order of reduction is determined by the definition of an appropriate evaluation context.

Reduction terminates when no more reduction rules are applicable. Soundness of the WebAssembly type system guarantees that this is only the case when the original instruction sequence has either been reduced to a sequence of const instructions, which can be interpreted as the values of the resulting operand stack, or if a trap occurred.

\(^{19}\) For example: Benjamin Pierce. *Types and Programming Languages*\(^20\). The MIT Press 2002

\(^{20}\) https://www.cis.upenn.edu/~bcpierce/tapl/
Note: For example, the following instruction sequence,

\[(f64.\text{const } x_1) \,(f64.\text{const } x_2) \,f64.\text{neg} \,(f64.\text{const } x_3) \,f64.\text{add} \,f64.\text{mul} \]

terminates after three steps:

\[(f64.\text{const } x_1) \,(f64.\text{const } x_2) \,f64.\text{neg} \,(f64.\text{const } x_3) \,f64.\text{add} \,f64.\text{mul} \]
\[\rightarrow (f64.\text{const } x_1) \,(f64.\text{const } x_4) \,(f64.\text{const } x_3) \,f64.\text{add} \,f64.\text{mul} \]
\[\rightarrow (f64.\text{const } x_1) \,(f64.\text{const } x_5) \,f64.\text{mul} \]
\[\rightarrow (f64.\text{const } x_6) \]

where \(x_4 = -x_2\) and \(x_5 = -x_2 + x_3\) and \(x_6 = x_1 \cdot (-x_2 + x_3)\).

### 4.2 Runtime Structure

*Store, stack, and other runtime structure* forming the WebAssembly abstract machine, such as *values* or *module instances*, are made precise in terms of additional auxiliary syntax.

#### 4.2.1 Values

WebAssembly computations manipulate *values* of either the four basic *number types*, i.e., *integers* and *floating-point data* of 32 or 64 bit width each, of *vectors* of 128 bit width, or of *reference type*.

In most places of the semantics, values of different types can occur. In order to avoid ambiguities, values are therefore represented with an abstract syntax that makes their type explicit. It is convenient to reuse the same notation as for the *const instructions* and *ref. null* producing them.

References other than *null* are represented with additional *administrative instructions*. They either are *function references*, pointing to a specific *function address*, or *external references* pointing to an uninterpreted form of *extern address* that can be defined by the *embedder* to represent its own objects.

\[
\text{num} ::= \ i32.\text{const } i32 \ |
\quad i64.\text{const } i64 \ |
\quad f32.\text{const } f32 \ |
\quad f64.\text{const } f64 \\
\text{vec} ::= \ v128.\text{const } i128 \\
\text{ref} ::= \ \text{ref. null } \ t \ |
\quad \text{ref. funcaddr} \\
\quad \text{ref. extern } \text{externaddr} \\
\text{val} ::= \ \text{num} \ | \ \text{vec} \ | \ \text{ref}
\]

Note: Future versions of WebAssembly may add additional forms of reference.

Each *value type* has an associated *default value*; it is the respective value 0 for *number types* and *null* for *reference types*.

\[
\text{default}_t = \ t.\text{const } 0 \quad \text{(if } t = \text{numtype}) \\
\text{default}_t = \ \text{ref. null } t \quad \text{(if } t = \text{reftype})
\]
Convention

• The meta variable $r$ ranges over reference values where clear from context.

4.2.2 Results

A result is the outcome of a computation. It is either a sequence of values or a trap.

\[
\text{result} ::= \text{val}* \mid \text{trap}
\]

Note: In the current version of WebAssembly, a result can consist of at most one value.

4.2.3 Store

The store represents all global state that can be manipulated by WebAssembly programs. It consists of the runtime representation of all instances of functions, tables, memories, and globals, element segments, and data segments that have been allocated during the life time of the abstract machine.\(^{21}\)

It is an invariant of the semantics that no element or data instance is addressed from anywhere else but the owning module instances.

Syntactically, the store is defined as a record listing the existing instances of each category:

\[
\text{store} ::= \{ \text{funcs funcinst*}, \\
\text{tables tableinst*}, \\
\text{mems meminst*}, \\
\text{globals globalinst*}, \\
\text{elems eleminst*}, \\
\text{datas datainst*} \}
\]

Convention

• The meta variable $S$ ranges over stores where clear from context.

4.2.4 Addresses

Function instances, table instances, memory instances, and global instances, element instances, and data instances in the store are referenced with abstract addresses. These are simply indices into the respective store component. In addition, an embedder may supply an uninterpreted set of host addresses.

\[
\text{addr} ::= 0 \mid 1 \mid 2 \mid \ldots \\
\text{funcaddr} ::= \text{addr} \\
\text{tableaddr} ::= \text{addr} \\
\text{memaddr} ::= \text{addr} \\
\text{globaladdr} ::= \text{addr} \\
\text{elemaddr} ::= \text{addr} \\
\text{dataaddr} ::= \text{addr} \\
\text{externaddr} ::= \text{addr}
\]

An embedder may assign identity to exported store objects corresponding to their addresses, even where this identity is not observable from within WebAssembly code itself (such as for function instances or immutable globals).

\(^{21}\) In practice, implementations may apply techniques like garbage collection to remove objects from the store that are no longer referenced. However, such techniques are not semantically observable, and hence outside the scope of this specification.
Note: Addresses are dynamic, globally unique references to runtime objects, in contrast to indices, which are static, module-local references to their original definitions. A memory address memaddr denotes the abstract address of a memory instance in the store, not an offset inside a memory instance.

There is no specific limit on the number of allocations of store objects, hence logical addresses can be arbitrarily large natural numbers.

4.2.5 Module Instances

A module instance is the runtime representation of a module. It is created by instantiating a module, and collects runtime representations of all entities that are imported, defined, or exported by the module.

\[
\text{moduleinst} ::= \{ \text{types functype}^*, \text{funcaddrs funcaddr}^*, \text{tableaddrs tableaddr}^*, \text{memaddrs memaddr}^*, \text{globaladdrs globaladdr}^*, \text{elemaddrs elemaddr}^*, \text{dataaddrs dataaddr}^*, \text{exports exportinst}^* \}
\]

Each component references runtime instances corresponding to respective declarations from the original module – whether imported or defined – in the order of their static indices. Function instances, table instances, memory instances, and global instances are referenced with an indirection through their respective addresses in the store.

It is an invariant of the semantics that all export instances in a given module instance have different names.

4.2.6 Function Instances

A function instance is the runtime representation of a function. It effectively is a closure of the original function over the runtime module instance of its originating module. The module instance is used to resolve references to other definitions during execution of the function.

\[
\text{funcinst} ::= \{ \text{type functype}, \text{module moduleinst}, \text{code func} \}
\]

A host function is a function expressed outside WebAssembly but passed to a module as an import. The definition and behavior of host functions are outside the scope of this specification. For the purpose of this specification, it is assumed that when invoked, a host function behaves non-deterministically, but within certain constraints that ensure the integrity of the runtime.

Note: Function instances are immutable, and their identity is not observable by WebAssembly code. However, the embedder might provide implicit or explicit means for distinguishing their addresses.

4.2.7 Table Instances

A table instance is the runtime representation of a table. It records its type and holds a vector of reference values.

\[
\text{tableinst} ::= \{ \text{type tabletype}, \text{elem vec(ref)} \}
\]

Table elements can be mutated through table instructions, the execution of an active element segment, or by external means provided by the embedder.

It is an invariant of the semantics that all table elements have a type equal to the element type of tabletype. It also is an invariant that the length of the element vector never exceeds the maximum size of tabletype, if present.

4.2. Runtime Structure
4.2.8 Memory Instances

A memory instance is the runtime representation of a linear memory. It records its type and holds a vector of bytes.

\[
\text{meminst} ::= \{ \text{type memtype, data vec(byte)} \}
\]

The length of the vector always is a multiple of the WebAssembly page size, which is defined to be the constant 65536 – abbreviated 64 Ki.

The bytes can be mutated through memory instructions, the execution of an active data segment, or by external means provided by the embedder.

It is an invariant of the semantics that the length of the byte vector, divided by page size, never exceeds the maximum size of memtype, if present.

4.2.9 Global Instances

A global instance is the runtime representation of a global variable. It records its type and holds an individual value.

\[
\text{globalinst} ::= \{ \text{type valtype, value val} \}
\]

The value of mutable globals can be mutated through variable instructions or by external means provided by the embedder.

It is an invariant of the semantics that the value has a type equal to the value type of globaltype.

4.2.10 Element Instances

An element instance is the runtime representation of an element segment. It holds a vector of references and their common type.

\[
\text{eleminst} ::= \{ \text{type reftype, elem vec(ref)} \}
\]

4.2.11 Data Instances

An data instance is the runtime representation of a data segment. It holds a vector of bytes.

\[
\text{datainst} ::= \{ \text{data vec(byte)} \}
\]

4.2.12 Export Instances

An export instance is the runtime representation of an export. It defines the export’s name and the associated external value.

\[
\text{exportinst} ::= \{ \text{name name, value externalval} \}
\]
4.2.13 External Values

An external value is the runtime representation of an entity that can be imported or exported. It is an address denoting either a function instance, table instance, memory instance, or global instances in the shared store.

\[
\text{externval} ::= \text{func funcaddr} \\
| \text{table tableaddr} \\
| \text{mem memaddr} \\
| \text{global globaladdr}
\]

Conventions

The following auxiliary notation is defined for sequences of external values. It filters out entries of a specific kind in an order-preserving fashion:

- \(\text{funcs(externval*)} = [\text{funcaddr} | (\text{func funcaddr}) \in \text{externval*}]\)
- \(\text{tables(externval*)} = [\text{tableaddr} | (\text{table tableaddr}) \in \text{externval*}]\)
- \(\text{mems(externval*)} = [\text{memaddr} | (\text{mem memaddr}) \in \text{externval*}]\)
- \(\text{globals(externval*)} = [\text{globaladdr} | (\text{global globaladdr}) \in \text{externval*}]\)

4.2.14 Stack

Besides the store, most instructions interact with an implicit stack. The stack contains three kinds of entries:

- **Values**: the operands of instructions.
- **Labels**: active structured control instructions that can be targeted by branches.
- **Activations**: the call frames of active function calls.

These entries can occur on the stack in any order during the execution of a program. Stack entries are described by abstract syntax as follows.

**Note**: It is possible to model the WebAssembly semantics using separate stacks for operands, control constructs, and calls. However, because the stacks are interdependent, additional bookkeeping about associated stack heights would be required. For the purpose of this specification, an interleaved representation is simpler.

**Values**

Values are represented by themselves.

**Labels**

Labels carry an argument arity \(n\) and their associated branch target, which is expressed syntactically as an instruction sequence:

\[
\text{label} ::= \text{label}_n\{\text{instr}^*\}
\]

Intuitively, \(\text{instr}^*\) is the continuation to execute when the branch is taken, in place of the original control construct.

**Note**: For example, a loop label has the form

\[
\text{label}_n\{\text{loop} \ldots \text{end}\}
\]
When performing a branch to this label, this executes the loop, effectively restarting it from the beginning. Conversely, a simple block label has the form

\[ \text{label}_n \{ \epsilon \} \]

When branching, the empty continuation ends the targeted block, such that execution can proceed with consecutive instructions.

**Activations and Frames**

Activations frames carry the return arity \( n \) of the respective function, hold the values of its locals (including arguments) in the order corresponding to their static local indices, and a reference to the function’s own module instance:

\[
\begin{align*}
\text{activation} & ::= \text{frame}_n \{ \text{frame} \} \\
\text{frame} & ::= \{ \text{locals val}^*, \text{module moduleinst} \}
\end{align*}
\]

The values of the locals are mutated by respective variable instructions.

**Conventions**

- The meta variable \( L \) ranges over labels where clear from context.
- The meta variable \( F \) ranges over frames where clear from context.
- The following auxiliary definition takes a block type and looks up the function type that it denotes in the current frame:

\[
\begin{align*}
\text{expand}_F(typeidx) & = F\text{.module.types}[typeidx] \\
\text{expand}_F([valtype?]) & = [] \rightarrow [valtype?] 
\end{align*}
\]

**4.2.15 Administrative Instructions**

**Note:** This section is only relevant for the formal notation.

In order to express the reduction of traps, calls, and control instructions, the syntax of instructions is extended to include the following administrative instructions:

\[
\begin{align*}
\text{instr} & ::= \ldots \\
& | \text{trap} \\
& | \text{ref funcaddr} \\
& | \text{ref.extern externaddr} \\
& | \text{invoke funcaddr} \\
& | \text{label}_n\{\text{instr}^*\} \text{ instr}^* \text{ end} \\
& | \text{frame}_n\{\text{frame}\} \text{ instr}^* \text{ end}
\end{align*}
\]

The trap instruction represents the occurrence of a trap. Traps are bubbled up through nested instruction sequences, ultimately reducing the entire program to a single trap instruction, signalling abrupt termination.

The ref instruction represents function reference values. Similarly, ref.extern represents external references.

The invoke instruction represents the imminent invocation of a function instance, identified by its address. It unifies the handling of different forms of calls.

The label and frame instructions model labels and frames “on the stack”. Moreover, the administrative syntax maintains the nesting structure of the original structured control instruction or function body and their instruction...
sequences with an end marker. That way, the end of the inner instruction sequence is known when part of an outer sequence.

Note: For example, the reduction rule for block is:

\[
\text{block } [t^n] \ instr^* \text{ end } \leftrightarrow \text{ label}_n \{\epsilon\} \ instr^* \text{ end}
\]

This replaces the block with a label instruction, which can be interpreted as “pushing” the label on the stack. When end is reached, i.e., the inner instruction sequence has been reduced to the empty sequence – or rather, a sequence of \( n \) const instructions representing the resulting values – then the label instruction is eliminated courtesy of its own reduction rule:

\[
\text{label}_n \{instr^*\} \ val^n \text{ end } \leftrightarrow \ val^n
\]

This can be interpreted as removing the label from the stack and only leaving the locally accumulated operand values.

Block Contexts

In order to specify the reduction of branches, the following syntax of block contexts is defined, indexed by the count \( k \) of labels surrounding a hole \([\_]\) that marks the place where the next step of computation is taking place:

\[
B^0 \quad ::= \quad \text{val}^* \ [\_] \ instr^* \\
B^{k+1} \quad ::= \quad \text{val}^* \ \text{label}_n \{instr^*\} \ B^k \ \text{end} \ instr^*
\]

This definition allows to index active labels surrounding a branch or return instruction.

Note: For example, the reduction of a simple branch can be defined as follows:

\[
\text{label}_0 \{instr^*\} \ B_l[br \ l] \text{ end } \leftrightarrow \ instr^*
\]

Here, the hole \([\_]\) of the context is instantiated with a branch instruction. When a branch occurs, this rule replaces the targeted label and associated instruction sequence with the label’s continuation. The selected label is identified through the label index \( l \), which corresponds to the number of surrounding label instructions that must be hopped over – which is exactly the count encoded in the index of a block context.

Configurations

A configuration consists of the current store and an executing thread.

A thread is a computation over instructions that operates relative to a current frame referring to the module instance in which the computation runs, i.e., where the current function originates from.

\[
\text{config} \ ::= \ store; \ \text{thread} \\
\text{thread} \ ::= \ frame; \ instr^*
\]

Note: The current version of WebAssembly is single-threaded, but configurations with multiple threads may be supported in the future.
Evaluation Contexts

Finally, the following definition of evaluation context and associated structural rules enable reduction inside instruction sequences and administrative forms as well as the propagation of traps:

\[
E ::= \_ | \text{val}^* E \text{instr}^* | \text{label}_n \{ \text{instr}^* \} E \text{end}
\]

\[
S; F ; E[\text{instr}^*] \leftrightarrow S'; F'; E[\text{instr}'^*]
\]

(if \( S; F; \text{instr}^* \rightarrow S'; F'; \text{instr}'^* \))

\[
S; F ; \text{frame}_n \{ F' \} \text{instr}^* \text{end} \leftrightarrow S'; F'; \text{frame}_n \{ F'' \} \text{instr}'^* \text{end}
\]

(if \( S; F'; \text{instr}'^* \rightarrow S'; F''; \text{instr}''^* \))

\[
S; F; E[\text{trap}] \leftrightarrow S; F; \text{trap} \quad (\text{if } E \neq \_)
\]

\[
S; F; \text{frame}_n \{ F' \} \text{trap end} \leftrightarrow S; F; \text{trap}
\]

Reduction terminates when a thread’s instruction sequence has been reduced to a result, that is, either a sequence of values or to a trap.

**Note:** The restriction on evaluation contexts rules out contexts like \( \_ \) and \( \epsilon \_ \) \( \epsilon \) for which \( E[\text{trap}] = \text{trap} \).

For an example of reduction under evaluation contexts, consider the following instruction sequence.

\((\text{f64.const } x_1) (\text{f64.const } x_2) \text{f64.neg} (\text{f64.const } x_3) \text{f64.add} \text{f64.mul}\)

This can be decomposed into \( E[(\text{f64.const } x_2) \text{f64.neg}] \) where

\( E = (\text{f64.const } x_1) \_ (\text{f64.const } x_3) \text{f64.add} \text{f64.mul} \)

Moreover, this is the only possible choice of evaluation context where the contents of the hole matches the left-hand side of a reduction rule.

### 4.3 Numerics

Numeric primitives are defined in a generic manner, by operators indexed over a bit width \( N \).

Some operators are **non-deterministic**, because they can return one of several possible results (such as different NaN values). Technically, each operator thus returns a set of allowed values. For convenience, deterministic results are expressed as plain values, which are assumed to be identified with a respective singleton set.

Some operators are **partial**, because they are not defined on certain inputs. Technically, an empty set of results is returned for these inputs.

In formal notation, each operator is defined by equational clauses that apply in decreasing order of precedence. That is, the first clause that is applicable to the given arguments defines the result. In some cases, similar clauses are combined into one by using the notation \( \pm \) or \( \mp \). When several of these placeholders occur in a single clause, then they must be resolved consistently: either the upper sign is chosen for all of them or the lower sign.

**Note:** For example, the fcopysign operator is defined as follows:

\[
\text{fcopysign}_N(\pm p_1, \pm p_2) = \pm p_1
\]

\[
\text{fcopysign}_N(\pm p_1, \mp p_2) = \mp p_1
\]

This definition is to be read as a shorthand for the following expansion of each clause into two separate ones:

\[
\text{fcopysign}_N(\pm p_1, \pm p_2) = \pm p_1
\]

\[
\text{fcopysign}_N(\pm p_1, \mp p_2) = \mp p_1
\]

\[
\text{fcopysign}_N(\pm p_1, \mp p_2) = \mp p_1
\]

\[
\text{fcopysign}_N(\pm p_1, \pm p_2) = \pm p_1
\]

\[
\text{fcopysign}_N(\pm p_1, \mp p_2) = \mp p_1
\]
Numeric operators are lifted to input sequences by applying the operator element-wise, returning a sequence of results. When there are multiple inputs, they must be of equal length.

\[
op(c^n_1, \ldots, c^n_k) = \op(c^n_1[0], \ldots, c^n_k[0]) \ldots \op(c^n_1[n-1], \ldots, c^n_k[n-1])\]

Note: For example, the unary operator \(\text{fabs}\), when given a sequence of floating-point values, return a sequence of floating-point results:

\[
\text{fabs}_N(z^n) = \text{fabs}_N(z[0]) \ldots \text{fabs}_N(z[n])
\]

The binary operator \(\text{iadd}\), when given two sequences of integers of the same length, \(n\), return a sequence of integer results:

\[
\text{iadd}_N(i^n_1, i^n_2) = \text{iadd}_N(i_1[0], i_2[0]) \ldots \text{iadd}_N(i_1[n], i_2[n])
\]

Conventions:

- The meta variable \(d\) is used to range over single bits.
- The meta variable \(p\) is used to range over (signless) magnitudes of floating-point values, including \(\text{nan}\) and \(\infty\).
- The meta variable \(q\) is used to range over (signless) rational magnitudes, excluding \(\text{nan}\) or \(\infty\).
- The notation \(f^{-1}\) denotes the inverse of a bijective function \(f\).
- Truncation of rational values is written \(\text{trunc}(\pm q)\), with the usual mathematical definition:
  \[
  \text{trunc}(\pm q) = \pm i \quad (\text{if } i \in \mathbb{N} \land +q - 1 < i \leq +q)
  \]
- Saturation of integers is written \(\text{sat}^u_N(i)\) and \(\text{sat}^s_N(i)\). The arguments to these two functions range over arbitrary signed integers.
  - Unsigned saturation, \(\text{sat}^u_N(i)\) clamps \(i\) to between 0 and \(2^N - 1\):
    \[
    \begin{align*}
    \text{sat}^u_N(i) &= 2^N - 1 \quad (\text{if } i > 2^N - 1) \\
    \text{sat}^u_N(i) &= 0 \quad (\text{if } i < 0) \\
    \text{sat}^u_N(i) &= i \quad (\text{otherwise})
    \end{align*}
    \]
  - Signed saturation, \(\text{sat}^s_N(i)\) clamps \(i\) to between \(-2^{N-1}\) and \(2^{N-1} - 1\):
    \[
    \begin{align*}
    \text{sat}^s_N(i) &= \text{signed}_{-N}^{-1}(-2^{N-1}) \quad (\text{if } i < -2^{N-1}) \\
    \text{sat}^s_N(i) &= \text{signed}_{-N}^{-1}(2^{N-1} - 1) \quad (\text{if } i > 2^{N-1} - 1) \\
    \text{sat}^s_N(i) &= i \quad (\text{otherwise})
    \end{align*}
    \]

### 4.3.1 Representations

Numbers have an underlying binary representation as a sequence of bits:

\[
\begin{align*}
\text{bits}_N(i) &= \text{ibits}_N(i) \\
\text{bits}_N(z) &= \text{fbits}_N(z)
\end{align*}
\]

Each of these functions is a bijection, hence they are invertible.
Integers

Integers are represented as base two unsigned numbers:
\[ \text{ibits}_N(i) = d_{N-1} \ldots d_0 \quad (i = 2^{N-1} \cdot d_{N-1} + \cdots + 2^0 \cdot d_0) \]

Boolean operators like \( \land, \lor, \) or \( \lor \) are lifted to bit sequences of equal length by applying them pointwise.

Floating-Point

Floating-point values are represented in the respective binary format defined by IEEE 754-2019 (Section 3.4):
\[ \text{fbits}_N(\pm(1 + m \cdot 2^{-M} \cdot 2^e)) = \text{fsign}(\pm) \text{ibits}_E(e + \text{fbias}_N) \text{ibits}_M(m) \]
\[ \text{fbits}_N(\pm(0 + m \cdot 2^{-M} \cdot 2^e)) = \text{fsign}(\pm) (0)^E \text{ibits}_M(m) \]
\[ \text{fbits}_N(\pm\infty) = \text{fsign}(\pm) (1)^E (0)^M \]
\[ \text{fbits}_N(\pm\text{nan}(n)) = \text{fsign}(\pm) (1)^E \text{ibits}_M(n) \]
\[ \text{fbias}_N = 2^{E-1} - 1 \]
\[ \text{fsign}(+) = 0 \]
\[ \text{fsign}(-) = 1 \]

where \( M = \text{signif}(N) \) and \( E = \text{expon}(N) \).

Storage

When a number is stored into memory, it is converted into a sequence of bytes in little endian\(^{23}\) byte order:
\[ \text{bytes}_t(i) = \text{littleendian} \left( \text{bits}_t(i) \right) \]
\[ \text{littleendian} \left( \epsilon \right) = \epsilon \]
\[ \text{littleendian} \left( d^8 \right) = \text{littleendian} \left( d^8 \right) \text{ibits}_8^{-1}(d^8) \]

Again these functions are invertible bijections.

Vectors

Numeric vectors have the same underlying representation as an \( \text{i128} \). They can also be interpreted as a sequence of numeric values packed into a \( \text{v128} \) with a particular shape.
\[ \text{lanes}_{\text{i128}}(c) = c_0 \ldots c_{N-1} \]
\[ \text{where } B = \lceil t/8 \rceil \]
\[ \land b^{16} = \text{bytes}_{\text{i128}}(c) \]
\[ \land c_i = \text{bytes}_{\text{i128}}^{-1}(b^{16}[i : B : B]) \]

These functions are bijections, so they are invertible.

4.3.2 Integer Operations

Sign Interpretation

Integer operators are defined on \( \text{i}N \) values. Operators that use a signed interpretation convert the value using the following definition, which takes the two’s complement when the value lies in the upper half of the value range (i.e., its most significant bit is 1):
\[ \text{signed}_N(i) = i \quad (0 \leq i < 2^{N-1}) \]
\[ \text{signed}_N(i) = i - 2^N \quad (2^{N-1} \leq i < 2^N) \]

This function is bijective, and hence invertible.

\(^{22}\) https://ieeexplore.ieee.org/document/8766229
\(^{23}\) https://en.wikipedia.org/wiki/Endianness#Little-endian
**Boolean Interpretation**

The integer result of predicates – i.e., tests and relational operators – is defined with the help of the following auxiliary function producing the value 1 or 0 depending on a condition.

\[
\text{bool}(C) = \begin{cases} 
1 & \text{if } C \\ 
0 & \text{otherwise}
\end{cases}
\]

- \(\text{iadd}_N(i_1, i_2)\)
  - Return the result of adding \(i_1\) and \(i_2\) modulo \(2^N\).
  
  \[
  \text{iadd}_N(i_1, i_2) = (i_1 + i_2) \mod 2^N
  \]

- \(\text{isub}_N(i_1, i_2)\)
  - Return the result of subtracting \(i_2\) from \(i_1\) modulo \(2^N\).
  
  \[
  \text{isub}_N(i_1, i_2) = (i_1 - i_2 + 2^N) \mod 2^N
  \]

- \(\text{imul}_N(i_1, i_2)\)
  - Return the result of multiplying \(i_1\) and \(i_2\) modulo \(2^N\).
  
  \[
  \text{imul}_N(i_1, i_2) = (i_1 \cdot i_2) \mod 2^N
  \]

- \(\text{idiv}_u_N(i_1, i_2)\)
  - If \(i_2\) is 0, then the result is undefined.
  - Else, return the result of dividing \(i_1\) by \(i_2\), truncated toward zero.
  
  \[
  \begin{align*}
  \text{idiv}_u_N(i_1, 0) &= \{\} \\
  \text{idiv}_u_N(i_1, i_2) &= \text{trunc}(i_1/i_2)
  \end{align*}
  \]

**Note:** This operator is **partial**.

- \(\text{idiv}_s_N(i_1, i_2)\)
  - Let \(j_1\) be the signed interpretation of \(i_1\).
  - Let \(j_2\) be the signed interpretation of \(i_2\).
  - If \(j_2\) is 0, then the result is undefined.
  - Else if \(j_1\) divided by \(j_2\) is \(2^{N-1}\), then the result is undefined.
  - Else, return the result of dividing \(j_1\) by \(j_2\), truncated toward zero.
  
  \[
  \begin{align*}
  \text{idiv}_s_N(i_1, 0) &= \{\} \\
  \text{idiv}_s_N(i_1, i_2) &= \{\} \quad \text{(if } \text{signed}_N(i_1)/\text{signed}_N(i_2) = 2^{N-1}) \\
  \text{idiv}_s_N(i_1, i_2) &= \text{signed}_N^{-1}(\text{trunc}(\text{signed}_N(i_1)/\text{signed}_N(i_2)))
  \end{align*}
  \]

**Note:** This operator is **partial**. Besides division by 0, the result of \((-2^{N-1})/(-1) = +2^{N-1}\) is not representable as an \(N\)-bit signed integer.
irem\_u_N(i_1, i_2)

- If \(i_2\) is 0, then the result is undefined.
- Else, return the remainder of dividing \(i_1\) by \(i_2\).

\[
\begin{align*}
\text{irem\_u}_N(i_1, 0) &= \{\} \\
\text{irem\_u}_N(i_1, i_2) &= i_1 - i_2 \cdot \text{trunc}(i_1 / i_2)
\end{align*}
\]

**Note:** This operator is **partial**.

As long as both operators are defined, it holds that \(i_1 = i_2 \cdot \text{idiv\_u}(i_1, i_2) + \text{irem\_u}(i_1, i_2)\).

irem\_s_N(i_1, i_2)

- Let \(j_1\) be the **signed interpretation** of \(i_1\).
- Let \(j_2\) be the **signed interpretation** of \(i_2\).
- If \(i_2\) is 0, then the result is undefined.
- Else, return the remainder of dividing \(j_1\) by \(j_2\), with the sign of the dividend \(j_1\).

\[
\begin{align*}
\text{irem\_s}_N(i_1, 0) &= \{\} \\
\text{irem\_s}_N(i_1, i_2) &= \text{signed}^{-1}_N(j_1 - j_2 \cdot \text{trunc}(j_1 / j_2)) \\
&\text{ (where } j_1 = \text{signed}_N(i_1) \land j_2 = \text{signed}_N(i_2))
\end{align*}
\]

**Note:** This operator is **partial**.

As long as both operators are defined, it holds that \(i_1 = i_2 \cdot \text{idiv\_s}(i_1, i_2) + \text{irem\_s}(i_1, i_2)\).

inot\_N(i)

- Return the bitwise negation of \(i\).

\[
inot_N(i) = \text{ibits}^{-1}_N(i) \land \text{ibits}_N(2^N - 1)
\]

iand\_N(i_1, i_2)

- Return the bitwise conjunction of \(i_1\) and \(i_2\).

\[
iand_N(i_1, i_2) = \text{ibits}^{-1}_N(i_1) \land \text{ibits}_N(i_2)
\]

iandnot\_N(i_1, i_2)

- Return the bitwise conjunction of \(i_1\) and the bitwise negation of \(i_2\).

\[
iandnot_N(i_1, i_2) = \text{iand}_N(i_1, \text{inot}_N(i_2))
\]
ior$_N(i_1, i_2)$
  • Return the bitwise disjunction of $i_1$ and $i_2$.
  \[
  \text{ior}_N(i_1, i_2) = \text{ibits}_N^{-1}(\text{ibits}_N(i_1) \lor \text{ibits}_N(i_2))
  \]

ixor$_N(i_1, i_2)$
  • Return the bitwise exclusive disjunction of $i_1$ and $i_2$.
  \[
  \text{ixor}_N(i_1, i_2) = \text{ibits}_N^{-1}(\text{ibits}_N(i_1) \oplus \text{ibits}_N(i_2))
  \]

ishl$_N(i_1, i_2)$
  • Let $k$ be $i_2$ modulo $N$.
  • Return the result of shifting $i_1$ left by $k$ bits, modulo $2^N$.
  \[
  \text{ishl}_N(i_1, i_2) = \text{ibits}_N^{-1}(d_2^{N-k} \times 0^k) \quad (\text{if } \text{ibits}_N(i_1) = d_1^{N-k} \land k = i_2 \mod N)
  \]

ishr_u$_N(i_1, i_2)$
  • Let $k$ be $i_2$ modulo $N$.
  • Return the result of shifting $i_1$ right by $k$ bits, extended with 0 bits.
  \[
  \text{ishr}_u_N(i_1, i_2) = \text{ibits}_N^{-1}(0^k \times d_1^{N-k}) \quad (\text{if } \text{ibits}_N(i_1) = d_1^{N-k} \land d_2^k \land k = i_2 \mod N)
  \]

ishr_s$_N(i_1, i_2)$
  • Let $k$ be $i_2$ modulo $N$.
  • Return the result of shifting $i_1$ right by $k$ bits, extended with the most significant bit of the original value.
  \[
  \text{ishr}_s_N(i_1, i_2) = \text{ibits}_N^{-1}(d_0^{k+1} \times d_1^{N-k-1}) \quad (\text{if } \text{ibits}_N(i_1) = d_0^{N-k-1} \land d_2^k \land k = i_2 \mod N)
  \]

irotl$_N(i_1, i_2)$
  • Let $k$ be $i_2$ modulo $N$.
  • Return the result of rotating $i_1$ left by $k$ bits.
  \[
  \text{irotl}_N(i_1, i_2) = \text{ibits}_N^{-1}(d_2^{N-k} \times d_1^k) \quad (\text{if } \text{ibits}_N(i_1) = d_1^{N-k} \land d_2^k \land k = i_2 \mod N)
  \]

irotr$_N(i_1, i_2)$
  • Let $k$ be $i_2$ modulo $N$.
  • Return the result of rotating $i_1$ right by $k$ bits.
  \[
  \text{irotr}_N(i_1, i_2) = \text{ibits}_N^{-1}(d_2^k \times d_1^{N-k}) \quad (\text{if } \text{ibits}_N(i_1) = d_1^{N-k} \land d_2^k \land k = i_2 \mod N)
  \]
iclz<sub>N</sub>(i)

- Return the count of leading zero bits in \( i \); all bits are considered leading zeros if \( i \) is 0.
  
  \[
  \text{iclz}_N(i) = k \quad \text{(if} \ \text{ibits}_N(i) = 0^k (1 d^*)^N) \]

ictz<sub>N</sub>(i)

- Return the count of trailing zero bits in \( i \); all bits are considered trailing zeros if \( i \) is 0.
  
  \[
  \text{ictz}_N(i) = k \quad \text{(if} \ \text{ibits}_N(i) = (d^* 1)^N 0^k) \]

ipopcnt<sub>N</sub>(i)

- Return the count of non-zero bits in \( i \).
  
  \[
  \text{ipopcnt}_N(i) = k \quad \text{(if} \ \text{ibits}_N(i) = (0^* 1)^N 0^k) \]

ieqz<sub>N</sub>(i)

- Return 1 if \( i \) is zero, 0 otherwise.
  
  \[
  \text{ieqz}_N(i) = \text{bool}(i = 0) \]

ieq<sub>N</sub>(i₁, i₂)

- Return 1 if \( i₁ \) equals \( i₂ \), 0 otherwise.
  
  \[
  \text{ieq}_N(i₁, i₂) = \text{bool}(i₁ = i₂) \]

ine<sub>N</sub>(i₁, i₂)

- Return 1 if \( i₁ \) does not equal \( i₂ \), 0 otherwise.
  
  \[
  \text{ine}_N(i₁, i₂) = \text{bool}(i₁ \neq i₂) \]

ilt<sub>u</sub>_N(i₁, i₂)

- Return 1 if \( i₁ \) is less than \( i₂ \), 0 otherwise.
  
  \[
  \text{ilt}_u_N(i₁, i₂) = \text{bool}(i₁ < i₂) \]

ilt<sub>s</sub>_N(i₁, i₂)

- Let \( j₁ \) be the signed interpretation of \( i₁ \).
- Let \( j₂ \) be the signed interpretation of \( i₂ \).
- Return 1 if \( j₁ \) is less than \( j₂ \), 0 otherwise.
  
  \[
  \text{ilt}_s_N(i₁, i₂) = \text{bool}(\text{signed}_N(i₁) < \text{signed}_N(i₂)) \]
4.3. Numerics

igt_uN(i₁, i₂)

• Return 1 if i₁ is greater than i₂, 0 otherwise.

\[ \text{igt}_uN(i₁, i₂) = \text{bool}(i₁ > i₂) \]

igt_sN(i₁, i₂)

• Let \(j₁\) be the *signed interpretation* of \(i₁\).
• Let \(j₂\) be the *signed interpretation* of \(i₂\).
• Return 1 if \(j₁\) is greater than \(j₂\), 0 otherwise.

\[ \text{igt}_sN(i₁, i₂) = \text{bool}(\text{signed}_N(i₁) > \text{signed}_N(i₂)) \]

ile_uN(i₁, i₂)

• Return 1 if \(i₁\) is less than or equal to \(i₂\), 0 otherwise.

\[ \text{ile}_uN(i₁, i₂) = \text{bool}(i₁ \leq i₂) \]

ile_sN(i₁, i₂)

• Let \(j₁\) be the *signed interpretation* of \(i₁\).
• Let \(j₂\) be the *signed interpretation* of \(i₂\).
• Return 1 if \(j₁\) is less than or equal to \(j₂\), 0 otherwise.

\[ \text{ile}_sN(i₁, i₂) = \text{bool}(\text{signed}_N(i₁) \leq \text{signed}_N(i₂)) \]

ige_uN(i₁, i₂)

• Return 1 if \(i₁\) is greater than or equal to \(i₂\), 0 otherwise.

\[ \text{ige}_uN(i₁, i₂) = \text{bool}(i₁ \geq i₂) \]

ige_sN(i₁, i₂)

• Let \(j₁\) be the *signed interpretation* of \(i₁\).
• Let \(j₂\) be the *signed interpretation* of \(i₂\).
• Return 1 if \(j₁\) is greater than or equal to \(j₂\), 0 otherwise.

\[ \text{ige}_sN(i₁, i₂) = \text{bool}(\text{signed}_N(i₁) \geq \text{signed}_N(i₂)) \]
iextend\textsubscript{\textit{M}_s\textit{N}}(i)

- Return \textit{extend}\textsubscript{\textit{M},\textit{N}}(i).

\[
iextend\textsubscript{\textit{M}_s\textit{N}}(i) = \textit{extend}\textsubscript{\textit{M},\textit{N}}(i)
\]

ibitselect\textsubscript{\textit{N}}(i\textsubscript{1}, i\textsubscript{2}, i\textsubscript{3})

- Let \(j_1\) be the bitwise conjunction of \(i_1\) and \(i_3\).
- Let \(j_3'\) be the bitwise negation of \(i_3\).
- Let \(j_2\) be the bitwise conjunction of \(i_2\) and \(j_3'\).
- Return the bitwise disjunction of \(j_1\) and \(j_2\).

\[
ibitselect\textsubscript{\textit{N}}(i\textsubscript{1}, i\textsubscript{2}, i\textsubscript{3}) = \textit{ior}_{\textit{N}}(\textit{iand}_{\textit{N}}(i\textsubscript{1}, i\textsubscript{3}), \textit{iand}_{\textit{N}}(i\textsubscript{2}, \textit{inot}_{\textit{N}}(i\textsubscript{3})))
\]

iabs\textsubscript{\textit{N}}(i)

- Let \(j\) be the \textit{signed interpretation} of \(i\).
- If \(j\) greater than or equal to 0, then return \(i\).
- Else return the negation of \(j\), modulo \(2^\textit{N}\).

\[
iabs\textsubscript{\textit{N}}(i) = \begin{cases} i & \text{if signed}_{\textit{N}}(i) \geq 0 \\ -\text{signed}_{\textit{N}}(i) \mod 2^\textit{N} & \text{otherwise} \end{cases}
\]

ineg\textsubscript{\textit{N}}(i)

- Return the result of negating \(i\), modulo \(2^\textit{N}\).

\[
ineg\textsubscript{\textit{N}}(i) = (2^\textit{N} - i) \mod 2^\textit{N}
\]

imin\textsubscript{\textit{u}\textit{N}}(i\textsubscript{1}, i\textsubscript{2})

- Return \(i_1\) if \textit{ilt}\textsubscript{\textit{u}\textit{N}}(i\textsubscript{1}, i\textsubscript{2}) is 1, return \(i_2\) otherwise.

\[
imin\textsubscript{\textit{u}\textit{N}}(i\textsubscript{1}, i\textsubscript{2}) = \begin{cases} i_1 & \text{if ilt}_{\textit{u}\textit{N}}(i\textsubscript{1}, i\textsubscript{2}) = 1 \\ i_2 & \text{otherwise} \end{cases}
\]

imin\textsubscript{\textit{s}\textit{N}}(i\textsubscript{1}, i\textsubscript{2})

- Return \(i_1\) if \textit{ilt}\textsubscript{\textit{s}\textit{N}}(i\textsubscript{1}, i\textsubscript{2}) is 1, return \(i_2\) otherwise.

\[
imin\textsubscript{\textit{s}\textit{N}}(i\textsubscript{1}, i\textsubscript{2}) = \begin{cases} i_1 & \text{if ilt}_{\textit{s}\textit{N}}(i\textsubscript{1}, i\textsubscript{2}) = 1 \\ i_2 & \text{otherwise} \end{cases}
\]
4.3. Numerics
iavgr_u_N(i_1, i_2)

- Let \( j \) be the result of adding \( i_1 \), \( i_2 \), and 1.
- Return the result of dividing \( j \) by 2, truncated toward zero.

\[
iavgr_u_N(i_1, i_2) = \text{trunc}(i_1 + i_2 + 1)/2
\]

iq15mulrsat_s_N(i_1, i_2)

- Return the result of \( \text{sat}_N(\text{ishr}_s_N(i_1 \cdot i_2 + 2^{14}, 15)) \).

\[
\text{iq15mulrsat}_s_N(i_1, i_2) = \text{sat}_N(\text{ishr}_s_N(i_1 \cdot i_2 + 2^{14}, 15))
\]

### 4.3.3 Floating-Point Operations

Floating-point arithmetic follows the IEEE 754-2019 standard, with the following qualifications:

- All operators use round-to-nearest ties-to-even, except where otherwise specified. Non-default directed rounding attributes are not supported.
- Following the recommendation that operators propagate \( \text{NaN} \) payloads from their operands is permitted but not required.
- All operators use “non-stop” mode, and floating-point exceptions are not otherwise observable. In particular, neither alternate floating-point exception handling attributes nor operators on status flags are supported. There is no observable difference between quiet and signalling NaNs.

**Note:** Some of these limitations may be lifted in future versions of WebAssembly.

### Rounding

Rounding always is round-to-nearest ties-to-even, in correspondence with IEEE 754-2019 (Section 4.3.1).

An exact floating-point number is a rational number that is exactly representable as a floating-point number of given bit width \( N \).

A limit number for a given floating-point bit width \( N \) is a positive or negative number whose magnitude is the smallest power of 2 that is not exactly representable as a floating-point number of width \( N \) (that magnitude is \( 2^{128} \) for \( N = 32 \) and \( 2^{1024} \) for \( N = 64 \)).

A candidate number is either an exact floating-point number or a positive or negative limit number for the given bit width \( N \).

A candidate pair is a pair \( z_1, z_2 \) of candidate numbers, such that no candidate number exists that lies between the two.

A real number \( r \) is converted to a floating-point value of bit width \( N \) as follows:

- If \( r \) is 0, then return +0.
- Else if \( r \) is an exact floating-point number, then return \( r \).
- Else if \( r \) greater than or equal to the positive limit, then return +\( \infty \).
- Else if \( r \) less than or equal to the negative limit, then return -\( \infty \).
- Else if \( z_1 \) and \( z_2 \) are a candidate pair such that \( z_1 < r < z_2 \), then:
- If $|r - z_1| < |r - z_2|$, then let $z$ be $z_1$.
- Else if $|r - z_1| > |r - z_2|$, then let $z$ be $z_2$.
- Else if $|r - z_1| = |r - z_2|$ and the significand of $z_1$ is even, then let $z$ be $z_1$.
- Else, let $z$ be $z_2$.

- If $z$ is 0, then:
  - If $r < 0$, then return $-0$.
  - Else, return $+0$.
- Else if $z$ is a limit number, then:
  - If $r < 0$, then return $-\infty$.
  - Else, return $+\infty$.
- Else, return $z$.

\[
\begin{align*}
\text{float}_N(0) & = +0 \\
\text{float}_N(r) & = r \quad \text{(if } r \in \text{exact}_N) \\
\text{float}_N(r) & = +\infty \quad \text{(if } r \geq +\text{limit}_N) \\
\text{float}_N(r) & = -\infty \quad \text{(if } r \leq -\text{limit}_N) \\
\text{float}_N(r) & = \text{closest}_N(r, z_1, z_2) \quad \text{(if } z_1 < r < z_2 \land (z_1, z_2) \in \text{candidatepair}_N) \\
\text{rectify}_N(r, z_1, z_2) & = \text{rectify}_N(r, z_1) \quad \text{(if } |r - z_1| < |r - z_2|) \\
\text{rectify}_N(r, z_1, z_2) & = \text{rectify}_N(r, z_2) \quad \text{(if } |r - z_1| > |r - z_2|) \\
\text{rectify}_N(r, z_1, z_2) & = \text{rectify}_N(r, z_1) \quad \text{(if } |r - z_1| = |r - z_2| \land \text{even}_N(z_1)) \\
\text{rectify}_N(r, z_1, z_2) & = \text{rectify}_N(r, z_2) \quad \text{(if } |r - z_1| = |r - z_2| \land \text{even}_N(z_2)) \\
\text{rectify}_N(r, \pm\text{limit}_N) & = \pm\infty \\
\text{rectify}_N(r, 0) & = +0 \quad (r \geq 0) \\
\text{rectify}_N(r, 0) & = -0 \quad (r < 0) \\
\text{rectify}_N(r, z) & = z
\end{align*}
\]

where:

\[
\begin{align*}
\text{exact}_N & = f_N \cap \mathbb{Q} \\
\text{limit}_N & = 2^{\text{exponent}(N) - 1} \\
\text{candidate}_N & = \text{exact}_N \cup \{ +\text{limit}_N, -\text{limit}_N \} \\
\text{candidatepair}_N & = \{(z_1, z_2) \in \text{candidate}_N^2 \mid z_1 < z_2 \land \forall z \in \text{candidate}_N, z \leq z_1 \lor z \geq z_2 \} \\
\text{even}_N((d + m \cdot 2^{-M}) \cdot 2^n) & \iff m \mod 2 = 0 \\
\text{even}_N(\pm\text{limit}_N) & \iff \text{true}
\end{align*}
\]

### NaN Propagation

When the result of a floating-point operator other than `fneg`, `fabs`, or `fcopysign` is a NaN, then its sign is non-deterministic and the payload is computed as follows:

- If the payload of all NaN inputs to the operator is canonical (including the case that there are no NaN inputs), then the payload of the output is canonical as well.
- Otherwise the payload is picked non-deterministically among all arithmetic NaNs; that is, its most significant bit is 1 and all others are unspecified.

This non-deterministic result is expressed by the following auxiliary function producing a set of allowed outputs from a set of inputs:

\[
\begin{align*}
\text{nans}_N(z^*) & = \{ +\text{nan}(n), -\text{nan}(n) \mid n = \text{canon}_N \} \quad \text{(if } \forall \text{nan}(n) \in z^*, n = \text{canon}_N) \\
\text{nans}_N(z^*) & = \{ +\text{nan}(n), -\text{nan}(n) \mid n \geq \text{canon}_N \} \quad \text{(otherwise)}
\end{align*}
\]
faddₙ(z₁, z₂)

• If either z₁ or z₂ is a NaN, then return an element of \( nans_ℕ \{ z₁, z₂ \} \).
• Else if both z₁ and z₂ are infinities of opposite signs, then return an element of \( nans_ℕ \{ \} \).
• Else if both z₁ and z₂ are infinities of equal sign, then return that infinity.
• Else if one of z₁ or z₂ is an infinity, then return that infinity.
• Else if both z₁ and z₂ are zeroes of opposite sign, then return positive zero.
• Else if both z₁ and z₂ are zeroes of equal sign, then return that zero.
• Else if one of z₁ or z₂ is a zero, then return the other operand.
• Else if both z₁ and z₂ are values with the same magnitude but opposite signs, then return positive zero.
• Else return the result of adding z₁ and z₂, \( \text{rounded to the nearest representable value} \).

\[
\begin{align*}
  \text{fadd}_N(\pm\text{nan}(n), z₂) &= nans_ℕ\{\pm\text{nan}(n), z₂\} \\
  \text{fadd}_N(z₁, \pm\text{nan}(n)) &= nans_ℕ\{\pm\text{nan}(n), z₁\} \\
  \text{fadd}_N(\pm\infty, \mp\infty) &= nans_ℕ\{\} \\
  \text{fadd}_N(\pm\infty, \pm\infty) &= \pm\infty \\
  \text{fadd}_N(z₁, \pm\infty) &= \pm\infty \\
  \text{fadd}_N(\pm\infty, z₂) &= \pm\infty \\
  \text{fadd}_N(\pm0, \mp0) &= +0 \\
  \text{fadd}_N(\pm0, \pm0) &= \pm0 \\
  \text{fadd}_N(z₁, \pm0) &= z₁ \\
  \text{fadd}_N(\pm0, z₂) &= z₂ \\
  \text{fadd}_N(\pm q, \mp q) &= +0 \\
  \text{fadd}_N(z₁, z₂) &= \text{float}_N(z₁ + z₂)
\end{align*}
\]

fsubₙ(z₁, z₂)

• If either z₁ or z₂ is a NaN, then return an element of \( nans_ℕ \{ z₁, z₂ \} \).
• Else if both z₁ and z₂ are infinities of equal signs, then return an element of \( nans_ℕ \{ \} \).
• Else if both z₁ and z₂ are infinities of opposite sign, then return z₁.
• Else if z₁ is an infinity, then return that infinity.
• Else if z₂ is an infinity, then return that infinity negated.
• Else if both z₁ and z₂ are zeroes of equal sign, then return positive zero.
• Else if both z₁ and z₂ are zeroes of opposite sign, then return z₁.
• Else if z₂ is a zero, then return z₁.
• Else if z₁ is a zero, then return z₂ negated.
• Else if both z₁ and z₂ are the same value, then return positive zero.
• Else return the result of subtracting z₂ from z₁, \( \text{rounded to the nearest representable value} \).
WebAssembly Specification, Release 2.0 (Draft 2022-04-16)

\[
\text{fsub}_N(\pm \text{nan}(n), z_2) = \text{nans}_N(\pm \text{nan}(n), z_2) \\
\text{fsub}_N(z_1, \pm \text{nan}(n)) = \text{nans}_N(\pm \text{nan}(n), z_1) \\
\text{fsub}_N(\pm \infty, \pm \infty) = \text{nans}_N(\pm \infty, \pm \infty) \\
\text{fsub}_N(\pm \infty, \mp \infty) = \pm \infty \\
\text{fsub}_N(\pm \infty, z_2) = \pm \infty \\
\text{fsub}_N(\pm 0, \pm 0) = +0 \\
\text{fsub}_N(\pm 0, \mp 0) = \mp 0 \\
\text{fsub}_N(z_1, \pm 0) = \pm 0 \quad (\text{note:} \; z_1 \neq \pm \text{zero}) \\
\text{fsub}_N(z_1, z_2) = \text{float}_N(z_1 \cdot z_2)
\]

**Note:** Up to the non-determinism regarding NaNs, it always holds that \(\text{fsub}_N(z_1, z_2) = \text{fadd}_N(z_1, \text{fneg}_N(z_2))\).

\[
\text{fmul}_N(z_1, z_2)
\]

- If either \(z_1\) or \(z_2\) is a NaN, then return an element of \(\text{nans}_N(z_1, z_2)\).
- Else if one of \(z_1\) and \(z_2\) is a zero and the other an infinity, then return an element of \(\text{nans}_N\) (the sign of the zero does not matter).
- Else if both \(z_1\) and \(z_2\) are infinities of equal sign, then return positive infinity.
- Else if both \(z_1\) and \(z_2\) are infinities of opposite sign, then return negative infinity.
- Else if one of \(z_1\) or \(z_2\) is an infinity and the other a value with equal sign, then return positive infinity.
- Else if one of \(z_1\) or \(z_2\) is an infinity and the other a value with opposite sign, then return negative infinity.
- Else if both \(z_1\) and \(z_2\) are zeroes of equal sign, then return positive zero.
- Else if both \(z_1\) and \(z_2\) are zeroes of opposite sign, then return negative zero.
- Else return the result of multiplying \(z_1\) and \(z_2\), rounded to the nearest representable value.

\[
\text{fmul}_N(\pm \text{nan}(n), z_2) = \text{nans}_N(\pm \text{nan}(n), z_2) \\
\text{fmul}_N(z_1, \pm \text{nan}(n)) = \text{nans}_N(\pm \text{nan}(n), z_1) \\
\text{fmul}_N(\pm \infty, \pm 0) = \text{nans}_N(\pm \infty, \pm 0) \\
\text{fmul}_N(\pm 0, \pm \infty) = \text{nans}_N(\pm 0, \pm \infty) \\
\text{fmul}_N(\pm \infty, \pm \infty) = \pm \infty \\
\text{fmul}_N(\pm \infty, \mp \infty) = \mp \infty \\
\text{fmul}_N(\pm \infty, \pm q_2) = \pm q_2 \\
\text{fmul}_N(\pm q_1, \pm 0) = +0 \\
\text{fmul}_N(\pm q_1, \mp 0) = \mp 0 \\
\text{fmul}_N(z_1, z_2) = \text{float}_N(z_1 \cdot z_2)
\]
\[ \text{fdiv}_N(z_1, z_2) \]

- If either \( z_1 \) or \( z_2 \) is a NaN, then return an element of \( \text{nans}_N\{z_1, z_2\} \).
- Else if both \( z_1 \) and \( z_2 \) are infinities, then return an element of \( \text{nans}_N\{\} \).
- Else if both \( z_1 \) and \( z_2 \) are zeroes, then return an element of \( \text{nans}_N\{z_1, z_2\} \).
- Else if \( z_1 \) is an infinity and \( z_2 \) a value with equal sign, then return positive infinity.
- Else if \( z_1 \) is an infinity and \( z_2 \) a value with opposite sign, then return negative infinity.
- Else if \( z_2 \) is an infinity and \( z_1 \) a value with equal sign, then return positive zero.
- Else if \( z_2 \) is an infinity and \( z_1 \) a value with opposite sign, then return negative zero.
- Else if \( z_1 \) is a zero and \( z_2 \) a value with equal sign, then return positive zero.
- Else if \( z_1 \) is a zero and \( z_2 \) a value with opposite sign, then return negative zero.
- Else if \( z_2 \) is a zero and \( z_1 \) a value with equal sign, then return positive infinity.
- Else if \( z_2 \) is a zero and \( z_1 \) a value with opposite sign, then return negative infinity.
- Else return the result of dividing \( z_1 \) by \( z_2 \), rounded to the nearest representable value.

\[
\begin{align*}
\text{fdiv}_N(\pm \text{nan}(n), z_2) &= \text{nans}_N\{\pm \text{nan}(n), z_2\} \\
\text{fmin}_N(z_1, z_2) &= \text{nan}_N\{\pm \text{nan}(n)\} \\
\text{fdiv}_N(\pm \infty, \pm \infty) &= \text{nans}_N\{\} \\
\text{fdiv}_N(\pm \infty, \mp \infty) &= \text{nans}_N\{\} \\
\text{fdiv}_N(\pm 0, \mp 0) &= \text{nans}_N\{\} \\
\text{fdiv}_N(\pm 0, \mp 0) &= \text{nans}_N\{\} \\
\text{fdiv}_N(\pm \infty, \mp q_2) &= +\infty \\
\text{fdiv}_N(\pm \infty, \mp q_2) &= -\infty \\
\text{fdiv}_N(\pm q_1, \pm \infty) &= +0 \\
\text{fdiv}_N(\pm q_1, \mp \infty) &= -0 \\
\text{fmin}_N(\pm 0, \mp q_2) &= +0 \\
\text{fdiv}_N(\pm q_1, \mp 0) &= -0 \\
\text{fdiv}_N(\pm q_1, \mp 0) &= -\infty \\
\text{fdiv}_N(z_1, z_2) &= \text{float}_N(z_1/z_2)
\end{align*}
\]

\[ \text{fmin}_N(z_1, z_2) \]

- If either \( z_1 \) or \( z_2 \) is a NaN, then return an element of \( \text{nans}_N\{z_1, z_2\} \).
- Else if one of \( z_1 \) or \( z_2 \) is a negative infinity, then return negative infinity.
- Else if one of \( z_1 \) or \( z_2 \) is a positive infinity, then return the other value.
- Else if both \( z_1 \) and \( z_2 \) are zeroes of opposite signs, then return negative zero.
- Else return the smaller value of \( z_1 \) and \( z_2 \).

\[
\begin{align*}
\text{fmin}_N(\pm \text{nan}(n), z_2) &= \text{nans}_N\{\pm \text{nan}(n), z_2\} \\
\text{fmin}_N(z_1, z_2) &= \text{nans}_N\{\pm \text{nan}(n)\} \\
\text{fmin}_N(+\infty, z_2) &= z_2 \\
\text{fmin}_N(-\infty, z_2) &= -\infty \\
\text{fmin}_N(z_1, +\infty) &= z_1 \\
\text{fmin}_N(z_1, -\infty) &= -\infty \\
\text{fmin}_N(\pm 0, +0) &= -0 \\
\text{fmin}_N(z_1, z_2) &= z_1 \quad \text{(if } z_1 \leq z_2) \\
\text{fmin}_N(z_1, z_2) &= z_2 \quad \text{(if } z_2 \leq z_1) 
\end{align*}
\]
\( \text{fmax}_N(z_1, z_2) \)

- If either \( z_1 \) or \( z_2 \) is a NaN, then return an element of \( \text{nans}_N \{ z_1, z_2 \} \).
- Else if one of \( z_1 \) or \( z_2 \) is a positive infinity, then return positive infinity.
- Else if one of \( z_1 \) or \( z_2 \) is a negative infinity, then return the other value.
- Else if both \( z_1 \) and \( z_2 \) are zeroes of opposite signs, then return positive zero.
- Else return the larger value of \( z_1 \) and \( z_2 \).

\[
\begin{align*}
\text{fmax}_N(\pm \text{nan}(n), z_2) &= \text{nans}_N \{ \pm \text{nan}(n), z_2 \} \\
\text{fmax}_N(z_1, \pm \text{nan}(n)) &= \text{nans}_N \{ \pm \text{nan}(n), z_1 \} \\
\text{fmax}_N(+\infty, z_2) &= +\infty \\
\text{fmax}_N(-\infty, z_2) &= z_2 \\
\text{fmax}_N(z_1, +\infty) &= +\infty \\
\text{fmax}_N(z_1, -\infty) &= z_1 \\
\text{fmax}_N(\pm 0, \mp 0) &= +0 \\
\text{fmax}_N(z_1, z_2) &= z_1 \quad (\text{if } z_1 \geq z_2) \\
\text{fmax}_N(z_1, z_2) &= z_2 \quad (\text{if } z_2 \geq z_1)
\end{align*}
\]

\( \text{fcopysign}_N(z_1, z_2) \)

- If \( z_1 \) and \( z_2 \) have the same sign, then return \( z_1 \).
- Else return \( z_1 \) with negated sign.

\[
\begin{align*}
\text{fcopysign}_N(\pm p_1, \pm p_2) &= \pm p_1 \\
\text{fcopysign}_N(\pm p_1, \mp p_2) &= \mp p_1
\end{align*}
\]

\( \text{fabs}_N(z) \)

- If \( z \) is a NaN, then return \( z \) with positive sign.
- Else if \( z \) is an infinity, then return positive infinity.
- Else if \( z \) is a zero, then return positive zero.
- Else if \( z \) is a positive value, then \( z \).
- Else return \( z \) negated.

\[
\begin{align*}
\text{fabs}_N(\pm \text{nan}(n)) &= +\text{nan}(n) \\
\text{fabs}_N(+\infty) &= +\infty \\
\text{fabs}_N(-0) &= +0 \\
\text{fabs}_N(\pm q) &= +q
\end{align*}
\]

\( \text{fneg}_N(z) \)

- If \( z \) is a NaN, then return \( z \) with negated sign.
- Else if \( z \) is an infinity, then return that infinity negated.
- Else if \( z \) is a zero, then return that zero negated.
- Else return \( z \) negated.

\[
\begin{align*}
\text{fneg}_N(\pm \text{nan}(n)) &= \mp \text{nan}(n) \\
\text{fneg}_N(+\infty) &= \mp \infty \\
\text{fneg}_N(-0) &= \mp 0 \\
\text{fneg}_N(\pm q) &= \mp q
\end{align*}
\]
\( \text{fsqrt}_N(z) \)

- If \( z \) is a NaN, then return an element of \( \text{nans}_N \{z\} \).
- Else if \( z \) is negative infinity, then return an element of \( \text{nans}_N \{\} \).
- Else if \( z \) is positive infinity, then return positive infinity.
- Else if \( z \) is a zero, then return that zero.
- Else if \( z \) has a negative sign, then return an element of \( \text{nans}_N \{\} \).
- Else return the square root of \( z \).

\[
\text{fsqrt}_N(\pm\text{nan}(n)) = \text{nans}_N\{\pm\text{nan}(n)\}
\]
\[
\text{fsqrt}_N(-\infty) = \text{nans}_N\{\}
\]
\[
\text{fsqrt}_N(+\infty) = +\infty
\]
\[
\text{fsqrt}_N(\pm0) = \pm0
\]
\[
\text{fsqrt}_N(-q) = \text{nans}_N\{\}
\]
\[
\text{fsqrt}_N(+q) = \text{float}_N(\sqrt{q})
\]

\( \text{fceil}_N(z) \)

- If \( z \) is a NaN, then return an element of \( \text{nans}_N \{z\} \).
- Else if \( z \) is an infinity, then return \( z \).
- Else if \( z \) is a zero, then return \( z \).
- Else if \( z \) is smaller than 0 but greater than \(-1\), then return negative zero.
- Else return the smallest integral value that is not smaller than \( z \).

\[
\text{fceil}_N(\pm\text{nan}(n)) = \text{nans}_N\{\pm\text{nan}(n)\}
\]
\[
\text{fceil}_N(\pm\infty) = \pm\infty
\]
\[
\text{fceil}_N(\pm0) = \pm0
\]
\[
\text{fceil}_N(-q) = -0 \quad \text{(if } -1 < -q < 0)\]
\[
\text{fceil}_N(\pm q) = \text{float}_N(i) \quad \text{(if } \pm q - 1 < i < \pm q + 1)\]

\( \text{ffloor}_N(z) \)

- If \( z \) is a NaN, then return an element of \( \text{nans}_N \{z\} \).
- Else if \( z \) is an infinity, then return \( z \).
- Else if \( z \) is a zero, then return \( z \).
- Else if \( z \) is greater than 0 but smaller than 1, then return positive zero.
- Else return the largest integral value that is not larger than \( z \).

\[
\text{ffloor}_N(\pm\text{nan}(n)) = \text{nans}_N\{\pm\text{nan}(n)\}
\]
\[
\text{ffloor}_N(\pm\infty) = \pm\infty
\]
\[
\text{ffloor}_N(\pm0) = \pm0
\]
\[
\text{ffloor}_N(+q) = +0 \quad \text{(if } 0 < +q < 1)\]
\[
\text{ffloor}_N(\pm q) = \text{float}_N(i) \quad \text{(if } \pm q - 1 < i < \pm q)\]
ftrunc\(_N(z)\)

- If \(z\) is a NaN, then return an element of \(\text{nans}_N\{z\}\).
- Else if \(z\) is an infinity, then return \(z\).
- Else if \(z\) is a zero, then return \(z\).
- Else if \(z\) is greater than 0 but smaller than 1, then return positive zero.
- Else if \(z\) is smaller than 0 but greater than −1, then return negative zero.
- Else return the integral value with the same sign as \(z\) and the largest magnitude that is not larger than the magnitude of \(z\).

\[
\begin{align*}
\text{ftrunc}_N(\pm\text{nan}(n)) &= \text{nans}_N\{\pm\text{nan}(n)\} \\
\text{ftrunc}_N(\pm\infty) &= \pm\infty \\
\text{ftrunc}_N(\pm0) &= \pm0 \\
\text{ftrunc}_N(+q) &= +0 & (\text{if } 0 < +q < 1) \\
\text{ftrunc}_N(-q) &= -0 & (\text{if } -1 < -q < 0) \\
\text{ftrunc}_N(\pm q) &= \text{float}_N(\pm i) & (\text{if } |+q - 1| < i \leq +q)
\end{align*}
\]

fnearest\(_N(z)\)

- If \(z\) is a NaN, then return an element of \(\text{nans}_N\{z\}\).
- Else if \(z\) is an infinity, then return \(z\).
- Else if \(z\) is a zero, then return \(z\).
- Else if \(z\) is greater than 0 but smaller than or equal to 0.5, then return positive zero.
- Else if \(z\) is smaller than 0 but greater than or equal to −0.5, then return negative zero.
- Else return the integral value that is nearest to \(z\); if two values are equally near, return the even one.

\[
\begin{align*}
\text{fnearest}_N(\pm\text{nan}(n)) &= \text{nans}_N\{\pm\text{nan}(n)\} \\
\text{fnearest}_N(\pm\infty) &= \pm\infty \\
\text{fnearest}_N(\pm0) &= \pm0 \\
\text{fnearest}_N(+q) &= +0 & (\text{if } 0 < +q \leq 0.5) \\
\text{fnearest}_N(-q) &= -0 & (\text{if } -0.5 \leq -q < 0) \\
\text{fnearest}_N(\pm q) &= \text{float}_N(\pm i) & (\text{if } |i - q| < 0.5) \\
\text{fnearest}_N(\pm q) &= \text{float}_N(\pm i) & (\text{if } |i - q| = 0.5 \land i \text{ even})
\end{align*}
\]

feq\(_N(z_1, z_2)\)

- If either \(z_1\) or \(z_2\) is a NaN, then return 0.
- Else if both \(z_1\) and \(z_2\) are zeroes, then return 1.
- Else if both \(z_1\) and \(z_2\) are the same value, then return 1.
- Else return 0.

\[
\begin{align*}
\text{feq}_N(\pm\text{nan}(n), z_2) &= 0 \\
\text{feq}_N(z_1, \pm\text{nan}(n)) &= 0 \\
\text{feq}_N(\pm0, \mp0) &= 1 \\
\text{feq}_N(z_1, z_2) &= \text{bool}(z_1 = z_2)
\end{align*}
\]

4.3. Numerics
\( \text{fne}_N(z_1, z_2) \)

- If either \( z_1 \) or \( z_2 \) is a NaN, then return 1.
- Else if both \( z_1 \) and \( z_2 \) are zeroes, then return 0.
- Else if both \( z_1 \) and \( z_2 \) are the same value, then return 0.
- Else return 1.

\[
\begin{align*}
\text{fne}_N(\pm \text{nan}(n), z_2) &= 1 \\
\text{fne}_N(z_1, \pm \text{nan}(n)) &= 1 \\
\text{fne}_N(\pm 0, \mp 0) &= 0 \\
\text{fne}_N(z_1, z_2) &= \text{bool}(z_1 \neq z_2)
\end{align*}
\]

\( \text{flt}_N(z_1, z_2) \)

- If either \( z_1 \) or \( z_2 \) is a NaN, then return 0.
- Else if \( z_1 \) and \( z_2 \) are the same value, then return 0.
- Else if \( z_1 \) is positive infinity, then return 0.
- Else if \( z_1 \) is negative infinity, then return 1.
- Else if \( z_2 \) is positive infinity, then return 0.
- Else if \( z_2 \) is negative infinity, then return 0.
- Else if both \( z_1 \) and \( z_2 \) are zeroes, then return 0.
- Else if \( z_1 \) is smaller than \( z_2 \), then return 1.
- Else return 0.

\[
\begin{align*}
\text{flt}_N(\pm \text{nan}(n), z_2) &= 0 \\
\text{flt}_N(z_1, \pm \text{nan}(n)) &= 0 \\
\text{flt}_N(z, z) &= 0 \\
\text{flt}_N(\pm \infty, z_2) &= 0 \\
\text{flt}_N(-\infty, z_2) &= 1 \\
\text{flt}_N(z_1, +\infty) &= 1 \\
\text{flt}_N(z_1, -\infty) &= 0 \\
\text{flt}_N(\pm 0, \mp 0) &= 0 \\
\text{flt}_N(z_1, z_2) &= \text{bool}(z_1 < z_2)
\end{align*}
\]

\( \text{fgt}_N(z_1, z_2) \)

- If either \( z_1 \) or \( z_2 \) is a NaN, then return 0.
- Else if \( z_1 \) and \( z_2 \) are the same value, then return 0.
- Else if \( z_1 \) is positive infinity, then return 1.
- Else if \( z_1 \) is negative infinity, then return 0.
- Else if \( z_2 \) is positive infinity, then return 0.
- Else if \( z_2 \) is negative infinity, then return 1.
- Else if both \( z_1 \) and \( z_2 \) are zeroes, then return 0.
- Else if \( z_1 \) is larger than \( z_2 \), then return 1.
- Else return 0.
\[ \text{fgt}_N(\pm \text{nan}(n), z_2) = 0 \]
\[ \text{fgt}_N(z_1, \pm \text{nan}(n)) = 0 \]
\[ \text{fgt}_N(z, z) = 0 \]
\[ \text{fgt}_N(+\infty, z_2) = 1 \]
\[ \text{fgt}_N(-\infty, z_2) = 0 \]
\[ \text{fgt}_N(z_1, +\infty) = 0 \]
\[ \text{fgt}_N(z_1, -\infty) = 1 \]
\[ \text{fgt}_N(\pm 0, \mp 0) = 0 \]
\[ \text{fgt}_N(z_1, z_2) = \text{bool}(z_1 > z_2) \]

\[ \text{fle}_N(z_1, z_2) \]

- If either \( z_1 \) or \( z_2 \) is a NaN, then return 0.
- Else if \( z_1 \) and \( z_2 \) are the same value, then return 1.
- Else if \( z_1 \) is positive infinity, then return 0.
- Else if \( z_1 \) is negative infinity, then return 1.
- Else if \( z_2 \) is positive infinity, then return 1.
- Else if \( z_2 \) is negative infinity, then return 0.
- Else if both \( z_1 \) and \( z_2 \) are zeroes, then return 1.
- Else if \( z_1 \) is smaller than or equal to \( z_2 \), then return 1.
- Else return 0.

\[ \text{fle}_N(\pm \text{nan}(n), z_2) = 0 \]
\[ \text{fle}_N(z_1, \pm \text{nan}(n)) = 0 \]
\[ \text{fle}_N(z, z) = 1 \]
\[ \text{fle}_N(+\infty, z_2) = 0 \]
\[ \text{fle}_N(-\infty, z_2) = 1 \]
\[ \text{fle}_N(z_1, +\infty) = 1 \]
\[ \text{fle}_N(z_1, -\infty) = 0 \]
\[ \text{fle}_N(\pm 0, \mp 0) = 1 \]
\[ \text{fle}_N(z_1, z_2) = \text{bool}(z_1 \leq z_2) \]

\[ \text{fge}_N(z_1, z_2) \]

- If either \( z_1 \) or \( z_2 \) is a NaN, then return 0.
- Else if \( z_1 \) and \( z_2 \) are the same value, then return 1.
- Else if \( z_1 \) is positive infinity, then return 1.
- Else if \( z_1 \) is negative infinity, then return 0.
- Else if \( z_2 \) is positive infinity, then return 0.
- Else if \( z_2 \) is negative infinity, then return 1.
- Else if both \( z_1 \) and \( z_2 \) are zeroes, then return 1.
- Else if \( z_1 \) is smaller than or equal to \( z_2 \), then return 1.
- Else return 0.
\[ f_{\geq N}(\pm \text{nan}(n), z_2) = 0 \]
\[ f_{\geq N}(z_1, \pm \text{nan}(n)) = 0 \]
\[ f_{\geq N}(z, z) = 1 \]
\[ f_{\geq N}(+\infty, z_2) = 1 \]
\[ f_{\geq N}(-\infty, z_2) = 0 \]
\[ f_{\geq N}(z_1, +\infty) = 0 \]
\[ f_{\geq N}(z_1, -\infty) = 1 \]
\[ f_{\geq N}(\pm 0, \mp 0) = 1 \]
\[ f_{\geq N}(z_1, z_2) = \text{bool}(z_1 \geq z_2) \]

\[ \text{fpmin}_N(z_1, z_2) \]
- If \( z_2 \) is less than \( z_1 \) then return \( z_2 \).
- Else return \( z_1 \).

\[
\begin{align*}
\text{fpmin}_N(z_1, z_2) &= z_2 \quad \text{(if } \text{flt}_N(z_2, z_1) = 1) \\
\text{fpmin}_N(z_1, z_2) &= z_1 \quad \text{(otherwise)}
\end{align*}
\]

\[ \text{fpmax}_N(z_1, z_2) \]
- If \( z_1 \) is less than \( z_2 \) then return \( z_2 \).
- Else return \( z_1 \).

\[
\begin{align*}
\text{fpmax}_N(z_1, z_2) &= z_2 \quad \text{(if } \text{flt}_N(z_1, z_2) = 1) \\
\text{fpmax}_N(z_1, z_2) &= z_1 \quad \text{(otherwise)}
\end{align*}
\]

### 4.3.4 Conversions

**extend^u_{M,N}(i)**
- Return \( i \).

\[
\text{extend}^u_{M,N}(i) = i
\]

**Note:** In the abstract syntax, unsigned extension just reinterprets the same value.

**extend^s_{M,N}(i)**
- Let \( j \) be the *signed interpretation* of \( i \) of size \( M \).
- Return the two's complement of \( j \) relative to size \( N \).

\[
\text{extend}^s_{M,N}(i) = \text{signed}_N^{-1}(\text{signed}_M(i))
\]
wrap<sub>M,N</sub>(i)
• Return \( i \mod 2^N \).

\[
wrap_{M,N}(i) = i \mod 2^N
\]

trunc<sup>u</sup><sub>M,N</sub>(z)
• If \( z \) is a NaN, then the result is undefined.
• Else if \( z \) is an infinity, then the result is undefined.
• Else if \( z \) is a number and \( \text{trunc}(z) \) is a value within range of the target type, then return that value.
• Else the result is undefined.

\[
\text{trunc}^u_{M,N}(\pm \text{nan}(n)) = \{\}
\]
\[
\text{trunc}^u_{M,N}(\pm \infty) = \{\}
\]
\[
\text{trunc}^u_{M,N}(\pm q) = \text{trunc}(\pm q) \quad (\text{if } -1 < \text{trunc}(\pm q) < 2^N)
\]
\[
\text{trunc}^u_{M,N}(\pm q) = \{\} \quad (\text{otherwise})
\]

Note: This operator is partial. It is not defined for NaNs, infinities, or values for which the result is out of range.

trunc<sup>s</sup><sub>M,N</sub>(z)
• If \( z \) is a NaN, then the result is undefined.
• Else if \( z \) is an infinity, then the result is undefined.
• Else if \( z \) is a number and \( \text{trunc}(z) \) is a value within range of the target type, then return that value.
• Else the result is undefined.

\[
\text{trunc}^s_{M,N}(\pm \text{nan}(n)) = \{\}
\]
\[
\text{trunc}^s_{M,N}(\pm \infty) = \{\}
\]
\[
\text{trunc}^s_{M,N}(\pm q) = \text{trunc}(\pm q) \quad (\text{if } -2^{N-1} - 1 < \text{trunc}(\pm q) < 2^{N-1})
\]
\[
\text{trunc}^s_{M,N}(\pm q) = \{\} \quad (\text{otherwise})
\]

Note: This operator is partial. It is not defined for NaNs, infinities, or values for which the result is out of range.

trunc_sat<sub>u</sub>_{M,N}(z)
• If \( z \) is a NaN, then return 0.
• Else if \( z \) is negative infinity, then return 0.
• Else if \( z \) is positive infinity, then return \( 2^N - 1 \).
• Else, return \( \text{sat}^u_N(\text{trunc}(z)) \).

\[
\text{trunc\_sat\_u}_{M,N}(\pm \text{nan}(n)) = 0
\]
\[
\text{trunc\_sat\_u}_{M,N}(\pm \infty) = 0
\]
\[
\text{trunc\_sat\_u}_{M,N}(\pm \infty) = 2^N - 1
\]
\[
\text{trunc\_sat\_u}_{M,N}(z) = \text{sat}^u_N(\text{trunc}(z))
\]

4.3. Numerics 85
trunc sat \_s_{M,N}(z)

- If \( z \) is a NaN, then return 0.
- Else if \( z \) is negative infinity, then return \(-2^{N-1}\).
- Else if \( z \) is positive infinity, then return \(2^{N-1} - 1\).
- Else, return \( \text{sat}^N(\text{trunc}(z)) \).

\[
\begin{align*}
\text{trunc sat } _s_{M,N}(\pm \text{nan}(n)) &= 0 \\
\text{trunc sat } _s_{M,N}(-\infty) &= -2^{N-1} \\
\text{trunc sat } _s_{M,N}(+\infty) &= 2^{N-1} - 1 \\
\text{trunc sat } _s_{M,N}(z) &= \text{sat}^N(\text{trunc}(z))
\end{align*}
\]

promote\_M,N(z)

- If \( z \) is a canonical NaN, then return an element of \( \text{nans}_N \) (i.e., a canonical NaN of size \( N \)).
- Else if \( z \) is a NaN, then return an element of \( \text{nans}_N\{\pm \text{nan}(1)\} \) (i.e., any arithmetic NaN of size \( N \)).
- Else, return \( z \).

\[
\begin{align*}
\text{promote}_{M,N}(\pm \text{nan}(n)) &= \text{nans}_N \{ \} & (\text{if } n = \text{canon}_N) \\
\text{promote}_{M,N}(\pm \text{nan}(n)) &= \text{nans}_N \{ +\text{nan}(1) \} & (\text{otherwise}) \\
\text{promote}_{M,N}(z) &= z
\end{align*}
\]

demote\_M,N(z)

- If \( z \) is a canonical NaN, then return an element of \( \text{nans}_N \) (i.e., a canonical NaN of size \( N \)).
- Else if \( z \) is a NaN, then return an element of \( \text{nans}_N\{\pm \text{nan}(1)\} \) (i.e., any NaN of size \( N \)).
- Else if \( z \) is an infinity, then return that infinity.
- Else if \( z \) is a zero, then return that zero.
- Else, return \( \text{float}_N(z) \).

\[
\begin{align*}
\text{demote}_{M,N}(\pm \text{nan}(n)) &= \text{nans}_N \{ \} & (\text{if } n = \text{canon}_N) \\
\text{demote}_{M,N}(\pm \text{nan}(n)) &= \text{nans}_N \{ +\text{nan}(1) \} & (\text{otherwise}) \\
\text{demote}_{M,N}(\pm \infty) &= \pm \infty \\
\text{demote}_{M,N}(\pm 0) &= \pm 0 \\
\text{demote}_{M,N}(\pm q) &= \text{float}_N(\pm q)
\end{align*}
\]

convert\text{u}\_M,N(i)

- Return \( \text{float}_N(i) \).

\[
\text{convert}^u_{M,N}(i) = \text{float}_N(i)
\]
convert\(_{M,N}(i)\)
- Let \(j\) be the \textit{signed interpretation} of \(i\).
- Return \(\text{float}_N(j)\).

\[
\text{convert}^s_{M,N}(i) = \text{float}_N(\text{signed}_M(i))
\]

reinterpret\(_{t_1,t_2}(c)\)
- Let \(d^*\) be the bit sequence \(\text{bits}_{t_1}(c)\).
- Return the constant \(c'\) for which \(\text{bits}_{t_2}(c') = d^*\).

\[
\text{reinterpret}^t_{t_1,t_2}(c) = \text{bits}^{-1}_{t_2}(\text{bits}_{t_1}(c))
\]

narrow\(_{M,N}(i)\)
- Let \(j\) be the \textit{signed interpretation} of \(i\) of size \(M\).
- Return \(\text{sat}^s_N(j)\).

\[
\text{narrow}^s_{M,N}(i) = \text{sat}^s_N(\text{signed}_M(i))
\]

narrow\(_{M,N}(i)\)
- Let \(j\) be the \textit{signed interpretation} of \(i\) of size \(M\).
- Return \(\text{sat}^u_N(j)\).

\[
\text{narrow}^u_{M,N}(i) = \text{sat}^u_N(\text{signed}_M(i))
\]

4.4 Instructions

WebAssembly computation is performed by executing individual \textit{instructions}.

4.4.1 Numeric Instructions

Numeric instructions are defined in terms of the generic \textit{numeric operators}. The mapping of numeric instructions to their underlying operators is expressed by the following definition:

\[
op_{i_N}(n_1, \ldots, n_k) = i\op_N(n_1, \ldots, n_k)
\]

\[
op_{f_N}(z_1, \ldots, z_k) = f\op_N(z_1, \ldots, z_k)
\]

And for conversion operators:

\[
\text{extop}_{t_1,t_2}^{sx}(c) = \text{extop}_{[t_1],[t_2]}^{sx}(c)
\]

Where the underlying operators are partial, the corresponding instruction will \textit{trap} when the result is not defined. Where the underlying operators are non-deterministic, because they may return one of multiple possible \textit{NaN} values, so are the corresponding instructions.

**Note:** For example, the result of instruction \texttt{i32.add} applied to operands \(i_1, i_2\) invokes \texttt{add}_32(i_1, i_2), which maps to the generic \texttt{iadd}_32(i_1, i_2) via the above definition. Similarly, \texttt{i64.trunc_f32_s} applied to \(z\) invokes \texttt{trunc}^{32,64}(z), which maps to the generic \texttt{trunc}_32,64(z).
\( t.\text{const} \ c \)

1. Push the value \( t.\text{const} \ c \) to the stack.

**Note:** No formal reduction rule is required for this instruction, since \( \text{const} \) instructions already are \textit{values}.

\( t.\text{unop} \)

1. Assert: due to validation, a value of value type \( t \) is on the top of the stack.
2. Pop the value \( t.\text{const} \ c_1 \) from the stack.
3. If \( \text{unop}_{t}(c_1) \) is defined, then:
   a. Let \( c \) be a possible result of computing \( \text{unop}_{t}(c_1) \).
   b. Push the value \( t.\text{const} \ c \) to the stack.
4. Else:
   a. Trap.

\[
\begin{align*}
(t.\text{const} \ c_1) \ t.\text{unop} & \leftrightarrow (t.\text{const} \ c) \quad (\text{if } c \in \text{unop}_{t}(c_1)) \\
(t.\text{const} \ c_1) \ t.\text{unop} & \leftrightarrow \text{trap} \quad (\text{if } \text{unop}_{t}(c_1) = \{\})
\end{align*}
\]

\( t.\text{binop} \)

1. Assert: due to validation, two values of value type \( t \) are on the top of the stack.
2. Pop the value \( t.\text{const} \ c_2 \) from the stack.
3. Pop the value \( t.\text{const} \ c_1 \) from the stack.
4. If \( \text{binop}_{t}(c_1, c_2) \) is defined, then:
   a. Let \( c \) be a possible result of computing \( \text{binop}_{t}(c_1, c_2) \).
   b. Push the value \( t.\text{const} \ c \) to the stack.
5. Else:
   a. Trap.

\[
\begin{align*}
(t.\text{const} \ c_1) \ (t.\text{const} \ c_2) \ t.\text{binop} & \leftrightarrow (t.\text{const} \ c) \quad (\text{if } c \in \text{binop}_{t}(c_1, c_2)) \\
(t.\text{const} \ c_1) \ (t.\text{const} \ c_2) \ t.\text{binop} & \leftrightarrow \text{trap} \quad (\text{if } \text{binop}_{t}(c_1, c_2) = \{\})
\end{align*}
\]

\( t.\text{testop} \)

1. Assert: due to validation, a value of value type \( t \) is on the top of the stack.
2. Pop the value \( t.\text{const} \ c_1 \) from the stack.
3. Let \( c \) be the result of computing \( \text{testop}_{t}(c_1) \).
4. Push the value \( \text{i32}.\text{const} \ c \) to the stack.

\[
(t.\text{const} \ c_1) \ t.\text{testop} \leftrightarrow (\text{i32}.\text{const} \ c) \quad (\text{if } c = \text{testop}_{t}(c_1))
\]


\texttt{t.relop}

1. Assert: due to validation, two values of value type \( t \) are on the top of the stack.
2. Pop the value \( \text{t.const } c_2 \) from the stack.
3. Pop the value \( \text{t.const } c_1 \) from the stack.
4. Let \( c \) be the result of computing \( \text{relop}_t(c_1, c_2) \).
5. Push the value \( \text{i32.const } c \) to the stack.

\( t_2 . \text{cvtop}_{t_1, sx} \)

1. Assert: due to validation, a value of value type \( t_1 \) is on the top of the stack.
2. Pop the value \( \text{t1.const } c_1 \) from the stack.
3. If \( \text{cvtop}_{t_1, t_2}(c_1) \) is defined:
   a. Let \( c_2 \) be a possible result of computing \( \text{cvtop}_{t_1, t_2}(c_1) \).
   b. Push the value \( \text{t2.const } c_2 \) to the stack.
4. Else:
   a. Trap.

\( \text{ref.null } t \)

1. Push the value \( \text{ref.null } t \) to the stack.

\textbf{Note:} No formal reduction rule is required for this instruction, since the \texttt{ref.null} instruction is already a \texttt{value}.

\texttt{ref.is.null}

1. Assert: due to validation, a reference value is on the top of the stack.
2. Pop the value \( \text{val} \) from the stack.
3. If \( \text{val} \) is \texttt{ref.null}, then:
   a. Push the value \( \text{i32.const } 1 \) to the stack.
4. Else:
   a. Push the value \( \text{i32.const } 0 \) to the stack.

\begin{align*}
\text{val ref.is.null} & \iff \text{i32.const } 1 \quad (\text{if } \text{val} = \text{ref.null}) \\
\text{val ref.is.null} & \iff \text{i32.const } 0 \quad (\text{otherwise})
\end{align*}
ref.func \( \text{x} \)

1. Let \( F \) be the current frame.
2. Assert: due to validation, \( F;\text{module.funcaddrs}[\text{x}] \) exists.
3. Let \( a \) be the function address \( F;\text{module.funcaddrs}[\text{x}] \).
4. Push the value \( \text{ref } a \) to the stack.

\[
F;\text{ref.func } \text{x} \leftrightarrow F;\text{ref } a \quad (\text{if } a = F;\text{module.funcaddrs}[\text{x}])
\]

### 4.4.3 Vector Instructions

Most vector instructions are defined in terms of generic numeric operators applied lane-wise based on the **shape**.

\[
op_{\text{rxN}}(n_1, \ldots, n_k) = \text{lanes}_{\text{rxN}}^1(\text{lanes}_{\text{rxN}}(n_1) \ldots \text{lanes}_{\text{rxN}}(n_k))
\]

**Note:** For example, the result of instruction \( i32x4;\text{add} \) applied to operands \( i_1, i_2 \) invokes \( \text{add}_{i32x4}(i_1, i_2) \), which maps to \( \text{lanes}_{i32x4}^{-1}(\text{add}_{i32}(i_1^+, i_2^+)) \), where \( i_1^+ \) and \( i_2^+ \) are sequences resulting from invoking \( \text{lanes}_{i32x4}(i_1) \) and \( \text{lanes}_{i32x4}(i_2) \) respectively.

\[\text{v128.const } \text{c} \]

1. Push the value \( \text{v128.const } \text{c} \) to the stack.

**Note:** No formal reduction rule is required for this instruction, since \( \text{const } \) instructions coincide with \( \text{values} \).

\[\text{v128.vvunop} \]

1. Assert: due to validation, a value of value type \( \text{v128} \) is on the top of the stack.
2. Pop the value \( \text{v128.const } \text{c}_1 \) from the stack.
3. Let \( \text{c} \) be the result of computing \( \text{vvunop}_{128}(\text{c}_1) \).
4. Push the value \( \text{v128.const } \text{c} \) to the stack.

\[
(\text{v128.const } \text{c}_1) \text{ v128.vvunop} \leftrightarrow (\text{v128.const } \text{c}) \quad (\text{if } \text{c} = \text{vvunop}_{128}(\text{c}_1))
\]

\[\text{v128.vvbinop} \]

1. Assert: due to validation, two values of value type \( \text{v128} \) are on the top of the stack.
2. Pop the value \( \text{v128.const } \text{c}_2 \) from the stack.
3. Pop the value \( \text{v128.const } \text{c}_1 \) from the stack.
4. Let \( \text{c} \) be the result of computing \( \text{vvbinop}_{128}(\text{c}_1, \text{c}_2) \).
5. Push the value \( \text{v128.const } \text{c} \) to the stack.

\[
(\text{v128.const } \text{c}_1) (\text{v128.const } \text{c}_2) \text{ v128.vvbinop} \leftrightarrow (\text{v128.const } \text{c}) \quad (\text{if } \text{c} = \text{vvbinop}_{128}(\text{c}_1, \text{c}_2))
\]
4.4. Instructions

\texttt{v128.veternop}

1. Assert: due to \textit{validation}, three values of \texttt{value type v128} are on the top of the stack.
2. Pop the value \texttt{v128.const c3} from the stack.
3. Pop the value \texttt{v128.const c2} from the stack.
4. Pop the value \texttt{v128.const c1} from the stack.
5. Let \( c \) be the result of computing \( \texttt{veternop}_{128}(c_1, c_2, c_3) \).
6. Push the value \texttt{v128.const c} to the stack.

\( \texttt{(v128.const c1) (v128.const c2) (v128.const c3) v128.veternop} \rightarrow \texttt{(v128.const c)} \) (if \( c = \texttt{veternop}_{128}(c_1, c_2, c_3) \))

\texttt{v128.any_true}

1. Assert: due to \textit{validation}, a value of \texttt{value type v128} is on the top of the stack.
2. Pop the value \texttt{v128.const c1} from the stack.
3. Let \( i \) be the result of computing \( \texttt{ine}_{128}(c_1, 0) \).
4. Push the value \texttt{i32.const i} onto the stack.

\( \texttt{(v128.const c1) v128.any_true} \rightarrow \texttt{(i32.const i)} \) (if \( i = \texttt{ine}_{128}(c_1, 0) \))

\texttt{i8x16.swizzle}

1. Assert: due to \textit{validation}, two values of \texttt{value type v128} are on the top of the stack.
2. Pop the value \texttt{v128.const c2} from the stack.
3. Let \( i^* \) be the sequence \( \texttt{lanes}_{8x16}(c_2) \).
4. Pop the value \texttt{v128.const c1} from the stack.
5. Let \( j^* \) be the sequence \( \texttt{lanes}_{8x16}(c_1) \).
6. Let \( c^* \) be the concatenation of the two sequences \( j^* 0^{240} \)
7. Let \( c' \) be the result of \( \texttt{lanes}_{8x16}^{-1}(c^*[0] \ldots c^*[15]) \).
8. Push the value \texttt{v128.const c'} onto the stack.

\( \texttt{(v128.const c1) (v128.const c2) v128.swizzle} \rightarrow \texttt{(v128.const c')} \)

\( \begin{align*}
& \texttt{if } i^* = \texttt{lanes}_{8x16}(c_2) \\
& \land c^* = \texttt{lanes}_{8x16}(c_1) 0^{240} \\
& \land c' = \texttt{lanes}_{8x16}^{-1}(c^*[0] \ldots c^*[15]) 
\end{align*} \)

\texttt{i8x16.shuffle x^*}

1. Assert: due to \textit{validation}, two values of \texttt{value type v128} are on the top of the stack.
2. Assert: due to \textit{validation}, for all \( x_i \) in \( x^* \) it holds that \( x_i < 32 \).
3. Pop the value \texttt{v128.const c2} from the stack.
4. Let \( i^*_2 \) be the sequence \( \texttt{lanes}_{8x16}(c_2) \).
5. Pop the value \texttt{v128.const c1} from the stack.
6. Let \( i^*_1 \) be the sequence \( \texttt{lanes}_{8x16}(c_1) \).
7. Let \( i^* \) be the concatenation of the two sequences \( i^*_1 i^*_2 \).
8. Let \( c \) be the result of \( \texttt{lanes}_{8x16}^{-1}(i^*[0] \ldots i^*[15]) \).
9. Push the value `v128.const c` onto the stack.
   \[(v128.const c_1) (v128.const c_2) v128.shuffle x^* \implies (v128.const c)
   \]
   \[(\text{if } i^* = \text{lanes}_{8x16}(c_1) \text{ lanes}_{8x16}(c_2)
     \wedge c = \text{lanes}_{8x16}^{-1}(i^*[x^*[0]] \ldots i^*[x^*[15]]))
   \]

`shape.splat`

1. Let \( t \) be the type \( \text{unpacked}(\text{shape}) \).
2. Assert: due to validation, a value of value type \( t \) is on the top of the stack.
3. Pop the value \( t.\text{const} c_1 \) from the stack.
4. Let \( N \) be the integer \( \text{dim}(\text{shape}) \).
5. Let \( c \) be the result of \( \text{lanes}_{\text{shape}}^{-1}(c_1^N) \).
6. Push the value `v128.const c` to the stack.
   \[(t.\text{const} c_1) \text{shape.splat} \implies (v128.const c) \quad (\text{if } t = \text{unpacked}(\text{shape}) \wedge c = \text{lanes}_{\text{shape}}^{-1}(c_1^{\text{dim}(\text{shape})}))
   \]

`t_{1\times N}.\text{extract_lane}_{sx^*} x`

1. Assert: due to validation, \( x < N \).
2. Assert: due to validation, a value of value type \( v128 \) is on the top of the stack.
3. Pop the value `v128.const c_1` from the stack.
4. Let \( i^* \) be the sequence \( \text{lanes}_{1\times N}(c_1) \).
5. Let \( t_2 \) be the type \( \text{unpacked}(t_{1\times N}) \).
6. Let \( c_2 \) be the result of computing \( \text{extend}_{t_{1\times N}}^{sx^*}(i^*[x]) \).
7. Push the value \( t_2.\text{const} c_2 \) to the stack.
   \[(v128.const c_1) t_{1\times N}.\text{extract_lane} x \implies (t_2.\text{const} c_2)
   \]
   \[(\text{if } t_2 = \text{unpacked}(t_{1\times N})
     \wedge c_2 = \text{extend}_{t_{1\times N}}^{sx^*}(\text{lanes}_{1\times N}(c_1)[x])
   \]

`shape.replace_lane x`

1. Assert: due to validation, \( x < \text{dim}(\text{shape}) \).
2. Let \( t_1 \) be the type \( \text{unpacked}(\text{shape}) \).
3. Assert: due to validation, a value of value type \( t_1 \) is on the top of the stack.
4. Pop the value `t_1.\text{const} c_1` from the stack.
5. Assert: due to validation, a value of value type \( v128 \) is on the top of the stack.
6. Pop the value `v128.const c_2` from the stack.
7. Let \( i^* \) be the sequence \( \text{lanes}_{\text{shape}}(c_2) \).
8. Let \( c \) be the result of computing \( \text{lanes}_{\text{shape}}^{-1}(i^*[x] = c_1) \)
   \[(t_1.\text{const} c_1) (v128.const c_2) \text{shape.replace_lane} x \implies (v128.const c)
   \]
   \[(\text{if } i^* = \text{lanes}_{\text{shape}}(c_2)
     \wedge c = \text{lanes}_{\text{shape}}^{-1}(i^*[x] = c_1)
   \]
shape.vunop

1. Assert: due to validation, a value of value type v128 is on the top of the stack.
2. Pop the value v128.const c1 from the stack.
3. Let c be the result of computing vunopshape(c1).
4. Push the value v128.const c to the stack.

\[(v128.\text{const } c1) \ v128.\text{vunop} \leftrightarrow (v128.\text{const } c) \quad \text{if } c = \text{vunopshape}(c1)\]

shape.vbinop

1. Assert: due to validation, two values of value type v128 are on the top of the stack.
2. Pop the value v128.const c2 from the stack.
3. Pop the value v128.const c1 from the stack.
4. If vbinopshape(c1, c2) is defined:
   a. Let c be a possible result of computing vbinopshape(c1, c2).
   b. Push the value v128.const c to the stack.
5. Else:
   a. Trap.

\[(v128.\text{const } c1) \ (v128.\text{const } c2) \ \text{shape.vbinop} \leftrightarrow (v128.\text{const } c) \quad \text{if } c \in \text{vbinopshape}(c1, c2)\]

\[(v128.\text{const } c1) \ (v128.\text{const } c2) \ \text{shape.vbinop} \leftrightarrow \text{trap} \quad \text{if } \text{vbinopshape}(c1, c2) = \{\}\]

txN.vrelop

1. Assert: due to validation, two values of value type v128 are on the top of the stack.
2. Pop the value v128.const c2 from the stack.
3. Pop the value v128.const c1 from the stack.
4. Let \(i^*\) be the sequence lanes\(_{txN}\)(c1).
5. Let \(j^*\) be the sequence lanes\(_{txN}\)(c2).
6. Let c be the result of computing lanes\(_{txN}\)(\(\text{vrelop}_{(i^*, j^*)}\)).
7. Push the value v128.const c to the stack.

\[(v128.\text{const } c1) \ (v128.\text{const } c2) \ \text{txN.vrelop} \leftrightarrow (v128.\text{const } c) \quad \text{if } c = \text{lanes}_{txN}^{-1}(\text{extend}^\text{v}_1, i(\text{vrelop}_{(i^*, j^*)}))\]

txN.vishiftop

1. Assert: due to validation, a value of value type i32 is on the top of the stack.
2. Pop the value i32.const s from the stack.
3. Assert: due to validation, a value of value type v128 is on the top of the stack.
4. Pop the value v128.const c1 from the stack.
5. Let \(i^*\) be the sequence lanes\(_{txN}\)(c1).
6. Let c be lanes\(_{txN}\)(\(\text{vishiftop}_{(i^*, s^N)}\)).
7. Push the value v128.const c to the stack.
\[(v128.\text{const } c_1) \ (i32.\text{const } s) \ txN.\text{vishiftop} \ \mapsto \ (v128.\text{const } c)\]
\[(\text{if } i^* = \text{lanes}_{txN}(c_1) \\land c = \text{lanes}_{txN}^{-1}(\text{vishiftop}_{txN}(i^*, s^N)))\]

**shape.all_true**

1. Assert: due to validation, a value of value type v128 is on the top of the stack.
2. Pop the value v128.\text{const } c_1 from the stack.
3. Let \(i_1^*\) be the sequence \(\text{lanes}_{\text{shape}}(c_1)\)
4. Let \(i\) be the result of computing \(\text{bool}(\wedge (i_1 \neq 0)^*)\).
5. Push the value i32.\text{const } i onto the stack.

\[(v128.\text{const } c_1) \ \text{shape.all_true} \ \mapsto \ (i32.\text{const } i)\]
\[(\text{if } i_1^* = \text{lanes}_{\text{shape}}(c) \\land i = \text{bool}(\wedge (i_1 \neq 0)^*)\)

**txN.bitmask**

1. Assert: due to validation, a value of value type v128 is on the top of the stack.
2. Pop the value v128.\text{const } c_1 from the stack.
3. Let \(i_1^N\) be the sequence \(\text{lanes}_{txN}(c)\).
4. Let \(B\) be the bit width \(|t|\) of value type \(t\).
5. Let \(i_2^N\) be the sequence as a result of computing \(\text{ilt}_S B(i_1^N, 0^N)\).
6. Let \(c\) be the integer \(\text{ibits}_{\text{32}}(i_2^N, 0^{32-N})\).
7. Push the value i32.\text{const } c onto the stack.

\[(v128.\text{const } c_1) \ txN.\text{bitmask} \ \mapsto \ (i32.\text{const } c)\]
\[(\text{if } c = \text{ibits}_{\text{32}}(\text{ilt}_S B(\text{lanes}_{\text{txN}}(c), 0^N)))\]

**t2xN.narrow_t1xM_sx**

1. Assert: due to syntax, \(N = 2 \cdot M\).
2. Assert: due to validation, two values of value type v128 are on the top of the stack.
3. Pop the value v128.\text{const } c_2 from the stack.
4. Let \(d_2^M\) be the result of computing \(\text{narrow}_{[t_1, t_2]}(\text{lanes}_{t_1 \times M}(c_2))\).
5. Pop the value v128.\text{const } c_1 from the stack.
6. Let \(d_1^M\) be the result of computing \(\text{narrow}_{[t_1, t_2]}(\text{lanes}_{t_1 \times M}(c_1))\).
7. Let \(c\) be the result of \(\text{lanes}_{t_{2xN}}^{-1}(d_1^M, d_2^M)\).
8. Push the value v128.\text{const } c onto the stack.

\[(v128.\text{const } c_1) \ (v128.\text{const } c_2) \ t2xN.\text{narrow}_t1xM_sx \ \mapsto \ (v128.\text{const } c)\]
\[(\text{if } d_1^M = \text{narrow}_{[t_1, t_2]}(\text{lanes}_{t_1 \times M}(c_1)) \\land d_2^M = \text{narrow}_{[t_1, t_2]}(\text{lanes}_{t_1 \times M}(c_2)) \\land c = \text{lanes}_{t_{2xN}}^{-1}(d_1^M, d_2^M)\]
\[ t_{2N}.vcvtop_{t+1}\times M_\text{sx} \]

1. Assert: due to \textit{validation}, a value of \textit{value type} \textsc{v128} is on the top of the stack.
2. Pop the value \textsc{v128}.const \( c_1 \) from the stack.
3. Let \( i^* \) be the sequence \textsc{lanes}_{t,1\times M}(c_1).
4. Let \( c \) be the result of computing \( \textsc{lanes}^{-1}_{t,2\times N}(\textsc{vcvtop}_{t,1\times M}(i^*)) \)
5. Push the value \textsc{v128}.const \( c \) onto the stack.

\[ (\textsc{v128}.const \; c_1) \; t_{2N}.vcvtop_{t+1}\times M_\text{sx} \; \leftrightarrow \; (\textsc{v128}.const \; c) \]

\[ \text{if } c = \textsc{lanes}^{-1}_{t,2\times N}(\textsc{vcvtop}_{t,1\times M}(\textsc{lanes}_{t,1\times M}(c_1))) \]

\[ t_{2N}.vcvtop_{\text{half}_{t+1}\times M_\text{sx}} \]

1. Assert: due to \textit{validation}, a value of \textit{value type} \textsc{v128} is on the top of the stack.
2. Pop the value \textsc{v128}.const \( c_1 \) from the stack.
3. If \textit{half} is low, then:
   a. Let \( i^* \) be the sequence \textsc{lanes}_{t,1\times M}(c_1)[0 : N].
4. Else:
   a. Let \( i^* \) be the sequence \textsc{lanes}_{t,1\times M}(c_1)[N : N].
5. Let \( j^* \) be the result of computing \( \textsc{vcvtop}^t_{t,1\times |t_2|}(i^*) \).
6. Let \( c \) be the result of computing \( \textsc{lanes}^{-1}_{t,2\times N}(j^*) \).
7. Push the value \textsc{v128}.const \( c \) onto the stack.

\[ (\textsc{v128}.const \; c_1) \; t_{2N}.vcvtop_{\text{half}_{t+1}\times M_\text{sx}} \; \leftrightarrow \; (\textsc{v128}.const \; c) \]

\[ \text{if } c = \textsc{lanes}^{-1}_{t,2\times N}(\textsc{vcvtop}^t_{t,1\times |t_2|}(\textsc{lanes}_{t,1\times M}(c_1)[\text{half}(0, N) : N])) \]

where:

\[
\begin{align*}
\text{low}(x, y) & = x \\
\text{high}(x, y) & = y
\end{align*}
\]

\[ t_{2N}.vcvtop_{t+1\times M_\text{sx}_\text{zero}} \]

1. Assert: due to \textit{validation}, a value of \textit{value type} \textsc{v128} is on the top of the stack.
2. Pop the value \textsc{v128}.const \( c_1 \) from the stack.
3. Let \( i^* \) be the sequence \textsc{lanes}_{t,1\times M}(c_1).
4. Let \( j^* \) be the result of computing \( \textsc{vcvtop}^t_{t,1\times |t_2|}(i^*) \) concatenated with the vector \( 0^M \).
5. Let \( c \) be the result of computing \( \textsc{lanes}^{-1}_{t,2\times N}(j^*) \).
6. Push the value \textsc{v128}.const \( c \) onto the stack.

\[ (\textsc{v128}.const \; c_1) \; t_{2N}.vcvtop_{t+1\times M_\text{sx}_\text{zero}} \; \leftrightarrow \; (\textsc{v128}.const \; c) \]

\[ \text{if } c = \textsc{lanes}^{-1}_{t,2\times N}(\textsc{vcvtop}^t_{t,1\times |t_2|}(\textsc{lanes}_{t,1\times M}(c_1)) \; 0^M) \]
i32x4.dot_i16x8.s

1. Assert: due to validation, two values of value type v128 is on the top of the stack.
2. Pop the value v128.const c₂ from the stack.
3. Pop the value v128.const c₁ from the stack.
4. Let \((i₁ \ i₂)^*\) be the result of computing \(\text{imul}_{32}(\text{extend}^s_{16,32}(\text{lanes}_{i16x8}(c₁)), \text{extend}^s_{16,32}(\text{lanes}_{i16x8}(c₂)))\)
5. Let \(j^*\) be the result of computing \(\text{iadd}_{32}(i₁, i₂)^*\).
6. Let \(c\) be the result of computing \(\text{lanes}_{i32x4}^{-1}(j^*)\).
7. Push the value v128.const \(c\) onto the stack.

\[
(v128.\text{const} \ c₁) \ (v128.\text{const} \ c₂) \ i32x4.\text{dot}_i16x8.s \ \leftrightarrow \ (v128.\text{const} \ c) \\
\text{if} \ (i₁ \ i₂)^* = \text{imul}_{32}(\text{extend}^s_{16,32}(\text{lanes}_{i16x8}(c₁)), \text{extend}^s_{16,32}(\text{lanes}_{i16x8}(c₂))) \\
\land \ j^* = \text{iadd}_{32}(i₁, i₂)^* \\
\land \ c = \text{lanes}_{i32x4}^{-1}(j^*)
\]

\(t₂xN.\text{extmul}_\text{half}_t\_M\_sx\)

1. Assert: due to validation, two values of value type v128 is on the top of the stack.
2. Pop the value v128.const c₂ from the stack.
3. Pop the value v128.const c₁ from the stack.
4. If half is low, then:
   a. Let \(i^*\) be the sequence \(\text{lanes}_{t×M}(c₁)[0 : N]\).
   b. Let \(j^*\) be the sequence \(\text{lanes}_{t×M}(c₂)[0 : N]\).
5. Else:
   a. Let \(i^*\) be the sequence \(\text{lanes}_{t×M}(c₁)[N : N]\).
   b. Let \(j^*\) be the sequence \(\text{lanes}_{t×M}(c₂)[N : N]\).
6. Let \(c\) be the result of computing \(\text{lanes}_{t×2N}^{-1}(\text{imul}_{t×2N}^{sx}(\text{extend}^{sx}_{[t₁, t₂]}(i^*), \text{extend}^{sx}_{[t₁, t₂]}(j^*))\))
7. Push the value v128.const \(c\) onto the stack.

\[
(v128.\text{const} \ c₁) \ (v128.\text{const} \ c₂) \ t₂xN.\text{extmul}_\text{half}_t\_M\_sx \ \leftrightarrow \ (v128.\text{const} \ c) \\
\text{if} \ i^* = \text{lanes}_{t×M}(c₁)[\text{half}(0, N) : N] \\
\land \ j^* = \text{lanes}_{t×M}(c₂)[\text{half}(0, N) : N] \\
\land \ c = \text{lanes}_{t×2N}^{-1}(\text{imul}_{t×2N}^{sx}(\text{extend}^{sx}_{[t₁, t₂]}(i^*), \text{extend}^{sx}_{[t₁, t₂]}(j^*))\))
\]

where:

\[
\text{low}(x, y) = x \\
\text{high}(x, y) = y
\]

\(t₂xN.\text{extadd}_\text{pairwise}_t\_M\_sx\)

1. Assert: due to validation, a value of value type v128 is on the top of the stack.
2. Pop the value v128.const \(c₁\) from the stack.
3. Let \((i₁ \ i₂)^*\) be the sequence \(\text{extend}^{sx}_{[t₁, t₂]}(\text{lanes}_{t×M}(c₁))\).
4. Let \(j^*\) be the result of computing \(\text{iadd}_N(i₁, i₂)^*\).
5. Let \(c\) be the result of computing \(\text{lanes}_{t×2N}^{-1}(j^*)\).
6. Push the value v128.const \(c\) to the stack.
(v128.const c₁) ∈ \text{1xN} \text{.extadd_pairwise}_{t₁xM \_sx} \leftrightarrow (v128.const c)

\begin{align*}
& \text{(if } (i₁, i₂) = \text{extend}_{t₁xM \_sx}⁻(\text{lanes}_{t₁xM}(c₁)) \wedge j₁ = \text{iadd}_{N}(i₁, i₂) \wedge c = \text{lanes}_{t₁xN}(j₁) \text{)}
\end{align*}

4.4.4 Parametric Instructions

\text{drop}

1. Assert: due to \textit{validation}, a value is on the top of the stack.
2. Pop the value \textit{val} from the stack.

\text{val} \text{ drop} \leftrightarrow \epsilon

\text{select} (t^*)

1. Assert: due to \textit{validation}, a value of \textit{value type} \text{i32} is on the top of the stack.
2. Pop the value \text{i32\.const} \textit{c} from the stack.
3. Assert: due to \textit{validation}, two more values (of the same \textit{value type}) are on the top of the stack.
4. Pop the value \textit{val}_2 from the stack.
5. Pop the value \textit{val}_1 from the stack.
6. If \textit{c} is not 0, then:
   a. Push the value \textit{val}_1 back to the stack.
7. Else:
   a. Push the value \textit{val}_2 back to the stack.

\text{val}_1 \text{ val}_2 (\text{i32\.const} \textit{c}) \text{ select} t' \leftrightarrow \text{val}_1 \quad (\text{if } c \neq 0)
\text{val}_1 \text{ val}_2 (\text{i32\.const} \textit{c}) \text{ select} t' \leftrightarrow \text{val}_2 \quad (\text{if } c = 0)

\textbf{Note:} In future versions of WebAssembly, \textit{select} may allow more than one value per choice.

4.4.5 Variable Instructions

\text{local}.\text{get} \text{ } x

1. Let \textit{F} be the \textit{current frame}.
3. Let \textit{val} be the value \textit{F}.\text{locals}[x].
4. Push the value \textit{val} to the stack.

\textit{F}; (\text{local}.\text{get} \text{ } x) \leftrightarrow \textit{F}; \text{val} \quad (\text{if } \textit{F}.\text{locals}[x] = \textit{val})
local.set $x$

1. Let $F$ be the current frame.
2. Assert: due to validation, $F$.locals[$x$] exists.
3. Assert: due to validation, a value is on the top of the stack.
4. Pop the value $val$ from the stack.
5. Replace $F$.locals[$x$] with the value $val$.

\[
F; \text{val} \,(\text{local.set } x) \leftrightarrow F'; \epsilon \quad \text{(if } F' = F \text{ with } \text{locals}[x] = val)\]

local.tee $x$

1. Assert: due to validation, a value is on the top of the stack.
2. Pop the value $val$ from the stack.
3. Push the value $val$ to the stack.
4. Push the value $val$ to the stack.
5. Execute the instruction (local.set $x$).

\[
\text{val} \,(\text{local.tee } x) \leftrightarrow \text{val} \,(\text{local.set } x)\]

global.get $x$

1. Let $F$ be the current frame.
3. Let $a$ be the global address $F$.module.globaladdrs[$x$].
5. Let $glob$ be the global instance $S$.globals[$a$].
6. Let $val$ be the value $glob$.value.
7. Push the value $val$ to the stack.

\[
S; F; \,(\text{global.get } x) \leftrightarrow S; F; \text{val} \\
\text{(if } S.\text{globals}[F.\text{module.globaladdrs}[x]].\text{value} = val)\]

global.set $x$

1. Let $F$ be the current frame.
3. Let $a$ be the global address $F$.module.globaladdrs[$x$].
5. Let $glob$ be the global instance $S$.globals[$a$].
6. Assert: due to validation, a value is on the top of the stack.
7. Pop the value $val$ from the stack.
8. Replace $glob$.value with the value $val$. 
4.4.6 Table Instructions

table.get \(x\)

1. Let \(F\) be the current frame.
2. Assert: due to validation, \(F\).module.tableaddrs[\(x\)] exists.
3. Let \(a\) be the table address \(F\).module.tableaddrs[\(x\)].
4. Assert: due to validation, \(S\).tables[\(a\)] exists.
5. Let \(tab\) be the table instance \(S\).tables[\(a\)].
6. Assert: due to validation, a value of value type i32 is on the top of the stack.
7. Pop the value i32.const \(i\) from the stack.
8. If \(i\) is not smaller than the length of tab.elem, then:
   a. Trap.
9. Let \(val\) be the value tab.elem[\(i\)].
10. Push the value \(val\) to the stack.

\[
S; F; (i32\text{-}const \ i) \ (table\text{-}get \ x) \iff S; F; val \\
\text{(if } S\.tables[F\.module.tableaddrs[x]].elem[\(i\)] = val) \\
S; F; (i32\text{-}const \ i) \ (table\text{-}get \ x) \iff S; F; \text{trap} \\
\text{(otherwise)}
\]

table.set \(x\)

1. Let \(F\) be the current frame.
2. Assert: due to validation, \(F\).module.tableaddrs[\(x\)] exists.
3. Let \(a\) be the table address \(F\).module.tableaddrs[\(x\)].
4. Assert: due to validation, \(S\).tables[\(a\)] exists.
5. Let \(tab\) be the table instance \(S\).tables[\(a\)].
6. Assert: due to validation, a reference value is on the top of the stack.
7. Pop the value \(val\) from the stack.
8. Assert: due to validation, a value of value type i32 is on the top of the stack.
9. Pop the value i32.const \(i\) from the stack.
10. If \(i\) is not smaller than the length of tab.elem, then:
    a. Trap.
11. Replace the element tab.elem[\(i\)] with \(val\).
gars;
(F; (i32.const i) val (table.set x) \rightarrow S'; F; ε
(if \( S' = S \) with tables[F.module.tableaddrs[x]].elem[i] = val)
S; F; (i32.const i) val (table.set x) \rightarrow S; F; trap
(otherwise)

\( S \), \( F \); (i32.const i) val (table.set x) \rightarrow S'; F; i

\( S \), \( F \); (i32.const i) val (table.set x) \rightarrow S; F; trap

1. Let \( F \) be the current frame.
2. Assert: due to validation, \( F.module.tableaddrs[x] \) exists.
3. Let \( a \) be the table address \( F.module.tableaddrs[x] \).
4. Assert: due to validation, \( S.tables[a] \) exists.
5. Let \( tab \) be the table instance \( S.tables[a] \).
6. Let \( sz \) be the length of \( tab.elem \).
7. Push the value \( i32.const sz \) to the stack.

\( S \); F; table.size x \rightarrow S; F; (i32.const sz)
(if \( |S.tables[F.module.tableaddrs[x]].elem| = sz \))

\( table.size x \)

1. Let \( F \) be the current frame.
2. Assert: due to validation, \( F.module.tableaddrs[x] \) exists.
3. Let \( a \) be the table address \( F.module.tableaddrs[x] \).
4. Assert: due to validation, \( S.tables[a] \) exists.
5. Let \( tab \) be the table instance \( S.tables[a] \).
6. Let \( sz \) be the length of \( S.tables[a] \).
7. Assert: due to validation, a value of value type \( i32 \) is on the top of the stack.
8. Pop the value \( i32.const n \) from the stack.
9. Assert: due to validation, a reference value is on the top of the stack.
10. Pop the value \( val \) from the stack.
11. Either, try growing table by \( n \) entries with initialization value \( val \):
   a. If it succeeds, push the value \( i32.const sz \) to the stack.
   b. Else, push the value \( i32.const (−1) \) to the stack.
12. Or, push the value \( i32.const (−1) \) to the stack.

\( S; F; val (i32.const n) table.grow x \rightarrow S'; F; (i32.const sz) \)
(if \( F.module.tableaddrs[x] = a \)
\land sz = |S.tables[a].elem|
\land S' = S with tables[a] = growtable(S.tables[a], n, val))
\( S; F; (i32.const n) table.grow x \rightarrow S; F; (i32.const −1) \)

\( table.grow x \)

1. Let \( F \) be the current frame.
2. Assert: due to validation, \( F.module.tableaddrs[x] \) exists.
3. Let \( a \) be the table address \( F.module.tableaddrs[x] \).
4. Assert: due to validation, \( S.tables[a] \) exists.
5. Let \( tab \) be the table instance \( S.tables[a] \).
6. Let \( sz \) be the length of \( S.tables[a] \).
7. Assert: due to validation, a value of value type \( i32 \) is on the top of the stack.
8. Pop the value \( i32.const n \) from the stack.
9. Assert: due to validation, a reference value is on the top of the stack.
10. Pop the value \( val \) from the stack.
11. Either, try growing table by \( n \) entries with initialization value \( val \):
   a. If it succeeds, push the value \( i32.const sz \) to the stack.
   b. Else, push the value \( i32.const (−1) \) to the stack.
12. Or, push the value \( i32.const (−1) \) to the stack.

\( S; F; val (i32.const n) table.grow x \rightarrow S'; F; (i32.const sz) \)
(if \( F.module.tableaddrs[x] = a \)
\land sz = |S.tables[a].elem|
\land S' = S with tables[a] = growtable(S.tables[a], n, val))
\( S; F; (i32.const n) table.grow x \rightarrow S; F; (i32.const −1) \)

Note: The table.grow instruction is non-deterministic. It may either succeed, returning the old table size \( sz \), or fail, returning \( −1 \). Failure must occur if the referenced table instance has a maximum size defined that would be exceeded. However, failure can occur in other cases as well. In practice, the choice depends on the resources available to the embedder.
table.fill \( x \)

1. Let \( F \) be the current frame.
2. Assert: due to validation, \( F\text{.module.tableaddrs}[x] \) exists.
3. Let \( ta \) be the table address \( F\text{.module.tableaddrs}[x] \).
4. Assert: due to validation, \( S\text{.tables}[ta] \) exists.
5. Let \( tab \) be the table instance \( S\text{.tables}[ta] \).
6. Assert: due to validation, a value of value type i32 is on the top of the stack.
7. Pop the value \( \text{i32.const } n \) from the stack.
8. Assert: due to validation, a reference value is on the top of the stack.
9. Pop the value \( \text{val} \) from the stack.
10. Assert: due to validation, a value of value type i32 is on the top of the stack.
11. Pop the value \( \text{i32.const } i \) from the stack.
12. If \( i + n \) is larger than the length of \( \text{tab.elem} \), then:
   a. Trap.
12. If \( n \) is 0, then:
   a. Return.
13. Push the value \( \text{i32.const } i \) to the stack.
14. Push the value \( \text{val} \) to the stack.
15. Execute the instruction \( \text{table.set } x \).
16. Push the value \( \text{i32.const } (i + 1) \) to the stack.
17. Push the value \( \text{val} \) to the stack.
18. Push the value \( \text{i32.const } (n - 1) \) to the stack.
19. Execute the instruction \( \text{table.fill } x \).

\[
\begin{align*}
\text{S; F; (i32.const } i \text{) val (i32.const } n \text{) (table.fill } x \text{) } & \iff \text{S; F; trap (if } i + n > |S\text{.tables}[F\text{.module.tableaddrs}[x]].\text{elem}|) \\
\text{S; F; (i32.const } i \text{) val (i32.const } 0 \text{) (table.fill } x \text{) } & \iff \text{S; F; } \epsilon \\
\text{otherwise} \\
\text{S; F; (i32.const } i \text{) val (i32.const } n + 1 \text{) (table.fill } x \text{) } & \iff \text{S; F; (i32.const } i \text{) val (table.set } x \text{)} \\
\text{(i32.const } i + 1 \text{) val (i32.const } n \text{) (table.fill } x \text{) } & \iff \text{(otherwise)}
\end{align*}
\]

4.4. Instructions

\section*{table.copy \( x \ y \)}

1. Let \( F \) be the current frame.
2. Assert: due to validation, \( F\text{.module.tableaddrs}[x] \) exists.
3. Let \( ta_x \) be the table address \( F\text{.module.tableaddrs}[x] \).
4. Assert: due to validation, \( S\text{.tables}[ta_x] \) exists.
5. Let \( tab_x \) be the table instance \( S\text{.tables}[ta_x] \).
6. Assert: due to validation, \( F\text{.module.tableaddrs}[y] \) exists.
7. Let \( ta_y \) be the table address \( F\text{.module.tableaddrs}[y] \).

9. Let $tab_y$ be the table instance $S.tables[ta_y]$.

10. Assert: due to validation, a value of value type i32 is on the top of the stack.

11. Pop the value i32.const $n$ from the stack.

12. Assert: due to validation, a value of value type i32 is on the top of the stack.

13. Pop the value i32.const $s$ from the stack.

14. Assert: due to validation, a value of value type i32 is on the top of the stack.

15. Pop the value i32.const $d$ from the stack.

16. If $s + n$ is larger than the length of $tab_y.elem$ or $d + n$ is larger than the length of $tab_x.elem$, then:
   a. Trap.

17. If $n = 0$, then:
   a. Return.

18. If $d \leq s$, then:
   a. Push the value i32.const $d$ to the stack.
   b. Push the value i32.const $s$ to the stack.
   c. Execute the instruction table.get $y$.
   d. Execute the instruction table.set $x$.
   e. Assert: due to the earlier check against the table size, $d + 1 < 2^{32}$.
   f. Push the value i32.const $(d + 1)$ to the stack.
   g. Assert: due to the earlier check against the table size, $s + 1 < 2^{32}$.
   h. Push the value i32.const $(s + 1)$ to the stack.

19. Else:
   a. Assert: due to the earlier check against the table size, $d + n - 1 < 2^{32}$.
   b. Push the value i32.const $(d + n - 1)$ to the stack.
   c. Assert: due to the earlier check against the table size, $s + n - 1 < 2^{32}$.
   d. Push the value i32.const $(s + n - 1)$ to the stack.
   c. Execute the instruction table.get $y$.
   f. Execute the instruction table.set $x$.
   g. Push the value i32.const $d$ to the stack.
   h. Push the value i32.const $s$ to the stack.

20. Push the value i32.const $(n - 1)$ to the stack.

21. Execute the instruction table.copy $x$ $y$. 

---

Chapter 4. Execution
4.4. Instructions

1. Let $F$ be the current frame.
3. Let $ta$ be the table address $F$.module.tableaddr[x].
5. Let $tab$ be the table instance $S.tables[ta]$.
7. Let $ea$ be the element address $F$.module.elemaddr[y].
10. Assert: due to validation, a value of value type i32 is on the top of the stack.
11. Pop the value i32 const $n$ from the stack.
12. Assert: due to validation, a value of value type i32 is on the top of the stack.
13. Pop the value i32 const $s$ from the stack.
14. Assert: due to validation, a value of value type i32 is on the top of the stack.
15. Pop the value i32 const $d$ from the stack.
16. If $s + n$ is larger than the length of elem.elem or $d + n$ is larger than the length of tab.elem, then:
   a. Trap.
17. If $n = 0$, then:
   a. Return.
18. Let $val$ be the reference value elem.elem[s].
19. Push the value i32 const $d$ to the stack.
20. Push the value $val$ to the stack.
21. Execute the instruction table.set $x$.
22. Assert: due to the earlier check against the table size, $d + 1 < 2^{32}$.
23. Push the value i32 const $(d + 1)$ to the stack.
24. Assert: due to the earlier check against the segment size, $s + 1 < 2^{32}$.
25. Push the value i32 const $(s + 1)$ to the stack.
26. Push the value \( \text{i32.const} (n - 1) \) to the stack.

27. Execute the instruction \( \text{table.init} \ x \ y \).

\[
S; F; (\text{i32.const } d) \ (\text{i32.const } s) \ (\text{i32.const } n) \ (\text{table.init } x \ y) \quad \leftrightarrow \quad S; F; \text{trap}
\]
\[
\text{if } s + n > |S.\text{elems}[F.\text{module}.\text{elemaddr}[y]].\text{elem}|
\]
\[
\text{∨ } d + n > |S.\text{tables}[F.\text{module}.\text{tableaddr}[x]].\text{elem}|
\]

\[
S; F; (\text{i32.const } d) \ (\text{i32.const } s) \ (\text{i32.const } 0) \ (\text{table.init } x \ y) \quad \leftrightarrow \quad S; F; \epsilon
\]

\[
\text{otherwise}
\]

\[
S; F; (\text{i32.const } d) \ (\text{i32.const } s) \ (\text{i32.const } n + 1) \ (\text{table.init } x \ y) \quad \leftrightarrow
\]

\[
S; F; (\text{i32.const } d) \ \text{val} \ (\text{table.set } x)
\]

\[
(i32.\text{const } d + 1) \ (\text{i32.const } s + 1) \ (\text{i32.const } n) \ (\text{table.init } x \ y)
\]

\[
\text{otherwise, if } \text{val} = S.\text{elems}[F.\text{module}.\text{elemaddr}[y]].\text{elem}[s]
\]

\[
\text{elem.drop } x
\]

1. Let \( F \) be the current frame.

2. Assert: due to validation, \( F.\text{module}.\text{elemaddr}[x] \) exists.

3. Let \( a \) be the element address \( F.\text{module}.\text{elemaddr}[x] \).

4. Assert: due to validation, \( S.\text{elems}[a] \) exists.

5. Replace \( S.\text{elems}[a] \) with the element instance \( \{ \text{elem} \} \).

\[
S; F; (\text{elem.drop } x) \quad \leftrightarrow \quad S'; F; \epsilon
\]

\[
\text{if } S' = S \text{ with } \text{elems}[F.\text{module}.\text{elemaddr}[x]] = \{ \text{elem} \}
\]

### 4.4.7 Memory Instructions

**Note:** The alignment \( \text{memory.align} \) in load and store instructions does not affect the semantics. It is an indication that the offset \( ea \) at which the memory is accessed is intended to satisfy the property \( ea \mod 2^\text{memory.align} = 0 \). A WebAssembly implementation can use this hint to optimize for the intended use. Unaligned access violating that property is still allowed and must succeed regardless of the annotation. However, it may be substantially slower on some hardware.

**t.load memory and t.load.Nsz memory**

1. Let \( F \) be the current frame.

2. Assert: due to validation, \( F.\text{module}.\text{memaddrs}[0] \) exists.

3. Let \( a \) be the memory address \( F.\text{module}.\text{memaddrs}[0] \).

4. Assert: due to validation, \( S.\text{mems}[a] \) exists.

5. Let \( \text{mem} \) be the memory instance \( S.\text{mems}[a] \).

6. Assert: due to validation, a value of value type \( \text{i32} \) is on the top of the stack.

7. Pop the value \( \text{i32.const } i \) from the stack.

8. Let \( \text{ea} \) be the integer \( i + \text{memory.offset} \).

9. If \( N \) is not part of the instruction, then:
   a. Let \( N \) be the bit width \( |t| \) of number type \( t \).
10. If \( ea + N/8 \) is larger than the length of \( \text{mem.data} \), then:
   a. Trap.

11. Let \( b^* \) be the byte sequence \( \text{mem.data}[ea : N/8] \).

12. If \( N \) and \( sx \) are part of the instruction, then:
   a. Let \( n \) be the integer for which \( \text{bytes}_{iN}(n) = b^* \).
   b. Let \( c \) be the result of computing \( \text{extend}^{sx}_{N,M}(n) \).

13. Else:
   a. Let \( c \) be the constant for which \( \text{bytes}_t(c) = b^* \).

14. Push the value \( t.\text{const} \) to the stack.

\[
S; F; (\text{i32.const } i) \cdot (t.\text{load memarg}) \rightarrow S; F; (t.\text{const } c)
\]
   (if \( ea = i + \text{memarg.offset} \)
   \& \( ea + |t|/8 \leq |S.\text{mems}[F.\text{module}.\text{memaddrs}[0]].\text{data}| \)
   \& \( \text{bytes}_t(c) = S.\text{mems}[F.\text{module}.\text{memaddrs}[0]].\text{data}[ea : |t|/8] \))

\[
S; F; (\text{i32.const } i) \cdot (t.\text{load}(N \_sx \text{memarg})) \rightarrow S; F; (t.\text{const extend}^{sx}_{N,M}(n))
\]
   (if \( ea = i + \text{memarg.offset} \)
   \& \( ea + N/8 \leq |S.\text{mems}[F.\text{module}.\text{memaddrs}[0]].\text{data}| \)
   \& \( \text{bytes}_{iN}(n) = S.\text{mems}[F.\text{module}.\text{memaddrs}[0]].\text{data}[ea : N/8] \))

\[
S; F; (\text{i32.const } k) \cdot (t.\text{load}(N \_sx)_2 \text{memarg}) \rightarrow S; F; \text{trap}
\]

(otherwise)

\section*{v128.loadMxN\_sx memarg}

1. Let \( F \) be the current frame.
2. Assert: due to validation, \( F.\text{module}.\text{memaddrs}[0] \) exists.
3. Let \( a \) be the memory address \( F.\text{module}.\text{memaddrs}[0] \).
4. Assert: due to validation, \( S.\text{mems}[a] \) exists.
5. Let \( \text{mem} \) be the memory instance \( S.\text{mems}[a] \).
6. Assert: due to validation, a value of value type \text{i32} is on the top of the stack.
7. Pop the value \text{i32.const } i \) from the stack.
8. Let \( ea \) be the integer \( i + \text{memarg.offset} \).
9. If \( ea + M \cdot N/8 \) is larger than the length of \( \text{mem.data} \), then:
   a. Trap.
10. Let \( b^* \) be the byte sequence \( \text{mem.data}[ea : M \cdot N/8] \).
11. Let \( n_k \) be the integer for which \( \text{bytes}_{iM}(n_k) = b^*[k \cdot M/8 : M/8] \).
12. Let \( W \) be the integer \( M \cdot 2 \).
13. Let \( n_k \) be the result of \( \text{extend}^{sx}_{M,W}(n_k) \).
14. Let \( c \) be the result of computing \( \text{lanes}^{1}_{WxN}(n_0 \ldots n_{N-1}) \).
15. Push the value \( v128.\text{const } c \) to the stack.
10. Let \( S; F; (\text{i32.const } i) \text{ (v128.load} \_\text{sx memory}) \) → \( S; F; (\text{v128.const } c) \)
    (if \( ea = i + \text{memarg.offset} \)
     ∧ \( ea + M \cdot N/8 \leq |S.\text{mems}[F.\text{module}.\text{memaddrs}[0]].\text{data}| \)
     ∧ \( \text{bytes}_{iM}(m_k) = S.\text{mems}[F.\text{module}.\text{memaddrs}[0]].\text{data}[ea + k \cdot M/8 : M/8] \)
     ∧ \( W = M \cdot 2 \)
     ∧ \( c = \text{lanes}^{-1}_{NxL}(\text{extend}_{MF_W}^X(m_0) \ldots \text{extend}_{MF_W}^X(m_{N-1})) \))

11. Let \( S; F; (\text{i32.const } k) \text{ (v128.load} \_\text{sx memory}) \) → \( S; F; \text{trap} \)
    (otherwise)

#### v128.loadN_splat memory

1. Let \( F \) be the current frame.
2. Assert: due to validation, \( F.\text{module}.\text{memaddrs}[0] \) exists.
3. Let \( a \) be the memory address \( F.\text{module}.\text{memaddrs}[0] \).
4. Assert: due to validation, \( S.\text{mems}[a] \) exists.
5. Let \( \text{mem} \) be the memory instance \( S.\text{mems}[a] \).
6. Assert: due to validation, a value of value type \text{i32} is on the top of the stack.
7. Pop the value \text{i32.const } i \) from the stack.
8. Let \( ea \) be the integer \( i + \text{memarg.offset} \).
9. If \( ea + N/8 \) is larger than the length of \( \text{mem}.\text{data} \), then:
   a. Trap.
10. Let \( b^* \) be the byte sequence \( \text{mem}.\text{data}[ea : N/8] \).
11. Let \( n \) be the integer for which \( \text{bytes}_{iN}(n) = b^* \).
12. Let \( L \) be the integer \( 128/N \).
13. Let \( c \) be the result of computing \( \text{lanes}^{-1}_{Nxl}(n^L) \).
14. Push the value \text{v128.const } c \) to the stack.

#### v128.loadN_zero memory

1. Let \( F \) be the current frame.
2. Assert: due to validation, \( F.\text{module}.\text{memaddrs}[0] \) exists.
3. Let \( a \) be the memory address \( F.\text{module}.\text{memaddrs}[0] \).
4. Assert: due to validation, \( S.\text{mems}[a] \) exists.
5. Let \( \text{mem} \) be the memory instance \( S.\text{mems}[a] \).
6. Assert: due to validation, a value of value type \text{i32} is on the top of the stack.
7. Pop the value \text{i32.const } i \) from the stack.
8. Let \( ea \) be the integer \( i + \text{memarg.offset} \).
9. If $ea + N/8$ is larger than the length of `mem.data`, then:
   a. Trap.
10. Let $b^*$ be the byte sequence `mem.data[ea : N/8].`
11. Let $a$ be the integer for which $\text{bytes}_{iN}(n) = b^*$.
12. Let $c$ be the result of $\text{extend}^a_{N,128}(n)$.
13. Push the value `v128.const c` to the stack.

$$S; F; (\text{v128.load\text{\_}zero\text{\_}memory}) \quad \mapsto \quad S; F; (\text{v128.const c})$$
(tmp)  
\[
\text{if } ea = i + \text{mem.arg.offset} \\
\land ea + N/8 \leq |\text{F.module.memaddrs[0].data}| \\
\land \text{bytes}_{iN}(n) = \text{F.module.memaddrs[0].data}[ea : N/8] \\
\land c = \text{extend}^a_{N,128}(n)
\]

$v128.load\text{\_}lane\text{\_}memory x$

1. Let $F$ be the current frame.
2. Assert: due to validation, $F.module.memaddrs[0]$ exists.
3. Let $a$ be the memory address $F.module.memaddrs[0]$.
5. Let $mem$ be the memory instance $S.mems[a]$.
6. Assert: due to validation, a value of value type v128 is on the top of the stack.
7. Pop the value `v128.const v` from the stack.
8. Assert: due to validation, a value of value type i32 is on the top of the stack.
9. Pop the value `i32.const i` from the stack.
10. Let $ea$ be the integer $i + \text{mem.arg.offset}$.
11. If $ea + N/8$ is larger than the length of `mem.data`, then:
   a. Trap.
12. Let $b^*$ be the byte sequence `mem.data[ea : N/8].`
13. Let $r$ be the constant for which $\text{bytes}_{iN}(r) = b^*$.
15. Let $c$ be the result of computing $\text{lanes}^{-1}_{\text{N\_\_L}}(\text{lanes}_{\text{N\_\_L}}(v) \text{ with } [x] = r)$.
16. Push the value `v128.const c` to the stack.

$$S; F; (\text{v128.load\text{\_}lane\text{\_}memory x}) \quad \mapsto \quad S; F; (\text{v128.const c})$$
(tmp)  
\[
\text{if } ea = i + \text{mem.arg.offset} \\
\land ea + N/8 \leq |\text{F.module.memaddrs[0].data}| \\
\land \text{bytes}_{iN}(r) = \text{F.module.memaddrs[0].data}[ea : N/8] \\
\land L = 128/N \\
\land c = \text{lanes}^{-1}_{\text{N\_\_L}}(\text{lanes}_{\text{N\_\_L}}(v) \text{ with } [x] = r)
\]
1. Let $F$ be the *current frame*.
2. Assert: due to *validation*, $F$.module.memaddrs[0] exists.
3. Let $a$ be the *memory address* $F$.module.memaddrs[0].
5. Let $mem$ be the *memory instance* $S$.mems[a].
6. Assert: due to *validation*, a value of *value type* $t$ is on the top of the stack.
7. Pop the value $t$.const $c$ from the stack.
8. Assert: due to *validation*, a value of *value type* i32 is on the top of the stack.
9. Pop the value i32.const $i$ from the stack.
10. Let $ea$ be the integer $i + memory.offset$.
11. If $N$ is not part of the instruction, then:
    a. Let $N$ be the *bit width* $|t|$ of *number type* $t$.
12. If $ea + N/8$ is larger than the length of mem.data, then:
    a. Trap.
13. If $N$ is part of the instruction, then:
    a. Let $n$ be the result of computing $\text{wrap}_{|t|,N}(c)$.
    b. Let $b^*$ be the byte sequence bytes$_{i,N}(n)$.
14. Else:
    a. Let $b^*$ be the byte sequence bytes$_{i}(c)$.
15. Replace the bytes mem.data[ea : $N$/8] with $b^*$.

1. Let $F$ be the *current frame*.
2. Assert: due to *validation*, $F$.module.memaddrs[0] exists.
3. Let $a$ be the *memory address* $F$.module.memaddrs[0].
5. Let $mem$ be the *memory instance* $S$.mems[a].
6. Assert: due to *validation*, a value of *value type* v128 is on the top of the stack.
7. Pop the value v128.const $c$ from the stack.
8. Assert: due to validation, a value of value type i32 is on the top of the stack.

9. Pop the value i32.const \( i \) from the stack.

10. Let \( \textnormal{ea} \) be the integer \( i + \textnormal{memarg.offset} \).

11. If \( \textnormal{ea} + N/8 \) is larger than the length of \( \textnormal{mem.data} \), then:
    a. Trap.

12. Let \( L = 128/N \).

13. Let \( b^* \) be the byte sequence \( \text{bytes}_N(\text{lanes}_{N\times L}(c)[x]) \).

14. Replace the bytes \( \text{mem.data}[\text{ea} : N/8] \) with \( b^* \).

\[
\begin{align*}
S; F; (i32\text{.const } i) (v128\text{.const } c) (v128\text{.store}_N\text{.lane memarg } x) & \iff S'; F; c \\
& \text{if } \text{ea} = i + \text{memarg.offset} \\
& \land \text{ea} + N \leq |S.\text{mems}[F.\text{module}.\text{memaddrs}[0]].\text{data}| \\
& \land L = 128/N \\
& \land S' = S \text{ with } \text{mems}[F.\text{module}.\text{memaddrs}[0]].\text{data}[\text{ea} : N/8] = \text{bytes}_N(\text{lanes}_{N\times L}(c)[x]) \\
S; F; (i32\text{.const } k) (v128\text{.const } c) (v128\text{.store}_N\text{.lane memarg } x) & \iff S; F; \text{trap} \\
& \text{(otherwise)}
\end{align*}
\]

memory.size

1. Let \( F \) be the current frame.

2. Assert: due to validation, \( F.\text{module}.\text{memaddrs}[0] \) exists.

3. Let \( a \) be the memory address \( F.\text{module}.\text{memaddrs}[0] \).

4. Assert: due to validation, \( S.\text{mems}[a] \) exists.

5. Let \( \text{mem} \) be the memory instance \( S.\text{mems}[a] \).

6. Let \( \text{sz} \) be the length of \( \text{mem.data} \) divided by the page size.

7. Push the value \( i32\text{.const } \text{sz} \) to the stack.

\[
\begin{align*}
S; F; \text{memory.size} & \iff S; F; (i32\text{.const } \text{sz}) \\
& \text{(if } |S.\text{mems}[F.\text{module}.\text{memaddrs}[0]].\text{data}| = \text{sz} \cdot 64 \text{Ki})
\end{align*}
\]

memory.grow

1. Let \( F \) be the current frame.

2. Assert: due to validation, \( F.\text{module}.\text{memaddrs}[0] \) exists.

3. Let \( a \) be the memory address \( F.\text{module}.\text{memaddrs}[0] \).

4. Assert: due to validation, \( S.\text{mems}[a] \) exists.

5. Let \( \text{mem} \) be the memory instance \( S.\text{mems}[a] \).

6. Let \( \text{sz} \) be the length of \( S.\text{mems}[a] \) divided by the page size.

7. Assert: due to validation, a value of value type i32 is on the top of the stack.

8. Pop the value \( i32\text{.const } n \) from the stack.

9. Let \( \text{err} \) be the \( i32 \) value \( 2^{32} - 1 \), for which \( \text{signed}_{32}(\text{err}) \) is \(-1\).

10. Either, try growing \( \text{mem} \) by \( n \) pages:
    a. If it succeeds, push the value \( i32\text{.const } \text{sz} \) to the stack.
    b. Else, push the value \( i32\text{.const } \text{err} \) to the stack.
11. Or, push the value  \( \text{i32.\text{const}} \ err \) to the stack.

\[
S; F; (\text{i32.\text{const}} \ n) \ \text{memory.grow} \ \leftrightarrow \ S'; F; (\text{i32.\text{const}} \ sz)
\]
\[
\text{if } F.\text{module.memaddrs}[0] = a
\]
\[
\wedge \ sz = |S.\text{mems}[a].\text{data}|/64\ \text{Ki}
\]
\[
\wedge S' = S \text{ with mems}[a] = \text{growmem}(S.\text{mems}[a], n)
\]

\[
S; F; (\text{i32.\text{const}} \ n) \ \text{memory.grow} \ \leftrightarrow \ S; F; (\text{i32.\text{const}} \text{signed}_{32} (-1))
\]

**Note:** The memory.grow instruction is non-deterministic. It may either succeed, returning the old memory size \( sz \), or fail, returning \(-1\). Failure **must** occur if the referenced memory instance has a maximum size defined that would be exceeded. However, failure **can** occur in other cases as well. In practice, the choice depends on the resources available to the embedder.

---

**memory.fill**

1. Let \( F \) be the *current frame*.
2. Assert: due to validation, \( F.\text{module.memaddrs}[0] \) exists.
3. Let \( ma \) be the *memory address* \( F.\text{module.memaddrs}[0] \).
4. Assert: due to validation, \( S.\text{mems}[ma] \) exists.
5. Let \( mem \) be the *memory instance* \( S.\text{mems}[ma] \).
6. Assert: due to validation, a value of *value type* \( \text{i32} \) is on the top of the stack.
7. Pop the value \( \text{i32.\text{const}} \ n \) from the stack.
8. Assert: due to validation, a value of *value type* \( \text{i32} \) is on the top of the stack.
9. Pop the value \( \text{val} \) from the stack.
10. Pop the value \( \text{i32.\text{const}} \ d \) from the stack.
11. If \( d + n \) is larger than the length of \( \text{mem.data} \), then:
   a. Trap.
12. If \( n = 0 \), then:
   a. Return.
13. Push the value \( \text{i32.\text{const}} \ d \) to the stack.
14. Push the value \( \text{val} \) to the stack.
15. Execute the instruction \( \text{i32.store8} \ \{ \text{offset} \ 0, \text{align} \ 0 \} \).
16. Assert: due to the earlier check against the memory size, \( d + 1 < 2^{32} \).
17. Push the value \( \text{i32.\text{const}} \ (d + 1) \) to the stack.
18. Push the value \( \text{val} \) to the stack.
19. Push the value \( \text{i32.\text{const}} \ (n - 1) \) to the stack.
20. Execute the instruction memory.fill.
Let $F$ be the current frame.

2. Assert: due to validation, $F$.module.memaddrs[0] exists.

3. Let $ma$ be the memory address $F$.module.memaddrs[0].


5. Let $mem$ be the memory instance $S$.mems[$ma$].

6. Assert: due to validation, a value of value type $i32$ is on the top of the stack.

7. Pop the value $i32$.const $n$ from the stack.

8. Assert: due to validation, a value of value type $i32$ is on the top of the stack.

9. Pop the value $i32$.const $s$ from the stack.

10. Assert: due to validation, a value of value type $i32$ is on the top of the stack.

11. Pop the value $i32$.const $d$ from the stack.

12. If $s + n$ is larger than the length of $mem$.data or $d + n$ is larger than the length of $mem$.data, then:
   a. Trap.

13. If $n = 0$, then:
   a. Return.

14. If $d \leq s$, then:
   a. Push the value $i32$.const $d$ to the stack.
   b. Push the value $i32$.const $s$ to the stack.
   c. Execute the instruction $i32$.load8_u {offset 0, align 0}.
   d. Execute the instruction $i32$.store8 {offset 0, align 0}.
   e. Assert: due to the earlier check against the memory size, $d + 1 < 2^{32}$.
   f. Push the value $i32$.const $(d + 1)$ to the stack.
   g. Assert: due to the earlier check against the memory size, $s + 1 < 2^{32}$.
   h. Push the value $i32$.const $(s + 1)$ to the stack.

15. Else:
   a. Assert: due to the earlier check against the memory size, $d + n - 1 < 2^{32}$.
   b. Push the value $i32$.const $(d + n - 1)$ to the stack.
   c. Assert: due to the earlier check against the memory size, $s + n - 1 < 2^{32}$.
   d. Push the value $i32$.const $(s + n - 1)$ to the stack.
   e. Execute the instruction $i32$.load8_u {offset 0, align 0}.
f. Execute the instruction `i32.store8 { offset 0, align 0 }`.
g. Push the value `i32.const d` to the stack.
h. Push the value `i32.const s` to the stack.
16. Push the value `i32.const (n - 1)` to the stack.
17. Execute the instruction `memory.copy`.

```
S; F; (i32.const d) (i32.const s) (i32.const n) memory.copy \(\iff s + n > |S.mems[F.module.memaddrs[0]].data|
\vee d + n > |S.mems[F.module.memaddrs[0]].data| \) \(\Rightarrow S; F; \text{trap}
\)
S; F; (i32.const d) (i32.const s) (i32.const n) memory.copy \(\iff d > s \) \(\Rightarrow\)
S; F; (i32.const d) (i32.const s) (i32.const n + 1) memory.copy
S; F; (i32.const d + n - 1)
(i32.const s + n - 1) (i32.load8_u { offset 0, align 0 })
(i32.store8 { offset 0, align 0 })
(i32.const d) (i32.const s) (i32.const n) memory.copy
(otherwise, if \(d \leq s\))
```

memory.init \(x\)

1. Let \(F\) be the current frame.
2. Assert: due to validation, \(F.module.memaddrs[0]\) exists.
3. Let \(ma\) be the memory address \(F.module.memaddrs[0]\).
4. Assert: due to validation, \(S.mems[ma]\) exists.
5. Let \(mem\) be the memory instance \(S.mems[ma]\).
6. Assert: due to validation, \(F.module.dataaddrs[x]\) exists.
7. Let \(da\) be the data address \(F.module.dataaddrs[x]\).
8. Assert: due to validation, \(S.datas[da]\) exists.
9. Let \(data\) be the data instance \(S.datas[da]\).
10. Assert: due to validation, a value of value type i32 is on the top of the stack.
11. Pop the value `i32.const n` from the stack.
12. Assert: due to validation, a value of value type i32 is on the top of the stack.
13. Pop the value `i32.const s` from the stack.
14. Assert: due to validation, a value of value type i32 is on the top of the stack.
15. Pop the value `i32.const d` from the stack.
16. If \(s + n\) is larger than the length of input.data or \(d + n\) is larger than the length of mem.data, then:
   a. Trap.
17. If \(n = 0\), then:
   a. Return.
18. Let $b$ be the byte $\text{data.data}[s]$.
19. Push the value $\text{i32.const } d$ to the stack.
20. Push the value $\text{i32.const } b$ to the stack.
21. Execute the instruction $\text{i32.store8 \{offset 0, align 0\}}$.
22. Assert: due to the earlier check against the memory size, $d + 1 < 2^{32}$.
23. Push the value $\text{i32.const } (d + 1)$ to the stack.
24. Assert: due to the earlier check against the memory size, $s + 1 < 2^{32}$.
25. Push the value $\text{i32.const } (s + 1)$ to the stack.
26. Push the value $\text{i32.const } (n - 1)$ to the stack.
27. Execute the instruction $\text{memory.init } x$.

\[
S; F; ((\text{i32.const } d) \ (\text{i32.const } s) \ (\text{i32.const } n) \ (\text{memory.init } x)) \leftrightarrow S; F; \text{trap}
\]

\[
(\text{if } s + n > |S.\text{datas}[F.\text{module}.\text{dataaddrs}[x]].\text{data}|
\lor d + n > |S.\text{mems}[F.\text{module}.\text{memaddrs}[x]].\text{data}|)
\]

\[
S; F; ((\text{i32.const } d) \ (\text{i32.const } s) \ (\text{i32.const } 0) \ (\text{memory.init } x)) \leftrightarrow S; F; \epsilon
\]

\[(\text{otherwise})\]

\[
S; F; ((\text{i32.const } d) \ (\text{i32.const } s) \ (\text{i32.const } n + 1) \ (\text{memory.init } x)) \leftrightarrow
S; F; ((\text{i32.const } d) \ (\text{i32.const } b) \ (\text{i32.store8 \{offset 0, align 0\}}))
\]

\[
((\text{i32.const } d + 1) \ (\text{i32.const } s + 1) \ (\text{i32.const } n) \ (\text{memory.init } x))
\]

\[(\text{otherwise, if } b = S.\text{datas}[F.\text{module}.\text{dataaddrs}[x]].\text{data}[s])\]

\[
\text{data.drop } x
\]

1. Let $F$ be the current frame.
2. Assert: due to validation, $F.\text{module}.\text{dataaddrs}[x]$ exists.
3. Let $a$ be the data address $F.\text{module}.\text{dataaddrs}[x]$.
4. Assert: due to validation, $S.\text{datas}[a]$ exists.
5. Replace $S.\text{datas}[a]$ with the data instance \{data $\epsilon$\}.

\[
S; F; (\text{data.drop } x) \leftrightarrow S'; F; \epsilon
\]

\[(\text{if } S' = S \text{ with } \text{datas}[F.\text{module}.\text{dataaddrs}[x]] = \{\text{data } \epsilon\})\]

### 4.4.8 Control Instructions

\text{nop}

1. Do nothing.
1. Trap.

unreachable

\[ \text{unreachable} \rightarrow \text{trap} \]

block blocktype instr* end

1. Assert: due to validation, \( \exp F (\text{blocktype}) \) is defined.
2. Let \([t_1^m] \rightarrow [t_2^m] \) be the function type \( \exp F (\text{blocktype}) \).
3. Let \( L \) be the label whose arity is \( n \) and whose continuation is the end of the block.
4. Assert: due to validation, there are at least \( m \) values on the top of the stack.
5. Pop the values \( \text{val}^m \) from the stack.
6. Enter the block \( \text{val}^m \ instr^* \) with label \( L \).

\[ F; \text{val}^m \ block \ bt \ instr^* \ end \leftrightarrow F; \text{label}_{l} \{ \epsilon \} \ \text{val}^m \ instr^* \ end \]

\[ \text{if expand}_{F}(bt) = [l_1^m] \rightarrow [l_2^m] \]

loop blocktype instr* end

1. Assert: due to validation, \( \exp F (\text{blocktype}) \) is defined.
2. Let \([t_1^m] \rightarrow [t_2^m] \) be the function type \( \exp F (\text{blocktype}) \).
3. Let \( L \) be the label whose arity is \( m \) and whose continuation is the start of the loop.
4. Assert: due to validation, there are at least \( m \) values on the top of the stack.
5. Pop the values \( \text{val}^m \) from the stack.
6. Enter the block \( \text{val}^m \ instr^* \) with label \( L \).

\[ F; \text{val}^m \ loop \ bt \ instr^* \ end \leftrightarrow F; \text{label}_{m} \{ \text{loop bt instr}^* \ end \} \ \text{val}^m \ instr^* \ end \]

\[ \text{if expand}_{F}(bt) = [l_1^m] \rightarrow [l_2^m] \]

if blocktype instr\_1* else instr\_2* end

1. Assert: due to validation, \( \exp F (\text{blocktype}) \) is defined.
2. Let \([t_1^m] \rightarrow [t_2^m] \) be the function type \( \exp F (\text{blocktype}) \).
3. Let \( L \) be the label whose arity is \( n \) and whose continuation is the end of the if instruction.
4. Assert: due to validation, a value of value type \( \text{i32} \) is on the top of the stack.
5. Pop the value \( \text{i32} . \text{const} \ c \) from the stack.
6. Assert: due to validation, there are at least \( m \) values on the top of the stack.
7. Pop the values \( \text{val}^m \) from the stack.
8. If \( c \) is non-zero, then:
   a. Enter the block \( \text{val}^m \ instr_1^* \) with label \( L \).
9. Else:
   a. Enter the block \( \text{val}^m \ instr_2^* \) with label \( L \).
\[ F; \text{val}^m (i32.\text{const} c) \text{ if } bt \text{ instr}^*_1 \text{ else } \text{instr}^*_2 \text{ end} \quad \leftrightarrow \quad F; \text{label}_n \{ \epsilon \} \text{val}^m \text{ instr}^*_1 \text{ end} \quad \text{(if } c \neq 0 \wedge \text{expand}_F(b) = [t^n] \rightarrow [t^2]) \]

\[ F; \text{val}^m (i32.\text{const} c) \text{ if } bt \text{ instr}^*_1 \text{ else } \text{instr}^*_2 \text{ end} \quad \leftrightarrow \quad F; \text{label}_n \{ \epsilon \} \text{val}^m \text{ instr}^*_2 \text{ end} \quad \text{(if } c = 0 \wedge \text{expand}_F(b) = [t^n] \rightarrow [t^2]) \]

\text{br}\ l

1. Assert: due to validation, the stack contains at least \( l + 1 \) labels.
2. Let \( L \) be the \( l \)-th label appearing on the stack, starting from the top and counting from zero.
3. Let \( n \) be the arity of \( L \).
4. Assert: due to validation, there are at least \( n \) values on the top of the stack.
5. Pop the values \( \text{val}^n \) from the stack.
6. Repeat \( l + 1 \) times:
   a. While the top of the stack is a value, do:
      i. Pop the value from the stack.
   b. Assert: due to validation, the top of the stack now is a label.
   c. Pop the label from the stack.
7. Push the values \( \text{val}^n \) to the stack.
8. Jump to the continuation of \( L \).

\[ \text{label}_n \{ \text{instr}^* \} B^l[\text{val}^m (\text{br}\ l)] \text{ end} \quad \leftrightarrow \quad \text{val}^n \text{ instr}^* \]

\text{br}_{if}\ l

1. Assert: due to validation, a value of value type \( i32 \) is on the top of the stack.
2. Pop the value \( i32.\text{const} c \) from the stack.
3. If \( c \) is non-zero, then:
   a. Execute the instruction \( (\text{br}\ l) \).
4. Else:
   a. Do nothing.

\[ (i32.\text{const} c) (\text{br}_{if}\ l) \quad \leftrightarrow \quad (\text{br}\ l) \quad (\text{if } c \neq 0) \]

\[ (i32.\text{const} c) (\text{br}_{if}\ l) \quad \leftrightarrow \quad \epsilon \quad (\text{if } c = 0) \]

\text{br}_{table}\ l^* l_N

1. Assert: due to validation, a value of value type \( i32 \) is on the top of the stack.
2. Pop the value \( i32.\text{const} i \) from the stack.
3. If \( i \) is smaller than the length of \( l^* \), then:
   a. Let \( l_i \) be the label \( l^*[i] \).
   b. Execute the instruction \( (\text{br}\ l_i) \).
4. Else:
   a. Execute the instruction \( (\text{br}\ l_N) \).
return

1. Let $F$ be the current frame.
2. Let $n$ be the arity of $F$.
3. Assert: due to validation, there are at least $n$ values on the top of the stack.
4. Pop the results $\text{val}^n$ from the stack.
5. Assert: due to validation, the stack contains at least one frame.
6. While the top of the stack is not a frame, do:
   a. Pop the top element from the stack.
7. Assert: the top of the stack is the frame $F$.
8. Pop the frame from the stack.
9. Push $\text{val}^n$ to the stack.
10. Jump to the instruction after the original call that pushed the frame.

\[
\text{frame}_n \{F\} B^k [\text{val}^n \text{return}] \text{ end} \leftrightarrow \text{val}^n
\]

call $x$

1. Let $F$ be the current frame.
3. Let $a$ be the function address $F$.module.funcaddrs[$x$].
4. Invoke the function instance at address $a$.
   \[
   F; (\text{call} \ x) \leftrightarrow F; (\text{invoke} \ a) \quad \text{if } F.\text{module.funcaddrs}[x] = a
   \]

call_indirect $x$ $y$

1. Let $F$ be the current frame.
3. Let $ta$ be the table address $F$.module.tableaddrs[$x$].
5. Let $tab$ be the table instance $S.tables[ta]$.
7. Let $ft_{\text{expect}}$ be the function type $F$.module.types[$y$].
8. Assert: due to validation, a value with value type $\text{i32}$ is on the top of the stack.
9. Pop the value $\text{i32.const} \ i$ from the stack.
10. If $i$ is not smaller than the length of $\text{tab.elem}$, then:
    a. Trap.
11. Let $r$ be the reference $\text{tab.elem}[i]$.
12. If $r$ is $\text{ref.null} t$, then:
a. Trap.

13. Assert: due to validation of table mutation, \( r \) is a function reference.

14. Let \( \text{ref} \ a \) be the function reference \( r \).

15. Assert: due to validation of table mutation, \( S.\text{funcs}[a] \) exists.

16. Let \( f \) be the function instance \( S.\text{funcs}[a] \).

17. Let \( f\text{\_actual}_a \) be the function type \( f.\text{type} \).

18. If \( f\text{\_actual}_a \) and \( f\text{\_expect}_a \) differ, then:
   a. Trap.

19. Invoke the function instance at address \( a \).

\[
S; F; (\text{is32\_const} \ i) (\text{call\_indirect} \ x \ y) \leftrightarrow S; F; (\text{invoke} \ a) \\
(\text{if} \ S.\text{tables}[F.\text{module}\text{-tableaddrs}[x]].\text{elem}[i] = \text{ref} \ a \\
\land S.\text{funcs}[a] = f \\
\land F.\text{module}\text{-types}[y] = f.\text{type}) \\
S; F; (\text{is32\_const} \ i) (\text{call\_indirect} \ x \ y) \leftrightarrow S; F; \text{trap} \\
(\text{otherwise})
\]

4.4.9 Blocks

The following auxiliary rules define the semantics of executing an instruction sequence that forms a block.

**Entering instr\^* with label \( L \)**

1. Push \( L \) to the stack.
2. Jump to the start of the instruction sequence \( \text{instr}^* \).

**Note:** No formal reduction rule is needed for entering an instruction sequence, because the label \( L \) is embedded in the administrative instruction that structured control instructions reduce to directly.

**Exiting instr\^* with label \( L \)**

When the end of a block is reached without a jump or trap aborting it, then the following steps are performed.

1. Let \( m \) be the number of values on the top of the stack.
2. Pop the values \( \text{val}^m \) from the stack.
3. Assert: due to validation, the label \( L \) is now on the top of the stack.
4. Pop the label from the stack.
5. Push \( \text{val}^m \) back to the stack.
6. Jump to the position after the end of the structured control instruction associated with the label \( L \).

\[
\text{label}_n \{\text{instr}^*\} \text{val}^m \text{end} \leftrightarrow \text{val}^m
\]

**Note:** This semantics also applies to the instruction sequence contained in a loop instruction. Therefore, execution of a loop falls off the end, unless a backwards branch is performed explicitly.
4.4.10 Function Calls

The following auxiliary rules define the semantics of invoking a function instance through one of the call instructions and returning from it.

Invocation of function address $a$

1. Assert: due to validation, $S.funcs[a]$ exists.
2. Let $f$ be the function instance, $S.funcs[a]$.
3. Let $[t^1_n] \rightarrow [t^m_m]$ be the function type $f$.type.
4. Let $t^*$ be the list of value types $f$.code.locals.
5. Let $\text{instr}^*$ end be the expression $f$.code.body.
6. Assert: due to validation, $n$ values are on the top of the stack.
7. Pop the values $val^n$ from the stack.
8. Let $val^*_n$ be the list of zero values of types $t^*$.
9. Let $F$ be the frame $\{\text{module } f\!.\text{module}, \text{locals } val^n (\text{default}_{t})^*\}$.
10. Push the activation of $F$ with arity $m$ to the stack.
11. Let $L$ be the label whose arity is $m$ and whose continuation is the end of the function.
12. Enter the instruction sequence $\text{instr}^*$ with label $L$.

\[
S; \text{val}^n (\text{invoke } a) \mapsto S; \text{frame}_m \{F\} \text{ label}_m \{\} \text{ instr}^* \text{ end end} \quad \text{(if } S.funcs[a] = f \text{)}
\]
\[
\quad \land f.type = [t^1_n] \rightarrow [t^m_m] \\
\quad \land f.code = \{\text{type } x, \text{locals } t^k, \text{body } \text{instr}^* \text{ end}\} \\
\quad \land F = \{\text{module } f\!.\text{module}, \text{locals } val^n (\text{default}_{t})^k\}\}
\]

Returning from a function

When the end of a function is reached without a jump (i.e., return) or trap aborting it, then the following steps are performed.

1. Let $F$ be the current frame.
2. Let $n$ be the arity of the activation of $F$.
3. Assert: due to validation, there are $n$ values on the top of the stack.
4. Pop the results $val^n$ from the stack.
5. Assert: due to validation, the frame $F$ is now on the top of the stack.
6. Pop the frame from the stack.
7. Push $val^n$ back to the stack.
8. Jump to the instruction after the original call.

\[
\text{frame}_n \{F\} \text{ val}^n \text{ end } \leftrightarrow \text{ val}^n
\]
Host Functions

Invoking a host function has non-deterministic behavior. It may either terminate with a trap or return regularly. However, in the latter case, it must consume and produce the right number and types of WebAssembly values on the stack, according to its function type.

A host function may also modify the store. However, all store modifications must result in an extension of the original store, i.e., they must only modify mutable contents and must not have instances removed. Furthermore, the resulting store must be valid, i.e., all data and code in it is well-typed.

\[
S; \text{val}_n \ (\text{invoke } a) \mapsto S'; \text{result} \quad \text{(if } S.\text{funcs}[a] = \{\text{type } [t_1^n] \rightarrow [t_m^n], \text{hostcode } hf \} \land (S'; \text{result}) \in hf(S; \text{val}_n))
\]

\[
S; \text{val}_n \ (\text{invoke } a) \mapsto S; \text{val}_n \ (\text{invoke } a) \quad \text{(if } S.\text{funcs}[a] = \{\text{type } [t_1^n] \rightarrow [t_m^n], \text{hostcode } hf \} \land \bot \in hf(S; \text{val}_n))
\]

Here, hf(S; val_\text{=}•) denotes the implementation-defined execution of host function \(hf\) in current store \(S\) with arguments \(\text{val}_n\). It yields a set of possible outcomes, where each element is either a pair of a modified store \(S'\) and a result or the special value \(\bot\) indicating divergence. A host function is non-deterministic if there is at least one argument for which the set of outcomes is not singular.

For a WebAssembly implementation to be sound in the presence of host functions, every host function instance must be valid, which means that it adheres to suitable pre- and post-conditions: under a valid store \(S\), and given arguments \(\text{val}_n\) matching the ascribed parameter types \(t_1^n\), executing the host function must yield a non-empty set of possible outcomes each of which is either divergence or consists of a valid store \(S'\) that is an extension of \(S\) and a result matching the ascribed return types \(t_m^n\). All these notions are made precise in the Appendix.

**Note:** A host function can call back into WebAssembly by invoking a function exported from a module. However, the effects of any such call are subsumed by the non-deterministic behavior allowed for the host function.

4.4.11 Expressions

An expression is evaluated relative to a current frame pointing to its containing module instance.

1. Jump to the start of the instruction sequence instr* of the expression.
2. Execute the instruction sequence.
3. Assert: due to validation, the top of the stack contains a value.
4. Pop the value val from the stack.

The value val is the result of the evaluation.

\[
S; F; \text{instr}^* \mapsto S'; F'; \text{instr}'^* \quad \text{(if } S; F; \text{instr}^* \text{ end } \mapsto S'; F'; \text{instr}'^* \text{ end})
\]

**Note:** Evaluation iterates this reduction rule until reaching a value. Expressions constituting function bodies are executed during function invocation.
4.5 Modules

For modules, the execution semantics primarily defines instantiation, which allocates instances for a module and its contained definitions, initializes tables and memories from contained element and data segments, and invokes the start function if present. It also includes invocation of exported functions.

Instantiation depends on a number of auxiliary notions for type-checking imports and allocating instances.

4.5.1 External Typing

For the purpose of checking external values against imports, such values are classified by external types. The following auxiliary typing rules specify this typing relation relative to a store S in which the referenced instances live.

\[ S \vdash \text{func } a : \text{func } S.\text{funcs}[a].\text{type} \]

\[ S \vdash \text{table } a : \text{table } S.\text{tables}[a].\text{type} \]

\[ S \vdash \text{mem } a : \text{mem } S.\text{mems}[a].\text{type} \]

\[ S \vdash \text{global } a : \text{global } S.\text{globals}[a].\text{type} \]
4.5.2 Value Typing

For the purpose of checking argument values against the parameter types of exported functions, values are classified by value types. The following auxiliary typing rules specify this typing relation relative to a store $S$ in which possibly referenced addresses live.

**Numeric Values** $t.\text{const } c$

- The value is valid with number type $t$.

$$S \vdash t.\text{const } c : t$$

**Null References** $\text{ref } \text{null } t$

- The value is valid with reference type $t$.

$$S \vdash \text{ref } \text{null } t : t$$

**Function References** $\text{ref } a$

- The external value $\text{func } a$ must be valid.
- Then the value is valid with reference type $\text{funcref}$.

$$S \vdash \text{func } a : \text{func } \text{functype} \quad \frac{}{S \vdash \text{ref } a : \text{funcref}}$$

**External References** $\text{ref } \text{extern } a$

- The value is valid with reference type $\text{externref}$.

$$S \vdash \text{ref } \text{extern } a : \text{externref}$$

4.5.3 Allocation

New instances of functions, tables, memories, and globals are allocated in a store $S$, as defined by the following auxiliary functions.

**Functions**

1. Let $\text{func}$ be the function to allocate and $\text{moduleinst}$ its module instance.
2. Let $a$ be the first free function address in $S$.
3. Let $\text{functype}$ be the function type $\text{moduleinst}.\text{types}[\text{func.type}]$.
4. Let $\text{funcinst}$ be the function instance $\{\text{type } \text{functype}, \text{module } \text{moduleinst}, \text{code } \text{func}\}$.
5. Append $\text{funcinst}$ to the functions of $S$.
allocfunc(S, func, moduleinst) = S’, funcaddr

where:
  funcaddr = |S.funcs|
  functype = moduleinst.types[func.type]
  funcinst = {type functype, module moduleinst, code func}
  S’ = S ⊕ {funcs funcinst}

Host Functions

1. Let hostfunc be the host function to allocate and functype its function type.
2. Let a be the first free function address in S.
3. Let funcinst be the function instance \{type functype, hostcode hostfunc\}.
4. Append funcinst to the funcs of S.
5. Return a.

allochostfunc(S, functype, hostfunc) = S’, funcaddr

where:
  funcaddr = |S.funcs|
  funcinst = {type functype, hostcode hostfunc}
  S’ = S ⊕ {funcs funcinst}

Note: Host functions are never allocated by the WebAssembly semantics itself, but may be allocated by the embedder.

Tables

1. Let tabletype be the table type to allocate and ref the initialization value.
2. Let (\{\{min n, max m\} reftype\}) be the structure of table type tabletype.
3. Let a be the first free table address in S.
4. Let tableinst be the table instance \{type tabletype, elem ref”\} with n elements set to ref.
5. Append tableinst to the tables of S.

alloctable(S, tabletype, ref) = S’, tableaddr

where:
  tabletype = \{\{min n, max m\} reftype\}
  tableaddr = |S.tables|
  tableinst = \{type tabletype, elem ref”\}
  S’ = S ⊕ {tables tableinst}
Memories

1. Let $\text{memtype}$ be the memory type to allocate.
2. Let $\{\text{min } n, \text{max } m\}$ be the structure of memory type $\text{memtype}$.
3. Let $a$ be the first free memory address in $S$.
4. Let $\text{meminst}$ be the memory instance $\{\text{type } \text{memtype}, \text{data } (0\times00)^n \cdot 64\text{Ki}\}$ that contains $n$ pages of zeroed bytes.
5. Append $\text{meminst}$ to the $\text{mems}$ of $S$.

\[
\text{allocmem}(S, \text{memtype}) = S', \text{memaddr}
\]
where:
\[
\begin{align*}
\text{memtype} &= \{\text{min } n, \text{max } m\} \\
\text{memaddr} &= |S.\text{mems}| \\
\text{meminst} &= \{\text{type } \text{memtype}, \text{data } (0\times00)^n \cdot 64\text{Ki}\} \\
S' &= S \oplus \{\text{mems meminst}\}
\end{align*}
\]

Globals

1. Let $\text{globaltype}$ be the global type to allocate and $\text{val}$ the value to initialize the global with.
2. Let $a$ be the first free global address in $S$.
3. Let $\text{globalinst}$ be the global instance $\{\text{type } \text{globaltype}, \text{value } \text{val}\}$.
4. Append $\text{globalinst}$ to the globals of $S$.
5. Return $a$.

\[
\text{allocglobal}(S, \text{globaltype}, \text{val}) = S', \text{globaladdr}
\]
where:
\[
\begin{align*}
\text{globaladdr} &= |S.\text{globals}| \\
\text{globalinst} &= \{\text{type } \text{globaltype}, \text{value } \text{val}\} \\
S' &= S \oplus \{\text{globals globalinst}\}
\end{align*}
\]

Element segments

1. Let $\text{reftype}$ be the elements’ type and $\text{ref}^*$ the vector of references to allocate.
2. Let $a$ be the first free element address in $S$.
3. Let $\text{eleminst}$ be the element instance $\{\text{type } t, \text{elem } \text{ref}^*\}$.
4. Append $\text{eleminst}$ to the $\text{elems}$ of $S$.
5. Return $a$.

\[
\text{allocelem}(S, \text{reftype}, \text{ref}^*) = S', \text{elemaddr}
\]
where:
\[
\begin{align*}
\text{elemaddr} &= |S.\text{elems}| \\
\text{eleminst} &= \{\text{type } \text{reftype}, \text{elem } \text{ref}^*\} \\
S' &= S \oplus \{\text{elems eleminst}\}
\end{align*}
\]
Data segments

1. Let bytes be the vector of bytes to allocate.
2. Let a be the first free data address in S.
3. Let datainst be the data instance \{data bytes\}.
4. Append datainst to the datas of S.
5. Return a.

\[
\text{allocdata}(S, \text{bytes}) = S', \text{dataaddr}
\]
where:
\[
\text{dataaddr} = |S.\text{datas}|
\]
\[
data\text{inst} = \{\text{data bytes}\}
\]
\[
S' = S \oplus \{\text{datas datainst}\}
\]

Growing tables

1. Let tableinst be the table instance to grow, n the number of elements by which to grow it, and ref the initialization value.
2. Let len be n added to the length of tableinst.elem.
3. If len is larger than or equal to \(2^{32}\), then fail.
4. Let limits t be the structure of table type tableinst.type.
5. Let limits' be limits with min updated to len.
6. If limits' is not valid, then fail.
7. Append ref^n to tableinst.elem.
8. Set tableinst.type to the table type limits' t.

\[
growtable(tableinst, n, ref) = \text{tableinst with type = limits' t with elem = tableinst.elem ref}^n
\]
if len = n + |tableinst.elem|
\[
\land \text{len} < 2^{32}
\land \text{limits t = tableinst.type}
\land \text{limits'} = \text{limits with min = len}
\land \neg \text{limits'} ok
\]

Growing memories

1. Let meminst be the memory instance to grow and n the number of pages by which to grow it.
2. Assert: The length of meminst.data is divisible by the page size 64 Ki.
3. Let len be n added to the length of meminst.data divided by the page size 64 Ki.
4. If len is larger than \(2^{16}\), then fail.
5. Let limits be the structure of memory type meminst.type.
6. Let limits' be limits with min updated to len.
7. If limits' is not valid, then fail.
8. Append n times 64 Ki bytes with value 0x00 to meminst.data.
9. Set meminst.type to the memory type limits'.
growmem(\textit{meminst}, n) = \textit{meminst} with type = limits' with data = \textit{meminst}.data (0x00)^n \cdot 64 \text{ Ki}

\text{if } len = n + |\textit{meminst}.data|/64 \text{ Ki}
\wedge len \leq 2^{16}
\wedge limits = \textit{meminst}.type
\wedge limits' = limits with min = len
\wedge \neg limits' \(ok

\section*{Modules}

The allocation function for \textit{modules} requires a suitable list of \textit{external values} that are assumed to \textit{match} the import vector of the module, a list of initialization \textit{values} for the module’s \textit{globals}, and list of \textit{reference} vectors for the module’s \textit{element segments}.

1. Let \textit{module} be the \textit{module} to allocate and externalval\textsubscript{im}* the vector of \textit{external values} providing the module’s imports, \textit{val}* the initialization values of the module’s \textit{globals}, and (\textit{ref})* the \textit{reference} vectors of the module’s \textit{element segments}.

2. For each \textit{function} \textit{func} in \textit{module}.\textit{funcs}, do:
   a. Let funcaddr\textsubscript{i} be the \textit{function address} resulting from allocating \textit{func} for the module instance \textit{moduleinst} defined below.

3. For each \textit{table} \textit{table} in \textit{module}.\textit{tables}, do:
   a. Let limits\textsubscript{i} \textit{t} be the \textit{table type} \textit{table},\textit{type}.
   b. Let tableaddr\textsubscript{i} be the \textit{table address} resulting from allocating \textit{table},\textit{type} with initialization value \textit{ref}\textsubscript{null} \textit{t}.

4. For each \textit{memory} \textit{mem} in \textit{module}.\textit{mems}, do:
   a. Let memaddr\textsubscript{i} be the \textit{memory address} resulting from allocating \textit{mem},\textit{type} with initializer value \textit{val'}[i].

5. For each \textit{global} \textit{global} in \textit{module}.\textit{globals}, do:
   a. Let globaladdr\textsubscript{i} be the \textit{global address} resulting from allocating \textit{global},\textit{type} with \textit{val'}[i].

6. For each \textit{element segment} \textit{elem} in \textit{module}.\textit{elems}, do:
   a. Let elemaddr\textsubscript{i} be the \textit{element address} resulting from allocating a \textit{element instance} of \textit{reference type} \textit{elem},\textit{type} with contents (\textit{ref})*[i].

7. For each \textit{data segment} \textit{data} in \textit{module}.\textit{datas}, do:
   a. Let dataaddr\textsubscript{i} be the \textit{data address} resulting from allocating a \textit{data instance} with contents \textit{data},\textit{init}.

8. Let funcaddr* be the concatenation of the \textit{function addresses} funcaddr\textsubscript{i} in index order.
9. Let tableaddr* be the concatenation of the \textit{table addresses} tableaddr\textsubscript{i} in index order.
10. Let memaddr* be the concatenation of the \textit{memory addresses} memaddr\textsubscript{i} in index order.
11. Let globaladdr* be the concatenation of the \textit{global addresses} globaladdr\textsubscript{i} in index order.
12. Let elemaddr* be the concatenation of the \textit{element addresses} elemaddr\textsubscript{i} in index order.
13. Let dataaddr* be the concatenation of the \textit{data addresses} dataaddr\textsubscript{i} in index order.
14. Let funcaddr\textsubscript{mod} be the list of \textit{function addresses} extracted from externalval\textsubscript{im}, concatenated with funcaddr*.
15. Let tableaddr\textsubscript{mod} be the list of \textit{table addresses} extracted from externalval\textsubscript{im}, concatenated with tableaddr*.
16. Let memaddr\textsubscript{mod} be the list of \textit{memory addresses} extracted from externalval\textsubscript{im}, concatenated with memaddr*.
17. Let globaladdr\textsubscript{mod} be the list of \textit{global addresses} extracted from externalval\textsubscript{im}, concatenated with globaladdr*.
18. For each export \( \text{export}_i \) in \( \text{module.exports} \), do:
   
a. If \( \text{export}_i \) is a function export for function index \( x \), then let \( \text{externval}_i \) be the external value \( \text{funcaddr}_{\text{mod}}^* \text{[mod][x]} \).
   
b. Else, if \( \text{export}_i \) is a table export for table index \( x \), then let \( \text{externval}_i \) be the external value \( \text{tableaddr}_{\text{mod}}^* \text{[mod][x]} \).
   
c. Else, if \( \text{export}_i \) is a memory export for memory index \( x \), then let \( \text{externval}_i \) be the external value \( \text{memaddr}_{\text{mod}}^* \text{[mod][x]} \).
   
d. Else, if \( \text{export}_i \) is a global export for global index \( x \), then let \( \text{externval}_i \) be the external value \( \text{globaladdr}_{\text{globaladdr}_i} \).
   
e. Let \( \text{exportinst}_i \) be the export instance \( \{ \text{name (\text{export}_i, \text{name}), value \text{externval}_i} \} \).

19. Let \( \text{moduleinst}^* \) be the concatenation of the export instances \( \text{exportinst}_i \) in index order.

20. Let \( \text{moduleinst} \) be the module instance \( \{ \text{types (\text{module.types}), funcaddr funcaddr}_{\text{mod}}^*, \text{tableaddr tableaddr}_{\text{mod}}^*, \text{memaddr memaddr}_{\text{mod}}^*, \text{globaladdr globaladdr}_{\text{mod}}^*, \text{exports exportinst}^* \} \).

21. Return \( \text{moduleinst} \).

\[
\text{allocmodule}(S, \text{module, externval}_{\text{im}}^*, \text{val}^*, (\text{ref}^*)^*) = S', \text{moduleinst}
\]

where:

\[
\begin{align*}
\text{table}^* &= \text{module.tables} \\
\text{mem}^* &= \text{module.mems} \\
\text{global}^* &= \text{moduleglobals} \\
\text{elem}^* &= \text{module.elems} \\
\text{data}^* &= \text{module.datas} \\
\text{export}^* &= \text{module.exports}
\end{align*}
\]

\[
\text{moduleinst}^* = \{ \text{types module.types, funcaddr funcaddr}_{\text{funcaddr}}^*, \text{tableaddr tableaddr}_{\text{tableaddr}}^*, \text{memaddr memaddr}_{\text{memaddr}}^*, \text{globaladdr globaladdr}_{\text{globaladdr}}^*, \text{elemaddr elemaddr}_{\text{elemaddr}}^*, \text{dataaddr dataaddr}_{\text{dataaddr}}^*, \text{exports exportinst}^* \}
\]

\[
\begin{align*}
S_1, \text{funcaddr}^* &= \text{allocfunc}^*(S, \text{module.funcs, moduleinst}) \\
S_2, \text{tableaddr}^* &= \text{allocatable}^*(S_1, (\text{table.type}^*, (\text{ref.null t})^*) \text{ (where (table.type) }^* = (\text{limits t})^*) \\
S_3, \text{memaddr}^* &= \text{allocmem}^*(S_2, (\text{mem.type}^*) \text{) } \\
S_4, \text{globaladdr}^* &= \text{allocoglobal}^*(S_3, (\text{global.type}^*, \text{val}^*) \text{) } \\
S_5, \text{elemaddr}^* &= \text{allocelmm}^*(S_4, (\text{elem.type}^*, (\text{ref}^*)^*) \text{) } \\
S', \text{dataaddr}^* &= \text{allocdata}^*(S_5, (\text{data.init})^*) \\
\text{exportinst}^* &= \{ \text{name (export.name), value externval}_{\text{ex}}^* \}^*
\end{align*}
\]

\[
\begin{align*}
\text{funcs(externval}_{\text{ex}}^*) &= (\text{moduleinst.funcs(x)}^* \text{ (where x }^* = \text{ funcs(export)*)} \\
\text{tables(externval}_{\text{ex}}^*) &= (\text{moduleinst.tables(x)}^* \text{ (where x }^* = \text{ tables(export)*)} \\
\text{mems(externval}_{\text{ex}}^*) &= (\text{moduleinst.mems(x)}^* \text{ (where x }^* = \text{ mems(export)*)} \\
\text{globals(externval}_{\text{ex}}^*) &= (\text{moduleinst.globals(x)}^* \text{ (where x }^* = \text{ globals(export)*)}
\end{align*}
\]

Here, the notation allocex^* is shorthand for multiple allocations of object kind \( X \), defined as follows:

\[
\text{allocex}^*(S_0, X^n, \ldots) = S_n, a^n
\]

where for all \( i < n \):

\[
S_{i+1}, a^n[i] = \text{allocex}(S_i, X^n[i], \ldots)
\]

Moreover, if the dots \( \ldots \) are a sequence \( A^m \) (as for globals or tables), then the elements of this sequence are passed to the allocation function pointwise.
Note: The definition of module allocation is mutually recursive with the allocation of its associated functions, because the resulting module instance \texttt{moduleinst} is passed to the function allocator as an argument, in order to form the necessary closures. In an implementation, this recursion is easily unraveled by mutating one or the other in a secondary step.

### 4.5.4 Instantiation

Given a \texttt{store} \( S \), a \texttt{module} \texttt{module} is instantiated with a list of \texttt{external values} \texttt{externval}\(^n\) supplying the required imports as follows.

Instantiation checks that the module is \texttt{valid} and the provided imports \texttt{match} the declared types, and may \texttt{fail} with an error otherwise. Instantiation can also result in a \texttt{trap} from executing the start function. It is up to the \texttt{embedder} to define how such conditions are reported.

1. If \texttt{module} is not \texttt{valid}, then:
   a. Fail.
2. Assert: \texttt{module} is \texttt{valid} with \texttt{external types} \texttt{externtype}\(^m\)\( _{im} \) classifying its \texttt{imports}.
3. If the number \( m \) of \texttt{imports} is not equal to the number \( n \) of provided \texttt{external values}, then:
   a. Fail.
4. For each \texttt{external value} \texttt{externval}_i in \texttt{externval}\(^n\) and \texttt{external type} \texttt{externtype}_i\(^′\) in \texttt{externtype}\(^m\)\( _{im} \), do:
   a. If \texttt{externval}_i is not \texttt{valid} with an \texttt{external type} \texttt{externtype}_i in \texttt{store} \( S \), then:
      i. Fail.
   b. If \texttt{externtype}_i does not \texttt{match} \texttt{externtype}_i\(^′\), then:
      i. Fail.
5. Let \texttt{moduleinst}_{init} be the auxiliary module instance \{\texttt{globaladdrs globals(externval}\(^n\)), \texttt{funcaddrs moduleinst}_{funcaddrs}\} that only consists of the imported globals and the imported and allocated functions from the final module instance \texttt{moduleinst}, defined below.
6. Let \( F_{init} \) be the auxiliary \texttt{frame} \{\texttt{module} \texttt{moduleinst}_{init}, \texttt{locals} \( \epsilon \)\}.
7. Push the frame \( F_{init} \) to the stack.
8. Let \( \texttt{val}^* \) be the vector of \texttt{global} initialization \texttt{values} determined by \texttt{module} and \texttt{externval}\(^n\). These may be calculated as follows.
   a. For each \texttt{global} \texttt{global}_i in \texttt{module.globals}, do:
      i. Let \texttt{val}_i be the result of \texttt{evaluating} the initializer expression \texttt{global}_i,\texttt{init}.
   b. Assert: due to \texttt{validation}, the frame \( F_{init} \) is now on the top of the stack.
   c. Let \( \texttt{val}^* \) be the concatenation of \texttt{val}_i in index order.
9. Let \( (\texttt{ref}^*)^* \) be the list of \texttt{reference} vectors determined by the \texttt{element segments} in \texttt{module}. These may be calculated as follows.
   a. For each \texttt{element segment} \texttt{elem}_i in \texttt{module.elems}, and for each element \texttt{expression} \texttt{expr}_{ij} in \texttt{elem}_i,\texttt{init}, do:
      i. Let \texttt{ref}_{ij} be the result of \texttt{evaluating} the initializer expression \texttt{expr}_{ij}.
   b. Let \( \texttt{ref}^* \) be the concatenation of function elements \texttt{ref}_{ij} in order of index \( j \).
   c. Let \( (\texttt{ref}^*)^* \) be the concatenation of function element vectors \texttt{ref}^*_i in order of index \( i \).
10. Pop the frame \( F_{init} \) from the stack.
11. Let moduleinst be a new module instance allocated from module in store S with imports externval\textsuperscript{m}, global initializer values val\textsuperscript{v}, and element segment contents (ref\textsuperscript{e})\textsuperscript{e}, and let S' be the extended store produced by module allocation.

12. Let F be the auxiliary frame \{module moduleinst, locals \epsilon\}.

13. Push the frame F to the stack.

14. For each element segment elem\textsubscript{i} in module.elems whose mode is of the form active \{table tableidx\textsubscript{i}, offset einstr\textsuperscript{e} end\}, do:
   a. Let n be the length of the vector elem\textsubscript{i}.init.
   b. Execute the instruction sequence einstr\textsuperscript{e}.
   c. Execute the instruction i32.const 0.
   d. Execute the instruction i32.const n.
   e. Execute the instruction table.init tableidx\textsubscript{i} i.
   f. Execute the instruction elem.drop i.

15. For each data segment data\textsubscript{i} in module.datas whose mode is of the form active \{memory memidx\textsubscript{i}, offset dinstr\textsuperscript{e} end\}, do:
   a. Assert: memidx\textsubscript{i} is 0.
   b. Let n be the length of the vector data\textsubscript{i}.init.
   c. Execute the instruction sequence dinstr\textsuperscript{e}.
   d. Execute the instruction i32.const 0.
   e. Execute the instruction i32.const n.
   f. Execute the instruction memory.init i.
   g. Execute the instruction data.drop i.

16. If the start function module.start is not empty, then:
   a. Let start be the start function module.start.
   b. Execute the instruction call start.func.

17. Assert: due to validation, the frame F is now on the top of the stack.

18. Pop the frame F from the stack.
\[ \text{instantiate}(S, \text{module}, \text{externval}^k) = S'; F; \text{runelem}_0(\text{elem}^n[0]) \ldots \text{runelem}_{n-1}(\text{elem}^n[n-1]) \]
\[ \text{rundata}_0(\text{data}^m[0]) \ldots \text{rundata}_{m-1}(\text{data}^m[m-1]) \]
\[ (\text{call start.func})^* \]
\[ \begin{align*}
\text{if} & \quad \vdash \text{module : extertype}_{\text{lm}}^k \to \text{extertype}_{\text{ex}}^* \hfill \\
\land & \quad (S \vdash \text{externval : extertype})^k \hfill \\
\land & \quad (\vdash \text{extertype} \leq \text{extertype}_{\text{lm}})^* \hfill \\
\land & \quad \text{module.globals} = \text{global}^* \hfill \\
\land & \quad \text{module.elems} = \text{elem}^n \hfill \\
\land & \quad \text{module.datas} = \text{data}^m \hfill \\
\land & \quad \text{module.start} = \text{start}^? \hfill \\
\land & \quad (\text{expr}_k = \text{global}.GINIT)^* \hfill \\
\land & \quad (\text{expr}_k = \text{elem}.EINIT)^n \hfill \\
\land & \quad S', \text{moduleinst} = \text{allocmodule}(S, \text{module}, \text{externval}^k, \text{val}^*, (\text{ref}^*)^n) \hfill \\
\land & \quad F = \{ \text{module moduleinst, locals } \epsilon \} \hfill \\
\land & \quad (S'; F; \text{expr}_k \leftarrow \ast S'; F; \text{val end})^* \hfill \\
\land & \quad ((S'; F; \text{expr}_e \rightarrow \ast S'; F; \text{ref end})^*)^n \hfill \\
\land & \quad (\text{tableaddr} = \text{moduleinst}.tableaddrs[\text{elem}.table])^* \hfill \\
\land & \quad (\text{memaddr} = \text{moduleinst}.memaddrs[\text{data}.memory])^* \hfill \\
\land & \quad (\text{funcaddr} = \text{moduleinst}.funcaddrs[\text{start}.func])^* \} \hfill \\
\end{align*} \]

where:

- \( \text{runelem}_i(\{ \text{type } t, \text{init ref } r, \text{mode } \text{passive} \}) = \epsilon \)
- \( \text{runelem}_i(\{ \text{type } t, \text{init ref } r, \text{mode } \text{active} \{ \text{table } 0, \text{offset } \text{instr } \text{end} \} \}) = \text{instr}_i(\text{i32}.\text{const } 0) (\text{i32}.\text{const } n) (\text{table}.\text{init } i) (\text{elem}.\text{drop } i) \)
- \( \text{runde}\_\text{elem}_i(\{ \text{type } t, \text{init ref } r, \text{mode } \text{declarative} \}) = (\text{elem}.\text{drop } i) \)
- \( \text{rundata}_i(\{ \text{init } b^0, \text{DMODE } \text{passive} \}) = \epsilon \)
- \( \text{rundata}_i(\{ \text{init } b^0, \text{DMODE } \text{active} \{ \text{memory } 0, \text{offset } \text{instr } \text{end} \} \}) = \text{instr}_i(\text{i32}.\text{const } 0) (\text{i32}.\text{const } n) (\text{memory}.\text{init } i) (\text{data}.\text{drop } i) \)

Note: Module allocation and the evaluation of global initializers and element segments are mutually recursive because the global initialization values \( \text{val}^* \) and element segment contents \( (\text{ref}^*)^* \) are passed to the module allocator while depending on the module instance \( \text{moduleinst} \) and store \( S' \) returned by allocation. However, this recursion is just a specification device. In practice, the initialization values can be determined beforehand by staging module allocation such that first, the module’s own function instances \(< \text{syntax} = \text{funcinst} > \) are pre-allocated in the store, then the initializer expressions are evaluated, then the rest of the module instance is allocated, and finally the new function instances’ module fields are set to that module instance. This is possible because validation ensures that initialization expressions cannot actually call a function, only take their reference.

All failure conditions are checked before any observable mutation of the store takes place. Store mutation is not atomic; it happens in individual steps that may be interleaved with other threads.

Evaluation of constant expressions does not affect the store.

### 4.5.5 Invocation

Once a module has been instantiated, any exported function can be invoked externally via its function address \( \text{funcaddr} \) in the store \( S \) and an appropriate list \( \text{val}^* \) of argument values.

Invocation may fail with an error if the arguments do not fit the function type. Invocation can also result in a trap. It is up to the embedder to define how such conditions are reported.

Note: If the embedder API performs type checks itself, either statically or dynamically, before performing an invocation, then no failure other than traps can occur.
The following steps are performed:

2. Let funcinst be the function instance $S$.funcs[funcaddr].
3. Let $[t_1^n] \rightarrow [t_2^m]$ be the function type funcinst.type.
4. If the length $|val^*|$ of the provided argument values is different from the number $n$ of expected arguments, then:
   a. Fail.
5. For each value type $t_i$ in $t_1^n$ and corresponding value $val_i$ in $val^*$, do:
   a. If $val_i$ is not valid with value type $t_i$, then:
      i. Fail.
6. Let $F$ be the dummy frame $\{\text{module }\}, \text{locals } \epsilon$.
7. Push the frame $F$ to the stack.
8. Push the values $val^*$ to the stack.
9. Invoke the function instance at address funcaddr.

Once the function has returned, the following steps are executed:

1. Assert: due to validation, $m$ values are on the top of the stack.
2. Pop $val_{res}^m$ from the stack.

The values $val_{res}^m$ are returned as the results of the invocation.

\[
\text{invoke}(S, \text{funcaddr}, val^n) = S; F^n (\text{invoke funcaddr}) \\
\text{if } S.\text{funcs}[\text{funcaddr}].\text{type} = [t_1^n] \rightarrow [t_2^m] \\
\wedge (S \vdash val : t_1^n) \\
\wedge F = \{\text{module }\}, \text{locals } \epsilon
\]
5.1 Conventions

The binary format for WebAssembly modules is a dense linear encoding of their abstract syntax.\(^{27}\)

The format is defined by an attribute grammar whose only terminal symbols are bytes. A byte sequence is a well-formed encoding of a module if and only if it is generated by the grammar.

Each production of this grammar has exactly one synthesized attribute: the abstract syntax that the respective byte sequence encodes. Thus, the attribute grammar implicitly defines a decoding function (i.e., a parsing function for the binary format).

Except for a few exceptions, the binary grammar closely mirrors the grammar of the abstract syntax.

**Note:** Some phrases of abstract syntax have multiple possible encodings in the binary format. For example, numbers may be encoded as if they had optional leading zeros. Implementations of decoders must support all possible alternatives; implementations of encoders can pick any allowed encoding.

The recommended extension for files containing WebAssembly modules in binary format is “.wasm” and the recommended Media Type\(^{26}\) is “application/wasm”.

5.1.1 Grammar

The following conventions are adopted in defining grammar rules for the binary format. They mirror the conventions used for abstract syntax. In order to distinguish symbols of the binary syntax from symbols of the abstract syntax, typewriter font is adopted for the former.

- Terminal symbols are bytes expressed in hexadecimal notation: \(0x0F\).
- Nonterminal symbols are written in typewriter font: valtype, instr.
- \(B^n\) is a sequence of \(n \geq 0\) iterations of \(B\).
- \(B^*\) is a possibly empty sequence of iterations of \(B\). (This is a shorthand for \(B^n\) used where \(n\) is not relevant.)

---

\(^{27}\) Additional encoding layers – for example, introducing compression – may be defined on top of the basic representation defined here. However, such layers are outside the scope of the current specification.

\(^{26}\) https://www.iana.org/assignments/media-types/media-types.xhtml
• $B^n$ is an optional occurrence of $B$. (This is a shorthand for $B^n$ where $n \leq 1$.)

• $x:B$ denotes the same language as the nonterminal $B$, but also binds the variable $x$ to the attribute synthesized for $B$.

• Productions are written $\text{sym} ::= B_1 \Rightarrow A_1 \mid \ldots \mid B_n \Rightarrow A_n$, where each $A_i$ is the attribute that is synthesized for $\text{sym}$ in the given case, usually from attribute variables bound in $B_i$.

• Some productions are augmented by side conditions in parentheses, which restrict the applicability of the production. They provide a shorthand for a combinatorial expansion of the production into many separate cases.

• If the same meta variable or non-terminal symbol appears multiple times in a production (in the syntax or in an attribute), then all those occurrences must have the same instantiation. (This is a shorthand for a side condition requiring multiple different variables to be equal.)

Note: For example, the binary grammar for number types is given as follows:

\[
\text{numtype} ::= 0x7F \Rightarrow \text{i32} \\
| 0x7E \Rightarrow \text{i64} \\
| 0x7D \Rightarrow \text{f32} \\
| 0x7C \Rightarrow \text{f64}
\]

Consequently, the byte 0x7F encodes the type i32, 0x7E encodes the type i64, and so forth. No other byte value is allowed as the encoding of a number type.

The binary grammar for limits is defined as follows:

\[
\text{limits} ::= 0x00 \ n:u32 \Rightarrow \{\min n, \max \epsilon\} \\
| 0x01 \ n:u32 \ m:u32 \Rightarrow \{\min n, \max m\}
\]

That is, a limits pair is encoded as either the byte 0x00 followed by the encoding of a $u32$ value, or the byte 0x01 followed by two such encodings. The variables $n$ and $m$ name the attributes of the respective $u32$ nonterminals, which in this case are the actual unsigned integers those decode into. The attribute of the complete production then is the abstract syntax for the limit, expressed in terms of the former values.

5.1.2 Auxiliary Notation

When dealing with binary encodings the following notation is also used:

• $\epsilon$ denotes the empty byte sequence.

• $||B||$ is the length of the byte sequence generated from the production $B$ in a derivation.

5.1.3 Vectors

Vectors are encoded with their $u32$ length followed by the encoding of their element sequence.

\[
\text{vec}(B) ::= n:u32 \ (x:B)^n \Rightarrow x^n
\]
5.2 Values

5.2.1 Bytes

Bytes encode themselves.

\[
\text{byte} ::= \text{0x00} \Rightarrow 0x00 \\
| \ldots \\
| 0xFF \Rightarrow 0xFF
\]

5.2.2 Integers

All integers are encoded using the LEB128\(^{28}\) variable-length integer encoding, in either unsigned or signed variant. Unsigned integers are encoded in unsigned LEB128\(^{29}\) format. As an additional constraint, the total number of bytes encoding a value of type \(u_N\) must not exceed \(\lceil N/7 \rceil\) bytes.

\[
\begin{align*}
\text{u}_N &::= \text{n:byte} \Rightarrow n \quad (\text{if } n < 2^7 \land n < 2^N) \\
&| \text{n:byte } m:u(N-7) \Rightarrow 2^7 \cdot m + (n - 2^7) \quad (\text{if } n \geq 2^7 \land N > 7)
\end{align*}
\]

Signed integers are encoded in signed LEB128\(^{30}\) format, which uses a two’s complement representation. As an additional constraint, the total number of bytes encoding a value of type \(s_N\) must not exceed \(\lceil N/7 \rceil\) bytes.

\[
\begin{align*}
\text{s}_N &::= \text{n:byte} \Rightarrow n \quad (\text{if } n < 2^6 \land n < 2^{N-1}) \\
&| \text{n:byte} \Rightarrow n - 2^7 \quad (\text{if } 2^6 \leq n < 2^7 \land n \geq 2^7 - 2^{N-1}) \\
&| \text{n:byte } m:s(N-7) \Rightarrow 2^7 \cdot m + (n - 2^7) \quad (\text{if } n \geq 2^7 \land N > 7)
\end{align*}
\]

Uninterpreted integers are encoded as signed integers.

\[
\text{i}_N ::= n:s_N \Rightarrow i \quad (\text{if } n = \text{signed}_N(i))
\]

Note: The side conditions \(N > 7\) in the productions for non-terminal bytes of the \(u\) and \(s\) encodings restrict the encoding’s length. However, “trailing zeros” are still allowed within these bounds. For example, 0x03 and 0x83 0x00 are both well-formed encodings for the value 3 as a \(u8\). Similarly, either of 0x7e and 0xFE 0xFF 0x7F and 0xFF 0xEF 0x7F are well-formed encodings of the value \(-2\) as a \(s16\).

The side conditions on the value \(n\) of terminal bytes further enforce that any unused bits in these bytes must be 0 for positive values and 1 for negative ones. For example, 0x83 0x10 is malformed as a \(u8\) encoding. Similarly, both 0x83 0x3E and 0xFF 0x7B are malformed as \(s8\) encodings.

5.2.3 Floating-Point

Floating-point values are encoded directly by their IEEE 754-2019\(^{31}\) (Section 3.4) bit pattern in little endian\(^{32}\) byte order:

\[
\text{f}_N ::= b^*:\text{byte}^{N/8} \Rightarrow \text{bytes}_{f_N}^{-1}(b^*)
\]

\(^{28}\) https://en.wikipedia.org/wiki/LEB128

\(^{29}\) https://en.wikipedia.org/wiki/LEB128#Unsigned_LEB128

\(^{30}\) https://en.wikipedia.org/wiki/LEB128#Signed_LEB128

\(^{31}\) https://ieeexplore.ieee.org/document/8766229

\(^{32}\) https://en.wikipedia.org/wiki/Endianness#Little-endian

---

5.2. Values
5.2.4 Names

Names are encoded as a vector of bytes containing the Unicode (Section 3.9) UTF-8 encoding of the name’s character sequence.

\[
\text{name} ::= b^* : \text{vec(byte)} \Rightarrow \text{name} \quad \text{if utf8(name) = b^*}
\]

The auxiliary utf8 function expressing this encoding is defined as follows:

\[
\begin{align*}
\text{utf8}(c^*) &= (\text{utf8}(c))^* \\
\text{utf8}(c) &= b \quad \text{if } c < U+80 \land c = b \\
\text{utf8}(c) &= b_1 b_2 \quad \text{if } U+80 \leq c < U+800 \land c = 2^6(b_1 - 0xC0) + (b_2 - 0x80) \\
\text{utf8}(c) &= b_1 b_2 b_3 \quad \text{if } U+800 \leq c < U+D800 \lor U+E000 \leq c < U+10000 \land c = 2^{12}(b_1 - 0x100) + 2^6(b_2 - 0x80) + (b_3 - 0x80) \\
\text{utf8}(c) &= b_1 b_2 b_3 b_4 \quad \text{if } U+10000 \leq c < U+110000 \land c = 2^{18}(b_1 - 0xF0) + 2^{12}(b_2 - 0x80) + 2^6(b_3 - 0x80) + (b_4 - 0x80)
\end{align*}
\]

Note: Unlike in some other formats, name strings are not 0-terminated.

5.3 Types

Note: In some places, possible types include both type constructors or types denoted by type indices. Thus, the binary format for type constructors corresponds to the encodings of small negative \(sN\) values, such that they can unambiguously occur in the same place as (positive) type indices.

5.3.1 Number Types

Number types are encoded by a single byte.

\[
\text{numtype} ::= 0x7F \Rightarrow \text{i32} \\
| \ 0x7E \Rightarrow \text{i64} \\
| \ 0x7D \Rightarrow \text{f32} \\
| \ 0x7C \Rightarrow \text{f64}
\]

5.3.2 Vector Types

Vector types are also encoded by a single byte.

\[
\text{vectype} ::= 0x7B \Rightarrow \text{v128}
\]

---

33 https://www.unicode.org/versions/latest/
5.3.3 Reference Types

Reference types are also encoded by a single byte.

```
reftype ::= 0x70 ⇒ funcref
    | 0x6F ⇒ externref
```

5.3.4 Value Types

Value types are encoded with their respective encoding as a number type, vector type, or reference type.

```
valtype ::= t:numtype ⇒ t
    | t:vectype ⇒ t
    | t:reftype ⇒ t
```

**Note:** Value types can occur in contexts where type indices are also allowed, such as in the case of block types. Thus, the binary format for types corresponds to the signed LEB128\(^{34}\) encoding of small negative \(sN\) values, so that they can coexist with (positive) type indices in the future.

5.3.5 Result Types

Result types are encoded by the respective vectors of value types.

```
resulttype ::= t*:vec(valtype) ⇒ [t*]
```

5.3.6 Function Types

Function types are encoded by the byte 0x60 followed by the respective vectors of parameter and result types.

```
functype ::= 0x60 rt1:resulttype rt2:resulttype ⇒ rt1 → rt2
```

5.3.7 Limits

Limits are encoded with a preceding flag indicating whether a maximum is present.

```
limits ::= 0x00 n:u32 ⇒ {min n, max e}
    | 0x01 n:u32 m:u32 ⇒ {min n, max m}
```

5.3.8 Memory Types

Memory types are encoded with their limits.

```
memtype ::= lim:limits ⇒ lim
```

5.3.9 Table Types

Table types are encoded with their limits and the encoding of their element reference type.

\[
\text{tabletype} ::= \text{et:reftype lim:limits} \Rightarrow \text{lim et}
\]

5.3.10 Global Types

Global types are encoded by their value type and a flag for their mutability.

\[
\text{globaltype} ::= \text{t:valtype m:mut} \Rightarrow \text{m t}
\]

\[
\text{mut} ::= 0x00 \Rightarrow \text{const}
| 0x01 \Rightarrow \text{var}
\]

5.4 Instructions

Instructions are encoded by opcodes. Each opcode is represented by a single byte, and is followed by the instruction’s immediate arguments, where present. The only exception are structured control instructions, which consist of several opcodes bracketing their nested instruction sequences.

**Note:** Gaps in the byte code ranges for encoding instructions are reserved for future extensions.

5.4.1 Control Instructions

Control instructions have varying encodings. For structured instructions, the instruction sequences forming nested blocks are terminated with explicit opcodes for end and else.

Block types are encoded in special compressed form, by either the byte 0x40 indicating the empty type, as a single value type, or as a type index encoded as a positive signed integer.

\[
\text{blocktype} ::= 0x40
| t:valtype
| x:s33
\]

\[
\text{instr} ::= 0x00 \Rightarrow \text{unreachable}
| 0x01 \Rightarrow \text{nop}
| 0x02 \text{bt:blocktype (in:instr)* 0x0B} \Rightarrow \text{block bt in* end}
| 0x03 \text{bt:blocktype (in:instr)* 0x0B} \Rightarrow \text{loop bt in* end}
| 0x04 \text{bt:blocktype (in1:instr)* 0x0B} \Rightarrow \text{if bt in* else \epsilon end}
| 0x05 \text{bt:blocktype (in1:instr)* 0x05 (in2:instr)* 0x0B} \Rightarrow \text{if bt in* else in* end}
| 0x0C \text{l:labelidx}
| 0x0D \text{l:labelidx}
| 0x0E \text{l*:vec(labelidx) lN:labelidx}
| 0x0F \Rightarrow \text{return}
| 0x10 \text{x:funcidx}
| 0x11 \text{y:typeidx x:tableidx}
\]

**Note:** The else opcode 0x05 in the encoding of an if instruction can be omitted if the following instruction sequence is empty.
Unlike any other occurrence, the type index in a block type is encoded as a positive signed integer, so that its signed LEB128 bit pattern cannot collide with the encoding of value types or the special code 0x40, which correspond to the LEB128 encoding of negative integers. To avoid any loss in the range of allowed indices, it is treated as a 33 bit signed integer.

### 5.4.2 Reference Instructions

Reference instructions are represented by single byte codes.

```
instr ::= ...
| 0xD0 t:reftype ⇒ ref.null t
| 0xD1 ⇒ ref.is_null
| 0xD2 x:funcidx ⇒ ref.func x
```

### 5.4.3 Parametric Instructions

Parametric instructions are represented by single byte codes, possibly followed by a type annotation.

```
instr ::= ...
| 0x1A ⇒ drop
| 0x1B ⇒ select
| 0x1C t*:vec(valtype) ⇒ select t*
```

### 5.4.4 Variable Instructions

Variable instructions are represented by byte codes followed by the encoding of the respective index.

```
instr ::= ...
| 0x20 x:localidx ⇒ local.get x
| 0x21 x:localidx ⇒ local.set x
| 0x22 x:localidx ⇒ local.tee x
| 0x23 x:globalidx ⇒ global.get x
| 0x24 x:globalidx ⇒ global.set x
```

### 5.4.5 Table Instructions

Table instructions are represented either by a single byte or a one byte prefix followed by a variable-length unsigned integer.

```
instr ::= ...
| 0x25 x:tableidx ⇒ table.get x
| 0x26 x:tableidx ⇒ table.set x
| 0xFC 12:u32 y:elemidx x:tableidx ⇒ table.init x y
| 0xFC 13:u32 x:elemidx ⇒ elem.drop x
| 0xFC 14:u32 x:tableidx y:tableidx ⇒ table.copy x y
| 0xFC 15:u32 x:tableidx ⇒ table.grow x
| 0xFC 16:u32 x:tableidx ⇒ table.size x
| 0xFC 17:u32 x:tableidx ⇒ table.fill x
```
5.4.6 Memory Instructions

Each variant of memory instruction is encoded with a different byte code. Loads and stores are followed by the encoding of their memarg immediate.

\[
\begin{align*}
\text{memarg} & ::= a:u32 \ o:u32 \Rightarrow \{\text{align } a, \text{ offset } o\} \\
\text{instr} & ::= \ldots \\
0x28 \ m: \text{memarg} & \Rightarrow \text{i32.load } m \\
0x29 \ m: \text{memarg} & \Rightarrow \text{i64.load } m \\
0x2A \ m: \text{memarg} & \Rightarrow \text{f32.load } m \\
0x2B \ m: \text{memarg} & \Rightarrow \text{f64.load } m \\
0x2C \ m: \text{memarg} & \Rightarrow \text{i32.load8 } s \ m \\
0x2D \ m: \text{memarg} & \Rightarrow \text{i32.load8 } u \ m \\
0x2E \ m: \text{memarg} & \Rightarrow \text{i32.load16 } s \ m \\
0x2F \ m: \text{memarg} & \Rightarrow \text{i32.load16 } u \ m \\
0x30 \ m: \text{memarg} & \Rightarrow \text{i64.load8 } s \ m \\
0x31 \ m: \text{memarg} & \Rightarrow \text{i64.load8 } u \ m \\
0x32 \ m: \text{memarg} & \Rightarrow \text{i64.load16 } s \ m \\
0x33 \ m: \text{memarg} & \Rightarrow \text{i64.load16 } u \ m \\
0x34 \ m: \text{memarg} & \Rightarrow \text{i64.load32 } s \ m \\
0x35 \ m: \text{memarg} & \Rightarrow \text{i64.load32 } u \ m \\
0x36 \ m: \text{memarg} & \Rightarrow \text{i32.store } m \\
0x37 \ m: \text{memarg} & \Rightarrow \text{i64.store } m \\
0x38 \ m: \text{memarg} & \Rightarrow \text{f32.store } m \\
0x39 \ m: \text{memarg} & \Rightarrow \text{f64.store } m \\
0x3A \ m: \text{memarg} & \Rightarrow \text{i32.store8 } m \\
0x3B \ m: \text{memarg} & \Rightarrow \text{i32.store16 } m \\
0x3C \ m: \text{memarg} & \Rightarrow \text{i64.store8 } m \\
0x3D \ m: \text{memarg} & \Rightarrow \text{i64.store16 } m \\
0x3E \ m: \text{memarg} & \Rightarrow \text{i64.store32 } m \\
0x3F 0x00 & \Rightarrow \text{memory.size} \\
0x40 0x00 & \Rightarrow \text{memory.grow} \\
0xFD 8:u32 x: \text{dataidx} 0x00 & \Rightarrow \text{memory.init } x \\
0xFD 9:u32 x: \text{dataidx} & \Rightarrow \text{data.drop } x \\
0xFD 10:u32 0x00 0x00 & \Rightarrow \text{memory.copy} \\
0xFD 11:u32 0x00 & \Rightarrow \text{memory.fill}
\end{align*}
\]

Note: In future versions of WebAssembly, the additional zero bytes occurring in the encoding of the memory.size, memory.grow, memory.copy, and memory.fill instructions may be used to index additional memories.

5.4.7 Numeric Instructions

All variants of numeric instructions are represented by separate byte codes.

The const instructions are followed by the respective literal.

\[
\begin{align*}
\text{instr} & ::= \ldots \\
0x41 \ n:i32 & \Rightarrow \text{i32.const } n \\
0x42 \ n:i64 & \Rightarrow \text{i64.const } n \\
0x43 \ z:f32 & \Rightarrow \text{f32.const } z \\
0x44 \ z:f64 & \Rightarrow \text{f64.const } z
\end{align*}
\]

All other numeric instructions are plain opcodes without any immediates.
### 5.4. Instructions

**Instr ::= ...**

<p>| 0x45  | i32.eqz |
| 0x46  | i32.eq  |
| 0x47  | i32.ne  |
| 0x48  | i32.lt_s |
| 0x49  | i32.lt_u |
| 0x4A  | i32.gt_s |
| 0x4B  | i32.gt_u |
| 0x4C  | i32.le_s |
| 0x4D  | i32.le_u |
| 0x4E  | i32.ge_s |
| 0x4F  | i32.ge_u |
| 0x50  | i64.eqz |
| 0x51  | i64.eq  |
| 0x52  | i64.ne  |
| 0x53  | i64.lt_s |
| 0x54  | i64.lt_u |
| 0x55  | i64.gt_s |
| 0x56  | i64.gt_u |
| 0x57  | i64.le_s |
| 0x58  | i64.le_u |
| 0x59  | i64.ge_s |
| 0x5A  | i64.ge_u |
| 0x5B  | f32.eq  |
| 0x5C  | f32.ne  |
| 0x5D  | f32.lt  |
| 0x5E  | f32.gt  |
| 0x5F  | f32.le  |
| 0x60  | f32.ge  |
| 0x61  | f64.eq  |
| 0x62  | f64.ne  |
| 0x63  | f64.lt  |
| 0x64  | f64.gt  |
| 0x65  | f64.le  |
| 0x66  | f64.ge  |
| 0x67  | i32.clz |
| 0x68  | i32.ctz |
| 0x69  | i32.popcnt |
| 0x6A  | i32.add |
| 0x6B  | i32.sub |
| 0x6C  | i32.mul |
| 0x6D  | i32.div_s |
| 0x6E  | i32.div_u |
| 0x6F  | i32.rem_s |
| 0x70  | i32.rem_u |
| 0x71  | i32.and |
| 0x72  | i32.or |
| 0x73  | i32.xor |
| 0x74  | i32.shl |
| 0x75  | i32.shr_s |
| 0x76  | i32.shr_u |
| 0x77  | i32.rotl |
| 0x78  | i32.rotr |</p>
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<tr>
<td>0x9D</td>
<td>f64.trunc</td>
</tr>
<tr>
<td>0x9E</td>
<td>f64.nearest</td>
</tr>
<tr>
<td>0x9F</td>
<td>f64.sqrt</td>
</tr>
<tr>
<td>0xA0</td>
<td>f64.add</td>
</tr>
<tr>
<td>0xA1</td>
<td>f64.sub</td>
</tr>
<tr>
<td>0xA2</td>
<td>f64.mul</td>
</tr>
<tr>
<td>0xA3</td>
<td>f64.div</td>
</tr>
<tr>
<td>0xA4</td>
<td>f64.min</td>
</tr>
<tr>
<td>0xA5</td>
<td>f64.max</td>
</tr>
<tr>
<td>0xA6</td>
<td>f64.copysign</td>
</tr>
</tbody>
</table>
The saturating truncation instructions all have a one byte prefix, whereas the actual opcode is encoded by a variable-length unsigned integer.

```
instr ::= ...
```

5.4.8 Vector Instructions

All variants of vector instructions are represented by separate byte codes. They all have a one byte prefix, whereas the actual opcode is encoded by a variable-length unsigned integer.

Vector loads and stores are followed by the encoding of their memory immediate.
The `const` instruction is followed by 16 immediate bytes, which are converted into a \( i128 \) in little-endian byte order:

```
instr ::= ...
| 0xFD 12:u32 (b:byte)\(^{16}\) ⇒ v128.const bytes\(^{16}\) of \( (b_0 \ldots b_{15}) \)
```

The `shuffle` instruction is also followed by the encoding of 16 `laneidx` immediates.

```
instr ::= ...
| 0xFD 13:u32 (i:laneidx)\(^{16}\) ⇒ i8x16.shuffle \(^{16}\)
```

`extract_lane` and `replace_lane` instructions are followed by the encoding of a `laneidx` immediate.

```
instr ::= ...
| 0xFD 21:u32 l:laneidx ⇒ i8x16.extract_lane_s l
| 0xFD 22:u32 l:laneidx ⇒ i8x16.extract_lane_u l
| 0xFD 23:u32 l:laneidx ⇒ i8x16.replace_lane l
| 0xFD 24:u32 l:laneidx ⇒ i16x8.extract_lane_s l
| 0xFD 25:u32 l:laneidx ⇒ i16x8.extract_lane_u l
| 0xFD 26:u32 l:laneidx ⇒ i16x8.replace_lane l
| 0xFD 27:u32 l:laneidx ⇒ i32x4.extract_lane l
| 0xFD 28:u32 l:laneidx ⇒ i32x4.replace_lane l
| 0xFD 29:u32 l:laneidx ⇒ i64x2.extract_lane l
| 0xFD 30:u32 l:laneidx ⇒ i64x2.replace_lane l
| 0xFD 31:u32 l:laneidx ⇒ f32x4.extract_lane l
| 0xFD 32:u32 l:laneidx ⇒ f32x4.replace_lane l
| 0xFD 33:u32 l:laneidx ⇒ f64x2.extract_lane l
| 0xFD 34:u32 l:laneidx ⇒ f64x2.replace_lane l
```
All other vector instructions are plain opcodes without any immediates.

\[
\text{instr} ::= \ldots
\]

\[
\begin{align*}
0xFD\ 14:u32 & \Rightarrow \text{i8x16.swizzle} \\
0xFD\ 15:u32 & \Rightarrow \text{i8x16.splat} \\
0xFD\ 16:u32 & \Rightarrow \text{i16x8.splat} \\
0xFD\ 17:u32 & \Rightarrow \text{i32x4.splat} \\
0xFD\ 18:u32 & \Rightarrow \text{i64x2.splat} \\
0xFD\ 19:u32 & \Rightarrow \text{f32x4.splat} \\
0xFD\ 20:u32 & \Rightarrow \text{f64x2.splat} \\
0xFD\ 35:u32 & \Rightarrow \text{i8x16.eq} \\
0xFD\ 36:u32 & \Rightarrow \text{i8x16.ne} \\
0xFD\ 37:u32 & \Rightarrow \text{i8x16.lt_s} \\
0xFD\ 38:u32 & \Rightarrow \text{i8x16.lt_u} \\
0xFD\ 39:u32 & \Rightarrow \text{i8x16.gt_s} \\
0xFD\ 40:u32 & \Rightarrow \text{i8x16.gt_u} \\
0xFD\ 41:u32 & \Rightarrow \text{i8x16.le_s} \\
0xFD\ 42:u32 & \Rightarrow \text{i8x16.le_u} \\
0xFD\ 43:u32 & \Rightarrow \text{i8x16.ge_s} \\
0xFD\ 44:u32 & \Rightarrow \text{i8x16.ge_u} \\
0xFD\ 45:u32 & \Rightarrow \text{i16x8.eq} \\
0xFD\ 46:u32 & \Rightarrow \text{i16x8.ne} \\
0xFD\ 47:u32 & \Rightarrow \text{i16x8.lt_s} \\
0xFD\ 48:u32 & \Rightarrow \text{i16x8.lt_u} \\
0xFD\ 49:u32 & \Rightarrow \text{i16x8.gt_s} \\
0xFD\ 50:u32 & \Rightarrow \text{i16x8.gt_u} \\
0xFD\ 51:u32 & \Rightarrow \text{i16x8.le_s} \\
0xFD\ 52:u32 & \Rightarrow \text{i16x8.le_u} \\
0xFD\ 53:u32 & \Rightarrow \text{i16x8.ge_s} \\
0xFD\ 54:u32 & \Rightarrow \text{i16x8.ge_u} \\
0xFD\ 55:u32 & \Rightarrow \text{i32x4.eq} \\
0xFD\ 56:u32 & \Rightarrow \text{i32x4.ne} \\
0xFD\ 57:u32 & \Rightarrow \text{i32x4.lt_s} \\
0xFD\ 58:u32 & \Rightarrow \text{i32x4.lt_u} \\
0xFD\ 59:u32 & \Rightarrow \text{i32x4.gt_s} \\
0xFD\ 60:u32 & \Rightarrow \text{i32x4.gt_u} \\
0xFD\ 61:u32 & \Rightarrow \text{i32x4.le_s} \\
0xFD\ 62:u32 & \Rightarrow \text{i32x4.le_u} \\
0xFD\ 63:u32 & \Rightarrow \text{i32x4.ge_s} \\
0xFD\ 64:u32 & \Rightarrow \text{i32x4.ge_u} \\
0xFD\ 214:u32 & \Rightarrow \text{i64x2.eq} \\
0xFD\ 215:u32 & \Rightarrow \text{i64x2.ne} \\
0xFD\ 216:u32 & \Rightarrow \text{i64x2.lt_s} \\
0xFD\ 217:u32 & \Rightarrow \text{i64x2.gt_s} \\
0xFD\ 218:u32 & \Rightarrow \text{i64x2.le_s} \\
0xFD\ 219:u32 & \Rightarrow \text{i64x2.ge_s} \\
0xFD\ 65:u32 & \Rightarrow \text{f32x4.eq} \\
0xFD\ 66:u32 & \Rightarrow \text{f32x4.ne} \\
0xFD\ 67:u32 & \Rightarrow \text{f32x4.lt} \\
0xFD\ 68:u32 & \Rightarrow \text{f32x4.gt} \\
0xFD\ 69:u32 & \Rightarrow \text{f32x4.le} \\
0xFD\ 70:u32 & \Rightarrow \text{f32x4.ge}
\end{align*}
\]
| 0xFD 71:u32 ⇒ f64x2.eq |
| 0xFD 72:u32 ⇒ f64x2.ne |
| 0xFD 73:u32 ⇒ f64x2.lt |
| 0xFD 74:u32 ⇒ f64x2.gt |
| 0xFD 75:u32 ⇒ f64x2.le |
| 0xFD 76:u32 ⇒ f64x2.ge |

| 0xFD 77:u32 ⇒ v128.not |
| 0xFD 78:u32 ⇒ v128.and |
| 0xFD 79:u32 ⇒ v128.andnot |
| 0xFD 80:u32 ⇒ v128.or |
| 0xFD 81:u32 ⇒ v128.xor |
| 0xFD 82:u32 ⇒ v128.bitselect |
| 0xFD 83:u32 ⇒ v128.any_true |

| 0xFD 96:u32 ⇒ i8x16.abs |
| 0xFD 97:u32 ⇒ i8x16.neg |
| 0xFD 98:u32 ⇒ i8x16.popcnt |
| 0xFD 99:u32 ⇒ i8x16.all_true |
| 0xFD 100:u32 ⇒ i8x16.bitmask |
| 0xFD 101:u32 ⇒ i8x16.narrow_i16x8_s |
| 0xFD 102:u32 ⇒ i8x16.narrow_i16x8_u |
| 0xFD 107:u32 ⇒ i8x16.shl |
| 0xFD 108:u32 ⇒ i8x16.shr_s |
| 0xFD 109:u32 ⇒ i8x16.shr_u |
| 0xFD 110:u32 ⇒ i8x16.add |
| 0xFD 111:u32 ⇒ i8x16.add_sat_s |
| 0xFD 112:u32 ⇒ i8x16.add_sat_u |
| 0xFD 113:u32 ⇒ i8x16.sub |
| 0xFD 114:u32 ⇒ i8x16.sub_sat_s |
| 0xFD 115:u32 ⇒ i8x16.sub_sat_u |
| 0xFD 118:u32 ⇒ i8x16.min_s |
| 0xFD 119:u32 ⇒ i8x16.min_u |
| 0xFD 120:u32 ⇒ i8x16.max_s |
| 0xFD 121:u32 ⇒ i8x16.max_u |
| 0xFD 123:u32 ⇒ i8x16.avgr_u |
5.4. Instructions

```plaintext
0xFD 124:u32  ⇒  i16x8.extadd_pairwise_i8x16_s
0xFD 125:u32  ⇒  i16x8.extadd_pairwise_i8x16_u
0xFD 128:u32  ⇒  i16x8.abs
0xFD 129:u32  ⇒  i16x8.neg
0xFD 130:u32  ⇒  i16x8.q15mulr_sat_s
0xFD 131:u32  ⇒  i16x8.all_true
0xFD 132:u32  ⇒  i16x8.bitmask
0xFD 133:u32  ⇒  i16x8.narrow_i32x4_s
0xFD 134:u32  ⇒  i16x8.narrow_i32x4_u
0xFD 135:u32  ⇒  i16x8.extend_low_i8x16_s
0xFD 136:u32  ⇒  i16x8.extend_high_i8x16_s
0xFD 137:u32  ⇒  i16x8.extend_low_i8x16_u
0xFD 138:u32  ⇒  i16x8.extend_high_i8x16_u
0xFD 139:u32  ⇒  i16x8 shl
0xFD 140:u32  ⇒  i16x8.shr_s
0xFD 141:u32  ⇒  i16x8.shr_u
0xFD 142:u32  ⇒  i16x8.add
0xFD 143:u32  ⇒  i16x8.add_sat_s
0xFD 144:u32  ⇒  i16x8.add_sat_u
0xFD 145:u32  ⇒  i16x8.sub
0xFD 146:u32  ⇒  i16x8.sub_sat_s
0xFD 147:u32  ⇒  i16x8.sub_sat_u
0xFD 149:u32  ⇒  i16x8.mul
0xFD 150:u32  ⇒  i16x8.min_s
0xFD 151:u32  ⇒  i16x8.min_u
0xFD 152:u32  ⇒  i16x8.max_s
0xFD 153:u32  ⇒  i16x8.max_u
0xFD 155:u32  ⇒  i16x8.avgr_u
0xFD 156:u32  ⇒  i16x8.extmul_low_i8x16_s
0xFD 157:u32  ⇒  i16x8.extmul_high_i8x16_s
0xFD 158:u32  ⇒  i16x8.extmul_low_i8x16_u
0xFD 159:u32  ⇒  i16x8.extmul_high_i8x16_u
0xFD 126:u32  ⇒  i32x4.extadd_pairwise_i32x16_s
0xFD 127:u32  ⇒  i32x4.extadd_pairwise_i32x16_u
0xFD 160:u32  ⇒  i32x4.abs
0xFD 161:u32  ⇒  i32x4.neg
0xFD 163:u32  ⇒  i32x4.all_true
0xFD 164:u32  ⇒  i32x4.bitmask
0xFD 167:u32  ⇒  i32x4.extend_low_i16x8_s
0xFD 168:u32  ⇒  i32x4.extend_high_i16x8_s
0xFD 169:u32  ⇒  i32x4.extend_low_i16x8_u
0xFD 170:u32  ⇒  i32x4.extend_high_i16x8_u
0xFD 171:u32  ⇒  i32x4 shl
0xFD 172:u32  ⇒  i32x4.shr_s
0xFD 173:u32  ⇒  i32x4.shr_u
0xFD 174:u32  ⇒  i32x4.add
0xFD 177:u32  ⇒  i32x4.sub
0xFD 181:u32  ⇒  i32x4.mul
0xFD 182:u32  ⇒  i32x4.min_s
0xFD 183:u32  ⇒  i32x4.min_u
0xFD 184:u32  ⇒  i32x4.max_s
0xFD 185:u32  ⇒  i32x4.max_u
0xFD 186:u32  ⇒  i32x4.dot_i16x8_s
0xFD 188:u32  ⇒  i32x4.dotmul_low_i16x8_s
0xFD 189:u32  ⇒  i32x4.dotmul_high_i16x8_s
0xFD 190:u32  ⇒  i32x4.dotmul_low_i16x8_u
0xFD 191:u32  ⇒  i32x4.dotmul_high_i16x8_u
```
| 0xFD 192:u32 | i64x2.abs |
| 0xFD 193:u32 | i64x2.neg |
| 0xFD 195:u32 | i64x2.all_true |
| 0xFD 196:u32 | i64x2.bitmask |
| 0xFD 199:u32 | i64x2.extend_low_i32x4_s |
| 0xFD 200:u32 | i64x2.extend_high_i32x4_s |
| 0xFD 201:u32 | i64x2.extend_low_i32x4_u |
| 0xFD 202:u32 | i64x2.extend_high_i32x4_u |
| 0xFD 203:u32 | i64x2.shl |
| 0xFD 204:u32 | i64x2.shr_s |
| 0xFD 205:u32 | i64x2.shr_u |
| 0xFD 206:u32 | i64x2.add |
| 0xFD 209:u32 | i64x2.sub |
| 0xFD 213:u32 | i64x2.mul |
| 0xFD 220:u32 | i64x2.extmul_low_i32x4_s |
| 0xFD 221:u32 | i64x2.extmul_high_i32x4_s |
| 0xFD 222:u32 | i64x2.extmul_low_i32x4_u |
| 0xFD 223:u32 | i64x2.extmul_high_i32x4_u |
| 0xFD 103:u32 | f32x4.ceil |
| 0xFD 104:u32 | f32x4.floor |
| 0xFD 105:u32 | f32x4.trunc |
| 0xFD 106:u32 | f32x4.nearest |
| 0xFD 224:u32 | f32x4.abs |
| 0xFD 225:u32 | f32x4.neg |
| 0xFD 227:u32 | f32x4.sqrt |
| 0xFD 228:u32 | f32x4.add |
| 0xFD 229:u32 | f32x4.sub |
| 0xFD 230:u32 | f32x4.mul |
| 0xFD 231:u32 | f32x4.div |
| 0xFD 232:u32 | f32x4.min |
| 0xFD 233:u32 | f32x4.max |
| 0xFD 234:u32 | f32x4.pmin |
| 0xFD 235:u32 | f32x4.pmax |
| 0xFD 116:u32 | f64x2.ceil |
| 0xFD 117:u32 | f64x2.floor |
| 0xFD 122:u32 | f64x2.trunc |
| 0xFD 148:u32 | f64x2.nearest |
| 0xFD 236:u32 | f64x2.abs |
| 0xFD 237:u32 | f64x2.neg |
| 0xFD 239:u32 | f64x2.sqrt |
| 0xFD 240:u32 | f64x2.add |
| 0xFD 241:u32 | f64x2.sub |
| 0xFD 242:u32 | f64x2.mul |
| 0xFD 243:u32 | f64x2.div |
| 0xFD 244:u32 | f64x2.min |
| 0xFD 245:u32 | f64x2.max |
| 0xFD 246:u32 | f64x2.pmin |
| 0xFD 247:u32 | f64x2.pmax |
5.4.9 Expressions

Expressions are encoded by their instruction sequence terminated with an explicit 0x0B opcode for end.

\[ \text{expr} ::= (\text{in:instr}^*) \text{ 0x0B} \Rightarrow \text{in}^* \text{ end} \]

5.5 Modules

The binary encoding of modules is organized into sections. Most sections correspond to one component of a module record, except that function definitions are split into two sections, separating their type declarations in the function section from their bodies in the code section.

Note: This separation enables parallel and streaming compilation of the functions in a module.

5.5.1 Indices

All indices are encoded with their respective value.

\[
\begin{align*}
\text{typeid} & ::= x:u32 \Rightarrow x \\
\text{funcidx} & ::= x:u32 \Rightarrow x \\
\text{tableidx} & ::= x:u32 \Rightarrow x \\
\text{memidx} & ::= x:u32 \Rightarrow x \\
\text{globalidx} & ::= x:u32 \Rightarrow x \\
\text{elemidx} & ::= x:u32 \Rightarrow x \\
\text{dataidx} & ::= x:u32 \Rightarrow x \\
\text{localidx} & ::= x:u32 \Rightarrow x \\
\text{labelidx} & ::= l:u32 \Rightarrow l
\end{align*}
\]

5.5.2 Sections

Each section consists of

- a one-byte section id,
- the u32 size of the contents, in bytes,
- the actual contents, whose structure is depended on the section id.

Every section is optional; an omitted section is equivalent to the section being present with empty contents.
The following parameterized grammar rule defines the generic structure of a section with id $N$ and contents described by the grammar $B$.

$$\text{section}_N(B) ::= N:\text{byte size:u32 cont:B} \Rightarrow \text{cont (if size = } |B||)$$

For most sections, the contents $B$ encodes a vector. In these cases, the empty result $\epsilon$ is interpreted as the empty vector.

**Note:** Other than for unknown custom sections, the size is not required for decoding, but can be used to skip sections when navigating through a binary. The module is malformed if the size does not match the length of the binary contents $B$.

The following section ids are used:

<table>
<thead>
<tr>
<th>Id</th>
<th>Section</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>custom section</td>
</tr>
<tr>
<td>1</td>
<td>type section</td>
</tr>
<tr>
<td>2</td>
<td>import section</td>
</tr>
<tr>
<td>3</td>
<td>function section</td>
</tr>
<tr>
<td>4</td>
<td>table section</td>
</tr>
<tr>
<td>5</td>
<td>memory section</td>
</tr>
<tr>
<td>6</td>
<td>global section</td>
</tr>
<tr>
<td>7</td>
<td>export section</td>
</tr>
<tr>
<td>8</td>
<td>start section</td>
</tr>
<tr>
<td>9</td>
<td>element section</td>
</tr>
<tr>
<td>10</td>
<td>code section</td>
</tr>
<tr>
<td>11</td>
<td>data section</td>
</tr>
<tr>
<td>12</td>
<td>data count section</td>
</tr>
</tbody>
</table>

### 5.5.3 Custom Section

Custom sections have the id 0. They are intended to be used for debugging information or third-party extensions, and are ignored by the WebAssembly semantics. Their contents consist of a name further identifying the custom section, followed by an uninterpreted sequence of bytes for custom use.

$$\text{customsec ::= section}_0(\text{custom})$$

$$\text{custom ::= name byte}^*$$

**Note:** If an implementation interprets the data of a custom section, then errors in that data, or the placement of the section, must not invalidate the module.

### 5.5.4 Type Section

The type section has the id 1. It decodes into a vector of function types that represent the types component of a module.

$$\text{typesec ::= ft*:section}_1(\text{vec(functype)}) \Rightarrow ft^*$$
5.5.5 Import Section

The import section has the id 2. It decodes into a vector of imports that represent the imports component of a module.

\[
\text{importsec} ::= \text{im}^*:\text{section}_2(\text{vec(import)}) \Rightarrow \text{im}^* \\
\text{import} ::= \text{mod:name nm:name d:importdesc} \Rightarrow \{\text{module mod, name nm, desc d}\} \\
\text{importdesc} ::= 0x00 x:typeidx \Rightarrow \text{func x} \\
| 0x01 tt:tabletype \Rightarrow \text{table tt} \\
| 0x02 mt:memtype \Rightarrow \text{mem mt} \\
| 0x03 gt:globaltype \Rightarrow \text{global gt}
\]

5.5.6 Function Section

The function section has the id 3. It decodes into a vector of type indices that represent the type fields of the functions in the funcs component of a module. The locals and body fields of the respective functions are encoded separately in the code section.

\[
\text{funcsec} ::= x^*:\text{section}_3(\text{vec(typeidx)}) \Rightarrow x^*
\]

5.5.7 Table Section

The table section has the id 4. It decodes into a vector of tables that represent the tables component of a module.

\[
\text{tablesec} ::= \text{tab}^*:\text{section}_4(\text{vec(table)}) \Rightarrow \text{tab}^* \\
\text{table} ::= \text{tt:tabletype} \Rightarrow \{\text{type tt}\}
\]

5.5.8 Memory Section

The memory section has the id 5. It decodes into a vector of memories that represent the mems component of a module.

\[
\text{memsec} ::= \text{mem}^*:\text{section}_5(\text{vec(mem)}) \Rightarrow \text{mem}^* \\
\text{mem} ::= \text{mt:memtype} \Rightarrow \{\text{type mt}\}
\]

5.5.9 Global Section

The global section has the id 6. It decodes into a vector of globals that represent the globals component of a module.

\[
\text{globalsec} ::= \text{glob}^*:\text{section}_6(\text{vec(global)}) \Rightarrow \text{glob}^* \\
\text{global} ::= \text{gt:globaltype e:expr} \Rightarrow \{\text{type gt, init e}\}
\]
5.5.10 Export Section

The export section has the id 7. It decodes into a vector of exports that represent the exports component of a module.

\[
\text{exportsec} ::= \text{ex}*:\text{section}_7(\text{vec}(\text{export})) \Rightarrow \text{ex}^
\]

\[
\text{export} ::= \text{nm} \cdot \text{name} \cdot d::\text{exportdesc} \Rightarrow \{\text{name nm}, \text{desc } d\}
\]

\[
\text{exportdesc} ::= 0x00 x::funcidx \Rightarrow \text{func } x
\]

| 0x01 x::tableidx \Rightarrow \text{table } x |
| 0x02 x::memidx \Rightarrow \text{mem } x |
| 0x03 x::globalidx \Rightarrow \text{global } x |

5.5.11 Start Section

The start section has the id 8. It decodes into an optional start function that represents the start component of a module.

\[
\text{startsec} ::= \text{st?} :: \text{section}_8(\text{start}) \Rightarrow \text{st?}
\]

\[
\text{start} ::= x::funcidx \Rightarrow \{\text{func } x\}
\]

5.5.12 Element Section

The element section has the id 9. It decodes into a vector of element segments that represent the elems component of a module.

\[
\text{elemsec} ::= \text{seg}*:\text{section}_9(\text{vec}(\text{elem})) \Rightarrow \text{seg}
\]

\[
\text{elem} ::= 0x00 c::\text{expr } y::\text{vec(funcidx)} \Rightarrow \{\text{type funcref, init } ((\text{ref.func } y) \text{ end})*, \text{mode active } \{\text{table } 0, \text{offset } e\}\}
\]

| 0x01 et::elemkind y::vec(funcidx) \Rightarrow \{\text{type et, init } ((\text{ref.func } y) \text{ end})*, \text{mode passive} \} |
| 0x02 x::tableidx c::\text{expr } et::elemkind y::vec(funcidx) \Rightarrow \{\text{type et, init } ((\text{ref.func } y) \text{ end})*, \text{mode active } \{\text{table } x, \text{offset } e\}\}
| 0x03 et::elemkind y::vec(funcidx) \Rightarrow \{\text{type et, init } ((\text{ref.func } y) \text{ end})*, \text{mode declarative} \}
| 0x04 c::\text{expr } el::vec(\text{expr}) \Rightarrow \{\text{type funcref, init } el, \text{mode active } \{\text{table } 0, \text{offset } e\}\}
| 0x05 et::reftype el::vec(\text{expr}) \Rightarrow \{\text{type et, init } el, \text{mode passive} \}
| 0x06 x::tableidx c::\text{expr } et::\text{reftype el}::\text{vec}(\text{expr}) \Rightarrow \{\text{type et, init } el, \text{mode active } \{\text{table } x, \text{offset } e\}\}
| 0x07 et::\text{reftype el}::\text{vec}(\text{expr}) \Rightarrow \{\text{type et, init } el, \text{mode declarative} \}

\[
\text{elemkind} ::= 0x00 \Rightarrow \text{funcref}
\]

Note: The initial byte can be interpreted as a bitfield. Bit 0 indicates a passive or declarative segment, bit 1 indicates the presence of an explicit table index for an active segment and otherwise distinguishes passive from declarative segments, bit 2 indicates the use of element type and element expressions instead of element kind and element indices.

Additional element kinds may be added in future versions of WebAssembly.
5.5.13 Code Section

The code section has the id 10. It decodes into a vector of code entries that are pairs of value type vectors and expressions. They represent the locals and body field of the functions in the funcs component of a module. The type fields of the respective functions are encoded separately in the function section.

The encoding of each code entry consists of

- the u32 size of the function code in bytes,
- the actual function code, which in turn consists of
  - the declaration of locals,
  - the function body as an expression.

Local declarations are compressed into a vector whose entries consist of

- a u32 count,
- a value type,

denoting count locals of the same value type.

Here, code ranges over pairs (valtype*, expr). The meta function concat((t*)*) concatenates all sequences t*i in (t*)*. Any code for which the length of the resulting sequence is out of bounds of the maximum size of a vector is malformed.

Note: Like with sections, the code size is not needed for decoding, but can be used to skip functions when navigating through a binary. The module is malformed if a size does not match the length of the respective function code.

5.5.14 Data Section

The data section has the id 11. It decodes into a vector of data segments that represent the datas component of a module.

Here, data ranges over pairs (valtype*, expr). The meta function concat((t*)*) concatenates all sequences t*i in (t*)*. Any code for which the length of the resulting sequence is out of bounds of the maximum size of a vector is malformed.

Note: The initial byte can be interpreted as a bitfield. Bit 0 indicates a passive segment, bit 1 indicates the presence of an explicit memory index for an active segment.

In the current version of WebAssembly, at most one memory may be defined or imported in a single module, so all valid active data segments have a memory value of 0.
5.5.15 Data Count Section

The data count section has the id 12. It decodes into an optional \( \text{u32} \) that represents the number of data segments in the data section. If this count does not match the length of the data segment vector, the module is malformed.

\[
\text{datacountsec} ::= \ n^? : \text{section}^{12}(\text{u32}) \Rightarrow n^?
\]

**Note:** The data count section is used to simplify single-pass validation. Since the data section occurs after the code section, the memory.init and data.drop instructions would not be able to check whether the data segment index is valid until the data section is read. The data count section occurs before the code section, so a single-pass validator can use this count instead of deferring validation.

5.5.16 Modules

The encoding of a module starts with a preamble containing a 4-byte magic number (the string ‘\0asm’) and a version field. The current version of the WebAssembly binary format is 1.

The preamble is followed by a sequence of sections. Custom sections may be inserted at any place in this sequence, while other sections must occur at most once and in the prescribed order. All sections can be empty.

The lengths of vectors produced by the (possibly empty) function and code section must match up.

Similarly, the optional data count must match the length of the data segment vector. Furthermore, it must be present
if any dataindex < syntax − dataidx > occurs in the code section.

\[
\text{magic} \ ::= \ 0x00 \ 0x61 \ 0x73 \ 0x6D \\
\text{version} \ ::= \ 0x01 \ 0x00 \ 0x00 \ 0x00 \\
\text{module} \ ::= \ \text{magic} \\
\text{version} \\
\text{customsec}^* \\
\text{functype}^* : \text{typesec} \\
\text{customsec}^* \\
\text{import}^* : \text{importsec} \\
\text{customsec}^* \\
\text{typeidx}^n : \text{funcsec} \\
\text{customsec}^* \\
\text{table} : \text{tablesec} \\
\text{customsec}^* \\
\text{mem} : \text{memsec} \\
\text{customsec}^* \\
\text{global}^* : \text{globalsec} \\
\text{customsec}^* \\
\text{export}^* : \text{exportsec} \\
\text{customsec}^* \\
\text{start}^* : \text{startsec} \\
\text{customsec}^* \\
\text{elem}^* : \text{elemsec} \\
\text{customsec}^* \\
\text{m}^* : \text{datacountsec} \\
\text{customsec}^* \\
\text{code}^m : \text{codesec} \\
\text{customsec}^* \\
\text{data}^m : \text{datasec} \\
\text{customsec}^* \\
\Rightarrow \{ \text{types functype}^*, \\
\text{funcs } \text{func}^n, \\
\text{tables table}^*, \\
\text{mems mem}^*, \\
\text{globals } \text{global}^*, \\
\text{elems elem}^*, \\
\text{datas data}^m, \\
\text{start start}? , \\
\text{imports } \text{import}^*, \\
\text{exports } \text{export}? \} \\
\text{(if } m^? \neq \epsilon \lor \text{dataidx}(\text{code}^n) = 0) \\
\]

where for each \(t^*_i, e_i\) in \(\text{code}^n\),

\[
\text{func}^n[i] = \{ \text{type typeidx}^n[i], \text{locals } t^*_i, \text{body } e_i \}\}
\]

\textbf{Note:} The version of the WebAssembly binary format may increase in the future if backward-incompatible changes have to be made to the format. However, such changes are expected to occur very infrequently, if ever. The binary format is intended to be forward-compatible, such that future extensions can be made without incrementing its version.
6.1 Conventions

The textual format for WebAssembly modules is a rendering of their abstract syntax into S-expressions. Like the binary format, the text format is defined by an attribute grammar. A text string is a well-formed description of a module if and only if it is generated by the grammar. Each production of this grammar has at most one synthesized attribute: the abstract syntax that the respective character sequence expresses. Thus, the attribute grammar implicitly defines a parsing function. Some productions also take a context as an inherited attribute that records bound identifiers.

Except for a few exceptions, the core of the text grammar closely mirrors the grammar of the abstract syntax. However, it also defines a number of abbreviations that are “syntactic sugar” over the core syntax.

The recommended extension for files containing WebAssembly modules in text format is “.wat”. Files with this extension are assumed to be encoded in UTF-8, as per Unicode (Section 2.5).

6.1.1 Grammar

The following conventions are adopted in defining grammar rules of the text format. They mirror the conventions used for abstract syntax and for the binary format. In order to distinguish symbols of the textual syntax from symbols of the abstract syntax, typewriter font is adopted for the former.

- Terminal symbols are either literal strings of characters enclosed in quotes or expressed as Unicode scalar values: ‘module’, U+0A. (All characters written literally are unambiguously drawn from the 7-bit ASCII subset of Unicode.)
- Nonterminal symbols are written in typewriter font: valtype, instr.
- $T^n$ is a sequence of $n \geq 0$ iterations of $T$.
- $T^*$ is a possibly empty sequence of iterations of $T$. (This is a shorthand for $T^n$ used where $n$ is not relevant.)
- $T^+$ is a sequence of one or more iterations of $T$. (This is a shorthand for $T^n$ where $n \geq 1$.)
- $T?$ is an optional occurrence of $T$. (This is a shorthand for $T^n$ where $n \leq 1$.)

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36 https://www.unicode.org/versions/latest/
37 https://www.unicode.org/versions/latest/
38 https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%.5bR2012%.5d
• \( x: T \) denotes the same language as the nonterminal \( T \), but also binds the variable \( x \) to the attribute synthesized for \( T \).

• Productions are written \( \text{sym} ::= T_1 \Rightarrow A_1 | \ldots | T_n \Rightarrow A_n \), where each \( A_i \) is the attribute that is synthesized for \( \text{sym} \) in the given case, usually from attribute variables bound in \( T_i \).

• Some productions are augmented by side conditions in parentheses, which restrict the applicability of the production. They provide a shorthand for a combinatorial expansion of the production into many separate cases.

• If the same meta variable or non-terminal symbol appears multiple times in a production (in the syntax or in an attribute), then all those occurrences must have the same instantiation.

• A distinction is made between lexical and syntactic productions. For the latter, arbitrary white space is allowed in any place where the grammar contains spaces. The productions defining lexical syntax and the syntax of values are considered lexical, all others are syntactic.

Note: For example, the textual grammar for number types is given as follows:

\[
\text{numtype} ::= \text{'i32'} \Rightarrow \text{i32} \\
| \text{'i64'} \Rightarrow \text{i64} \\
| \text{'f32'} \Rightarrow \text{f32} \\
| \text{'f64'} \Rightarrow \text{f64}
\]

The textual grammar for limits is defined as follows:

\[
\text{limits} ::= n: \text{u32} \Rightarrow \{\text{min } n, \text{max } \epsilon\} \\
| n: \text{u32} m: \text{u32} \Rightarrow \{\text{min } n, \text{max } m\}
\]

The variables \( n \) and \( m \) name the attributes of the respective \( \text{u32} \) nonterminals, which in this case are the actual unsigned integers those parse into. The attribute of the complete production then is the abstract syntax for the limit, expressed in terms of the former values.

### 6.1.2 Abbreviations

In addition to the core grammar, which corresponds directly to the abstract syntax, the textual syntax also defines a number of abbreviations that can be used for convenience and readability.

Abbreviations are defined by rewrite rules specifying their expansion into the core syntax:

\[
\text{abbreviation syntax} \equiv \text{expanded syntax}
\]

These expansions are assumed to be applied, recursively and in order of appearance, before applying the core grammar rules to construct the abstract syntax.

### 6.1.3 Contexts

The text format allows the use of symbolic identifiers in place of indices. To resolve these identifiers into concrete indices, some grammar production are indexed by an identifier context \( I \) as a synthesized attribute that records the declared identifiers in each index space. In addition, the context records the types defined in the module, so that parameter indices can be computed for functions.
It is convenient to define identifier contexts as *records* \( I \) with abstract syntax as follows:

\[
\begin{align*}
I & ::= \{ \text{types} (\text{id}^*)^*, \\
& \quad \text{funcs} (\text{id}^*)^*, \\
& \quad \text{tables} (\text{id}^*)^*, \\
& \quad \text{mems} (\text{id}^*)^*, \\
& \quad \text{globals} (\text{id}^*)^*, \\
& \quad \text{elem} (\text{id}^*)^*, \\
& \quad \text{data} (\text{id}^*)^*, \\
& \quad \text{locals} (\text{id}^*)^*, \\
& \quad \text{labels} (\text{id}^*)^*, \\
& \quad \text{typedefs} \ \text{functype}^* \} \\
\end{align*}
\]

For each index space, such a context contains the list of *identifiers* assigned to the defined indices. Unnamed indices are associated with empty (\( \epsilon \)) entries in these lists.

An identifier context is *well-formed* if no index space contains duplicate identifiers.

**Conventions**

To avoid unnecessary clutter, empty components are omitted when writing out identifier contexts. For example, the record \( \{ \} \) is shorthand for an identifier context whose components are all empty.

### 6.1.4 Vectors

*Vectors* are written as plain sequences, but with a restriction on the length of these sequence.

\[
\text{vec}(A) ::= (x:A)^n \Rightarrow x^n \quad \text{(if } n < 2^{32})
\]

### 6.2 Lexical Format

#### 6.2.1 Characters

The text format assigns meaning to *source text*, which consists of a sequence of *characters*. Characters are assumed to be represented as valid *Unicode*\(^{39}\) (Section 2.4) *scalar values*.

\[
\begin{align*}
\text{source} & ::= \text{char}^* \\
\text{char} & ::= U+00 \mid \ldots \mid U+D7FF \mid U+E000 \mid \ldots \mid U+10FFFF
\end{align*}
\]

**Note:** While source text may contain any Unicode character in *comments* or *string* literals, the rest of the grammar is formed exclusively from the characters supported by the 7-bit *ASCII*\(^{40}\) subset of Unicode.

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\(^{39}\) https://www.unicode.org/versions/latest/

\(^{40}\) https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d
6.2.2 Tokens

The character stream in the source text is divided, from left to right, into a sequence of tokens, as defined by the following grammar.

\[
\begin{align*}
\text{token} & := \text{keyword} | uN | sN | fN | \text{string} | \text{id} | '(' | ')' | \text{reserved} \\
\text{keyword} & := ('a' | \ldots | 'z') \text{idchar}^* \\
\text{reserved} & := \text{idchar}^*
\end{align*}
\]

Tokens are formed from the input character stream according to the longest match rule. That is, the next token always consists of the longest possible sequence of characters that is recognized by the above lexical grammar. Tokens can be separated by whitespace, but except for strings, they cannot themselves contain whitespace.

The set of keyword tokens is defined implicitly, by all occurrences of a terminal symbol in literal form, such as ‘keyword’, in a syntactic production of this chapter.

Any token that does not fall into any of the other categories is considered reserved, and cannot occur in source text.

Note: The effect of defining the set of reserved tokens is that all tokens must be separated by either parentheses or whitespace. For example, ‘0$z’ is a single reserved token. Consequently, it is not recognized as two separate tokens ‘0’ and ‘$z’, but instead disallowed. This property of tokenization is not affected by the fact that the definition of reserved tokens overlaps with other token classes.

6.2.3 White Space

Whitespace is any sequence of literal space characters, formatting characters, or comments. The allowed formatting characters correspond to a subset of the ASCII format effectors, namely, horizontal tabulation (U+09), line feed (U+0A), and carriage return (U+0D).

\[
\begin{align*}
\text{space} & := (' ' | \text{format} | \text{comment})^* \\
\text{format} & := \text{U+09 | U+0A | U+0D}
\end{align*}
\]

The only relevance of white space is to separate tokens. It is otherwise ignored.

6.2.4 Comments

A comment can either be a line comment, started with a double semicolon ‘;;’ and extending to the end of the line, or a block comment, enclosed in delimiters ‘(( . . . ))’. Block comments can be nested.

\[
\begin{align*}
\text{comment} & := \text{linecomment} | \text{blockcomment} \\
\text{linecomment} & := ';;' \text{linechar}^* (\text{U+0A | } \text{eof}) \\
\text{linechar} & := \text{cchar} \\
\text{blockcomment} & := '(( blockschar+ '))' \\
\text{blockschar} & := \text{cchar} \\
\text{comment} & := \text{cchar} \\
| ';;' & (if \text{c} \neq \text{U+0A}) \\
| '(' & (if the next character is not ‘)’) \\
| ')' & (if the next character is not ‘;)’) \\
| \text{blockcomment}
\end{align*}
\]

Here, the pseudo token eof indicates the end of the input. The look-ahead restrictions on the productions for blockschar disambiguate the grammar such that only well-bracketed uses of block comment delimiters are allowed.

Note: Any formatting and control characters are allowed inside comments.

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41 https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d
6.3 Values

The grammar productions in this section define lexical syntax, hence no white space is allowed.

6.3.1 Integers

All integers can be written in either decimal or hexadecimal notation. In both cases, digits can optionally be separated by underscores.

\[
\begin{align*}
\text{sign} & ::= \varepsilon \Rightarrow + \mid \text{'+'} \Rightarrow + \mid \text{'-'} \Rightarrow - \\
\text{digit} & ::= '0' \Rightarrow 0 \mid \ldots \mid '9' \Rightarrow 9 \\
\text{hexdigit} & ::= d:digit \Rightarrow d \\
\text{num} & ::= d:digit \Rightarrow d \\
\text{hexnum} & ::= h:hexdigit \\
\text{n:hexnum} & ::= n:hexnum \mid n:hexnum \cdot h:hexdigit \Rightarrow 16 \cdot n + h \\
\text{num} & ::= n:hexnum \cdot d:digit \Rightarrow 10 \cdot n + d
\end{align*}
\]

The allowed syntax for integer literals depends on size and signedness. Moreover, their value must lie within the range of the respective type.

\[
\begin{align*}
\text{uN} & ::= n: num \Rightarrow n \quad \text{(if } n < 2^N) \\
\text{sN} & ::= \pm: sign \cdot n: num \Rightarrow \pm n \quad \text{(if } -2^{N-1} \leq \pm n < 2^{N-1})
\end{align*}
\]

Uninterpreted integers can be written as either signed or unsigned, and are normalized to unsigned in the abstract syntax.

\[
\begin{align*}
\text{iN} & ::= n:uN \Rightarrow n \\
\text{i:sN} & \Rightarrow n \quad \text{(if } i = \text{signed}(n))
\end{align*}
\]

6.3.2 Floating-Point

Floating-point values can be represented in either decimal or hexadecimal notation.

\[
\begin{align*}
\text{frac} & ::= d:digit \Rightarrow d/10 \\
\text{hexfrac} & ::= h: hexdigit \Rightarrow h/16 \\
\text{p:frac} & ::= p:frac \Rightarrow (d + p/10)/10 \\
\text{p:hexfrac} & ::= p:hexfrac \Rightarrow (h + p/16)/16 \\
\text{float} & ::= p: num \cdot \pm: sign \cdot e: num \Rightarrow p \cdot 10^{\pm e} \\
\text{p:frac} & ::= p: frac \Rightarrow p + q \\
\text{p:hexfrac} & ::= p: hexfrac \Rightarrow p + q \\
\text{hexfloat} & ::= \text{p:hexnum} \cdot \pm: sign \cdot e: num \Rightarrow p \cdot 2^{\pm e}
\end{align*}
\]

The value of a literal must not lie outside the representable range of the corresponding IEEE 754-2019\footnote{https://ieeexplore.ieee.org/document/8766229} type (that is, a numeric value must not overflow to \(\pm \infty\)), but it may be rounded to the nearest representable value.
Note: Rounding can be prevented by using hexadecimal notation with no more significant bits than supported by the required type.

Floating-point values may also be written as constants for infinity or canonical NaN (not a number). Furthermore, arbitrary NaN values may be expressed by providing an explicit payload value.

\[
\begin{align*}
  f_N & ::= \pm : \text{sign} \, z : f_N \text{mag} \Rightarrow \pm z \\
  f_N \text{mag} & ::= z : \text{float} \Rightarrow \text{float}_N(z) \quad \text{(if } \text{float}_N(z) \neq \pm \infty) \\
  & | z : \text{hexfloat} \Rightarrow \text{float}_N(z) \quad \text{(if } \text{float}_N(z) \neq \pm \infty) \\
  & | '\text{inf}' \Rightarrow \infty \\
  & | '\text{nan}' \Rightarrow \text{nan}(2^{\text{signif}(N)-1}) \\
  & | '\text{nan}: 0x n : \text{hexnum} \Rightarrow \text{nan}(n) \quad \text{(if } 1 \leq n < 2^{\text{signif}(N)})
\end{align*}
\]

### 6.3.3 Strings

Strings denote sequences of bytes that can represent both textual and binary data. They are enclosed in quotation marks and may contain any character other than ASCII control characters, quotation marks (""), or backslash (\), except when expressed with an escape sequence.

\[
\begin{align*}
  \text{string} ::= "" (b*:stringelem) "" \Rightarrow \text{concat}((b*)*) \\
  \text{stringelem} ::= c: \text{stringchar} \Rightarrow \text{utf8}(c) \\
  & | \text{\'n:hexdigit m:hexdigit} \Rightarrow 16 \cdot n + m
\end{align*}
\]

Each character in a string literal represents the byte sequence corresponding to its UTF-8 Unicode (Section 2.5) encoding, except for hexadecimal escape sequences \"\hh\", which represent raw bytes of the respective value.

\[
\begin{align*}
  \text{stringchar} ::= c: \text{char} \Rightarrow c \\
  & | 't' \Rightarrow U+09 \\
  & | 'n' \Rightarrow U+0A \\
  & | 'r' \Rightarrow U+0D \\
  & | 'r' \Rightarrow U+22 \\
  & | 'r' \Rightarrow U+27 \\
  & | 'u\{ n:hexnum \}' \Rightarrow U+(n) \quad \text{(if } n < 0xD800 \lor 0xE000 \leq n < 0x110000)
\end{align*}
\]

### 6.3.4 Names

Names are strings denoting a literal character sequence. A name string must form a valid UTF-8 encoding as defined by Unicode (Section 2.5) and is interpreted as a string of Unicode scalar values.

\[
\begin{align*}
  \text{name} ::= b*: \text{string} \Rightarrow c* \quad \text{(if } b* = \text{utf8}(c*)\)
\end{align*}
\]

Note: Presuming the source text is itself encoded correctly, strings that do not contain any uses of hexadecimal byte escapes are always valid names.

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43 https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d
44 https://www.unicode.org/versions/latest/
45 https://www.unicode.org/versions/latest/
6.3.5 Identifiers

Indices can be given in both numeric and symbolic form. Symbolic identifiers that stand in lieu of indices start with ‘$’, followed by any sequence of printable ASCII characters that does not contain a space, quotation mark, comma, semicolon, or bracket.

\[
\begin{align*}
\text{id} & ::= \text{'$'} \text{idchar}^+ \\
\text{idchar} & ::= \text{'0'} \ldots \text{'9'} \\
& \quad | \text{'A'} \ldots \text{'Z'} \\
& \quad | \text{'a'} \ldots \text{'z'} \\
& \quad | \text{'}!\text{'} | \text{'}#\text{'} | \text{'}\text{"}\text{'} | \text{'}%\text{'} | \text{'}&\text{'} | \text{'}*\text{'} | \text{'}+\text{'} | \text{'}-\text{'} | \text{'}=\text{'} | \text{'}\text{'}\text{'} | \text{'}\text{'}\text{'} | \text{'}\text{'}\text{'} | \text{'}\text{'}\text{'} | \text{'}\text{'}\text{'} | \text{'}\text{'}\text{'} |
\end{align*}
\]

Conventions

The expansion rules of some abbreviations require insertion of a fresh identifier. That may be any syntactically valid identifier that does not already occur in the given source text.

6.4 Types

6.4.1 Number Types

\[
\begin{align*}
\text{numtype} & ::= \text{'i32'} \Rightarrow \text{i32} \\
& \quad | \text{'i64'} \Rightarrow \text{i64} \\
& \quad | \text{'f32'} \Rightarrow \text{f32} \\
& \quad | \text{'f64'} \Rightarrow \text{f64}
\end{align*}
\]

6.4.2 Vector Types

\[
\begin{align*}
\text{vectype} & ::= \text{'v128'} \Rightarrow \text{v128}
\end{align*}
\]

6.4.3 Reference Types

\[
\begin{align*}
\text{reftype} & ::= \text{'funcref'} \Rightarrow \text{funcref} \\
& \quad | \text{'externref'} \Rightarrow \text{externref} \\
\text{heaptype} & ::= \text{'func'} \Rightarrow \text{funcref} \\
& \quad | \text{'extern'} \Rightarrow \text{externref}
\end{align*}
\]

6.4.4 Value Types

\[
\begin{align*}
\text{valtype} & ::= \text{t}:\text{numtype} \Rightarrow \text{t} \\
& \quad | \text{t}:\text{vectype} \Rightarrow \text{t} \\
& \quad | \text{t}:\text{reftype} \Rightarrow \text{t}
\end{align*}
\]

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6.4. Types 161

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46 https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d
6.4.5 Function Types

functype ::= ‘(‘func’ $t_1^*:\text{vec}(\text{param})$ $t_2^*:\text{vec}(\text{result})$ ’)’ ⇒ $[t_1^*] \rightarrow [t_2^*]$

param ::= ‘(‘param’ id’ $t$:valtype ’)’ ⇒ $t$

result ::= ‘(‘result’ $t$:valtype ’)’ ⇒ $t$

**Note:** The optional identifier names for parameters in a function type only have documentation purpose. They cannot be referenced from anywhere.

**Abbreviations**

Multiple anonymous parameters or results may be combined into a single declaration:

\[
\text{‘(‘param’ valtype*) ’} \equiv \text{‘(‘param’ valtype ’)’}^* \\
\text{‘(‘result’ valtype*) ’} \equiv \text{‘(‘result’ valtype ’)’}^*
\]

6.4.6 Limits

limits ::= $n$:$u32$ ⇒ $\{\text{min } n, \text{max } \epsilon\}$

| $n$:$u32$ $m$:$u32$ ⇒ $\{\text{min } n, \text{max } m\}$

6.4.7 Memory Types

memtype ::= $\text{lim:limits}$ ⇒ $\text{lim}$

6.4.8 Table Types

tabletype ::= $\text{lim:limits et:reftype}$ ⇒ $\text{lim et}$

6.4.9 Global Types

globaltype ::= $t$:$\text{valtype}$ ⇒ $\text{const } t$

| $\text{‘(‘mut’ } t$:$\text{valtype ‘)’} \Rightarrow \text{var } t$

6.5 Instructions

Instructions are syntactically distinguished into *plain* and *structured* instructions.

\[
instr_I ::= \text{in:plaininstr}_I \Rightarrow \text{in} \\
| \text{in:blockinstr}_I \Rightarrow \text{in}
\]

In addition, as a syntactic abbreviation, instructions can be written as S-expressions in *folded* form, to group them visually.
6.5.1 Labels

Structured control instructions can be annotated with a symbolic label identifier. They are the only symbolic identifiers that can be bound locally in an instruction sequence. The following grammar handles the corresponding update to the identifier context by composing the context with an additional label entry.

\[
\begin{align*}
\text{label}_I &::= v:\text{id} \Rightarrow \{\text{labels } v\} \oplus I \quad (\text{if } v \notin I.\text{labels}) \\
&\quad | \quad \epsilon \Rightarrow \{\text{labels } (\epsilon)\} \oplus I
\end{align*}
\]

**Note:** The new label entry is inserted at the beginning of the label list in the identifier context. This effectively shifts all existing labels up by one, mirroring the fact that control instructions are indexed relatively not absolutely.

6.5.2 Control Instructions

Structured control instructions can bind an optional symbolic label identifier. The same label identifier may optionally be repeated after the corresponding end and else pseudo instructions, to indicate the matching delimiters.

Their block type is given as a type use, analogous to the type of functions. However, the special case of a type use that is syntactically empty or consists of only a single result is not regarded as an abbreviation for an inline function type, but is parsed directly into an optional value type.

\[
\begin{align*}
\text{blocktype}_I &::= (t:\text{result}) \Rightarrow t^I \\
&\quad | \quad x, I:\text{typeuse}_I \Rightarrow x \quad (\text{if } I = \{\text{locals } (\epsilon)\})
\end{align*}
\]

\[
\begin{align*}
\text{blockinstr}_I &::= \text{‘block’ } I:\text{label}_I \ b t:\text{blocktype}_I (i n : \text{instr}_I)^* \ ‘\text{end’ } i d^I \Rightarrow \text{block } b t \text{ in }^* \text{ end} \quad (\text{if } id^I = \epsilon \lor id^I = \text{label}) \\
&\quad | \quad \text{‘loop’ } I:\text{label}_I \ b t:\text{blocktype}_I (i n : \text{instr}_I)^* \ ‘\text{end’ } i d^I \Rightarrow \text{loop } b t \text{ in }^* \text{ end} \quad (\text{if } id^I = \epsilon \lor id^I = \text{label}) \\
&\quad | \quad \text{‘if’ } I:\text{label}_I \ b t:\text{blocktype}_I (i n_1 : \text{instr}_I)^* \ ‘\text{else’ } i d^I_1 \ (i n_2 : \text{instr}_I)^* \ ‘\text{end’ } i d^I_2 \Rightarrow \text{if } b t \text{ in}_1 \text{ else in}_2 \text{ end} \quad (\text{if } id^I_1 = \epsilon \lor id^I_1 = \text{label}, id^I_2 = \epsilon \lor id^I_2 = \text{label})
\end{align*}
\]

**Note:** The side condition stating that the identifier context \( I \) must only contain unnamed entries in the rule for typeuse block types enforces that no identifier can be bound in any param declaration for a block type.

All other control instruction are represented verbatim.

\[
\begin{align*}
\text{plaininstr}_I &::= \text{‘unreachable’} \Rightarrow \text{unreachable} \\
&\quad | \quad \text{‘nop’} \Rightarrow \text{nop} \\
&\quad | \quad \text{‘br’ } l:\text{labelidx}_I \Rightarrow \text{br } l \\
&\quad | \quad \text{‘br_if’ } l:\text{labelidx}_I \Rightarrow \text{br_if } l \\
&\quad | \quad \text{‘br_table’ } l:\text{vec(labelidx}_I) \ l_N:\text{labelidx}_I \Rightarrow \text{br_table } l^* l_N \\
&\quad | \quad \text{‘return’} \Rightarrow \text{return} \\
&\quad | \quad \text{‘call’ } x:\text{funcidx}_I \Rightarrow \text{call } x \\
&\quad | \quad \text{‘call_indirect’ } x:\text{tableidx } y, I:\text{typeuse}_I \Rightarrow \text{call_indirect } x y \quad (\text{if } I = \{\text{locals } (\epsilon)\})
\end{align*}
\]

**Note:** The side condition stating that the identifier context \( I \) must only contain unnamed entries in the rule for call_indirect enforces that no identifier can be bound in any param declaration appearing in the type annotation.
Abbreviations

The ‘else’ keyword of an ‘if’ instruction can be omitted if the following instruction sequence is empty.

\[
\text{‘if’ label blocktype instr* ‘end’} \equiv \text{‘if’ label blocktype instr* ‘else’ ‘end’}
\]

Also, for backwards compatibility, the table index to ‘call_indirect’ can be omitted, defaulting to 0.

\[
\text{‘call_indirect’ typeuse} \equiv \text{‘call_indirect’ 0 typeuse}
\]

6.5.3 Reference Instructions

\[
\text{plaininstr} \ ::= \ ...
\]

| ‘ref.null’ t:heaptpe ⇒ ref.null t
| ‘ref.is_null’ ⇒ ref.is_null
| ‘ref.func’ x:funcidx ⇒ ref.func x

6.5.4 Parametric Instructions

\[
\text{plaininstr} \ ::= \ ...
\]

| ‘drop’ ⇒ drop
| ‘select’((t:resutl)*’ ⇒ select (t*)’

6.5.5 Variable Instructions

\[
\text{plaininstr} \ ::= \ ...
\]

| ‘local.get’ x:localidx ⇒ local.get x
| ‘local.set’ x:localidx ⇒ local.set x
| ‘local.tee’ x:localidx ⇒ local.tee x
| ‘global.get’ x:globalidx ⇒ global.get x
| ‘global.set’ x:globalidx ⇒ global.set x

6.5.6 Table Instructions

\[
\text{plaininstr} \ ::= \ ...
\]

| ‘table.get’ x:tableidx ⇒ table.get x
| ‘table.set’ x:tableidx ⇒ table.set x
| ‘table.size’ x:tableidx ⇒ table.size x
| ‘table.grow’ x:tableidx ⇒ table.grow x
| ‘table.fill’ x:tableidx ⇒ table.fill x
| ‘table.copy’ x:tableidx y:tableidx ⇒ table.copy x y
| ‘table.init’ x:tableidx y:elemidx ⇒ table.init x y
| ‘elem.drop’ x:elemidx ⇒ elem.drop x
Abbreviations

For backwards compatibility, all `tableindices < syntax = tableidx >` may be omitted from table instructions, defaulting to 0.

\[
\begin{align*}
\text{'table.get'} & \equiv \text{'table.get' '0'} \\
\text{'table.set'} & \equiv \text{'table.set' '0'} \\
\text{'table.size'} & \equiv \text{'table.size' '0'} \\
\text{'table.grow'} & \equiv \text{'table.grow' '0'} \\
\text{'table.fill'} & \equiv \text{'table.fill' '0'} \\
\text{'table.copy'} & \equiv \text{'table.copy' '0' '0'} \\
\text{'table.init'} x:elemidx & \equiv \text{'table.init' '0' '0'}:elemidx \\
\end{align*}
\]

6.5.7 Memory Instructions

The offset and alignment immediates to memory instructions are optional. The offset defaults to 0, the alignment to the storage size of the respective memory access, which is its natural alignment. Lexically, an offset or align phrase is considered a single keyword token, so no white space is allowed around the `='.

\[
\begin{align*}
\text{memarg}_N & ::= o\text{offset} a\text{align}_N \Rightarrow \{\text{align } n, \text{ offset } o\} \ (\text{if } a = 2^n) \\
\text{offset} & ::= \text{offset}='a':u32 \Rightarrow o \\
\text{align}_N & ::= \text{align}='a':u32 \Rightarrow a \\
\text{plaininstr}_I & ::= \ldots \\
\text{'}i32.load'} m:memarg & \Rightarrow \text{i32.load } m \\
\text{'}i64.load'} m:memarg & \Rightarrow \text{i64.load } m \\
\text{'}f32.load'} m:memarg & \Rightarrow \text{f32.load } m \\
\text{'}f64.load'} m:memarg & \Rightarrow \text{f64.load } m \\
\text{'}i32.load8_s'} m:memarg & \Rightarrow \text{i32.load8 } s m \\
\text{'}i32.load8_u'} m:memarg & \Rightarrow \text{i32.load8 } u m \\
\text{'}i32.load16_s'} m:memarg & \Rightarrow \text{i32.load16 } s m \\
\text{'}i32.load16_u'} m:memarg & \Rightarrow \text{i32.load16 } u m \\
\text{'}i64.load8_s'} m:memarg & \Rightarrow \text{i64.load8 } s m \\
\text{'}i64.load8_u'} m:memarg & \Rightarrow \text{i64.load8 } u m \\
\text{'}i64.load16_s'} m:memarg & \Rightarrow \text{i64.load16 } s m \\
\text{'}i64.load16_u'} m:memarg & \Rightarrow \text{i64.load16 } u m \\
\text{'}i64.load32_s'} m:memarg & \Rightarrow \text{i64.load32 } s m \\
\text{'}i64.load32_u'} m:memarg & \Rightarrow \text{i64.load32 } u m \\
\text{'}i32.store'} m:memarg & \Rightarrow \text{i32.store } m \\
\text{'}i64.store'} m:memarg & \Rightarrow \text{i64.store } m \\
\text{'}f32.store'} m:memarg & \Rightarrow \text{f32.store } m \\
\text{'}f64.store'} m:memarg & \Rightarrow \text{f64.store } m \\
\text{'}i32.store8'} m:memarg & \Rightarrow \text{i32.store8 } m \\
\text{'}i32.store16'} m:memarg & \Rightarrow \text{i32.store16 } m \\
\text{'}i64.store8'} m:memarg & \Rightarrow \text{i64.store8 } m \\
\text{'}i64.store16'} m:memarg & \Rightarrow \text{i64.store16 } m \\
\text{'}i64.store32'} m:memarg & \Rightarrow \text{i64.store32 } m \\
\text{'}memory.size'} & \Rightarrow \text{memory.size} \\
\text{'}memory.grow'} & \Rightarrow \text{memory.grow} \\
\text{'}memory.fill'} & \Rightarrow \text{memory.fill} \\
\text{'}memory.copy'} & \Rightarrow \text{memory.copy} \\
\text{'}memory.init'} x:elemidx & \Rightarrow \text{memory.init } x \\
\text{'}data.drop'} x:elemidx & \Rightarrow \text{data.drop } x
\end{align*}
\]
6.5.8 Numeric Instructions

plaininstr \( ::= \ldots \)

\[ \'	ext{i32.const}' \ n:i32 \Rightarrow \text{i32.const} \ n \]
\[ \'	ext{i64.const}' \ n:i64 \Rightarrow \text{i64.const} \ n \]
\[ \'	ext{f32.const}' \ z:f32 \Rightarrow \text{f32.const} \ z \]
\[ \'	ext{f64.const}' \ z:f64 \Rightarrow \text{f64.const} \ z \]
\[ \'	ext{i32.clz}' \Rightarrow \text{i32.clz} \]
\[ \'	ext{i32.ctz}' \Rightarrow \text{i32.ctz} \]
\[ \'	ext{i32.popcnt}' \Rightarrow \text{i32.popcnt} \]
\[ \'	ext{i32.add}' \Rightarrow \text{i32.add} \]
\[ \'	ext{i32.sub}' \Rightarrow \text{i32.sub} \]
\[ \'	ext{i32.mul}' \Rightarrow \text{i32.mul} \]
\[ \'	ext{i32.div_s}' \Rightarrow \text{i32.div_s} \]
\[ \'	ext{i32.div_u}' \Rightarrow \text{i32.div_u} \]
\[ \'	ext{i32.rem_s}' \Rightarrow \text{i32.rem_s} \]
\[ \'	ext{i32.rem_u}' \Rightarrow \text{i32.rem_u} \]
\[ \'	ext{i32.and}' \Rightarrow \text{i32.and} \]
\[ \'	ext{i32.or}' \Rightarrow \text{i32.or} \]
\[ \'	ext{i32.xor}' \Rightarrow \text{i32.xor} \]
\[ \'	ext{i32.shl}' \Rightarrow \text{i32.shl} \]
\[ \'	ext{i32.shr_s}' \Rightarrow \text{i32.shr_s} \]
\[ \'	ext{i32.shr_u}' \Rightarrow \text{i32.shr_u} \]
\[ \'	ext{i32.rotl}' \Rightarrow \text{i32.rotl} \]
\[ \'	ext{i32.rotr}' \Rightarrow \text{i32.rotr} \]
\[ \'	ext{i64.clz}' \Rightarrow \text{i64.clz} \]
\[ \'	ext{i64.ctz}' \Rightarrow \text{i64.ctz} \]
\[ \'	ext{i64.popcnt}' \Rightarrow \text{i64.popcnt} \]
\[ \'	ext{i64.add}' \Rightarrow \text{i64.add} \]
\[ \'	ext{i64.sub}' \Rightarrow \text{i64.sub} \]
\[ \'	ext{i64.mul}' \Rightarrow \text{i64.mul} \]
\[ \'	ext{i64.div_s}' \Rightarrow \text{i64.div_s} \]
\[ \'	ext{i64.div_u}' \Rightarrow \text{i64.div_u} \]
\[ \'	ext{i64.rem_s}' \Rightarrow \text{i64.rem_s} \]
\[ \'	ext{i64.rem_u}' \Rightarrow \text{i64.rem_u} \]
\[ \'	ext{i64.and}' \Rightarrow \text{i64.and} \]
\[ \'	ext{i64.or}' \Rightarrow \text{i64.or} \]
\[ \'	ext{i64.xor}' \Rightarrow \text{i64.xor} \]
\[ \'	ext{i64.shl}' \Rightarrow \text{i64.shl} \]
\[ \'	ext{i64.shr_s}' \Rightarrow \text{i64.shr_s} \]
\[ \'	ext{i64.shr_u}' \Rightarrow \text{i64.shr_u} \]
\[ \'	ext{i64.rotl}' \Rightarrow \text{i64.rotl} \]
\[ \'	ext{i64.rotr}' \Rightarrow \text{i64.rotr} \]
6.5. Instructions

| 'f32.abs'     | ⇒ f32.abs |
| 'f32.neg'     | ⇒ f32.neg |
| 'f32.ceil'    | ⇒ f32.ceil |
| 'f32.floor'   | ⇒ f32.floor |
| 'f32.trunc'   | ⇒ f32.trunc |
| 'f32.nearest' | ⇒ f32.nearest |
| 'f32.sqrt'    | ⇒ f32.sqrt |
| 'f32.add'     | ⇒ f32.add |
| 'f32.sub'     | ⇒ f32.sub |
| 'f32.mul'     | ⇒ f32.mul |
| 'f32.div'     | ⇒ f32.div |
| 'f32.min'     | ⇒ f32.min |
| 'f32.max'     | ⇒ f32.max |
| 'f32.copysign'| ⇒ f32.copysign |

| 'f64.abs'     | ⇒ f64.abs |
| 'f64.neg'     | ⇒ f64.neg |
| 'f64.ceil'    | ⇒ f64.ceil |
| 'f64.floor'   | ⇒ f64.floor |
| 'f64.trunc'   | ⇒ f64.trunc |
| 'f64.nearest' | ⇒ f64.nearest |
| 'f64.sqrt'    | ⇒ f64.sqrt |
| 'f64.add'     | ⇒ f64.add |
| 'f64.sub'     | ⇒ f64.sub |
| 'f64.mul'     | ⇒ f64.mul |
| 'f64.div'     | ⇒ f64.div |
| 'f64.min'     | ⇒ f64.min |
| 'f64.max'     | ⇒ f64.max |
| 'f64.copysign'| ⇒ f64.copysign |

| 'i32.eqz'     | ⇒ i32.eqz |
| 'i32.eq'      | ⇒ i32.eq |
| 'i32.ne'      | ⇒ i32.ne |
| 'i32.lt_s'    | ⇒ i32.lt_s |
| 'i32.lt_u'    | ⇒ i32.lt_u |
| 'i32.gt_s'    | ⇒ i32.gt_s |
| 'i32.gt_u'    | ⇒ i32.gt_u |
| 'i32.le_s'    | ⇒ i32.le_s |
| 'i32.le_u'    | ⇒ i32.le_u |
| 'i32.ge_s'    | ⇒ i32.ge_s |
| 'i32.ge_u'    | ⇒ i32.ge_u |

| 'i64.eqz'     | ⇒ i64.eqz |
| 'i64.eq'      | ⇒ i64.eq |
| 'i64.ne'      | ⇒ i64.ne |
| 'i64.lt_s'    | ⇒ i64.lt_s |
| 'i64.lt_u'    | ⇒ i64.lt_u |
| 'i64.gt_s'    | ⇒ i64.gt_s |
| 'i64.gt_u'    | ⇒ i64.gt_u |
| 'i64.le_s'    | ⇒ i64.le_s |
| 'i64.le_u'    | ⇒ i64.le_u |
| 'i64.ge_s'    | ⇒ i64.ge_s |
| 'i64.ge_u'    | ⇒ i64.ge_u |
| 'f32.eq'       | f32.eq       |
| 'f32.ne'       | f32.ne       |
| 'f32.lt'       | f32.lt       |
| 'f32.gt'       | f32.gt       |
| 'f32.le'       | f32.le       |
| 'f32.ge'       | f32.ge       |
| 'f64.eq'       | f64.eq       |
| 'f64.ne'       | f64.ne       |
| 'f64.lt'       | f64.lt       |
| 'f64.gt'       | f64.gt       |
| 'f64.le'       | f64.le       |
| 'f64.ge'       | f64.ge       |
| 'i32.wrap_i64' | i32.wrap_i64 |
| 'i32.trunc_f32_s' | i32.trunc_f32_s |
| 'i32.trunc_f32_u' | i32.trunc_f32_u |
| 'i32.trunc_f64_s' | i32.trunc_f64_s |
| 'i32.trunc_f64_u' | i32.trunc_f64_u |
| 'i32.trunc_sat_f32_s' | i32.trunc_sat_f32_s |
| 'i32.trunc_sat_f32_u' | i32.trunc_sat_f32_u |
| 'i32.trunc_sat_f64_s' | i32.trunc_sat_f64_s |
| 'i32.trunc_sat_f64_u' | i32.trunc_sat_f64_u |
| 'i64.extend_i32_s' | i64.extend_i32_s |
| 'i64.extend_i32_u' | i64.extend_i32_u |
| 'i64.trunc_f32_s' | i64.trunc_f32_s |
| 'i64.trunc_f32_u' | i64.trunc_f32_u |
| 'i64.trunc_f64_s' | i64.trunc_f64_s |
| 'i64.trunc_f64_u' | i64.trunc_f64_u |
| 'i64.trunc_sat_f32_s' | i64.trunc_sat_f32_s |
| 'i64.trunc_sat_f32_u' | i64.trunc_sat_f32_u |
| 'i64.trunc_sat_f64_s' | i64.trunc_sat_f64_s |
| 'i64.trunc_sat_f64_u' | i64.trunc_sat_f64_u |
| 'f32.convert_i32_s' | f32.convert_i32_s |
| 'f32.convert_i32_u' | f32.convert_i32_u |
| 'f32.convert_i64_s' | f32.convert_i64_s |
| 'f32.convert_i64_u' | f32.convert_i64_u |
| 'f32.demote_f64' | f32.demote_f64 |
| 'f64.convert_i32_s' | f64.convert_i32_s |
| 'f64.convert_i32_u' | f64.convert_i32_u |
| 'f64.convert_i64_s' | f64.convert_i64_s |
| 'f64.convert_i64_u' | f64.convert_i64_u |
| 'f64.promote_f32' | f64.promote_f32 |
| 'i32.reinterpret_f32' | i32.reinterpret_f32 |
| 'i64.reinterpret_f64' | i64.reinterpret_f64 |
| 'f32.reinterpret_i32' | f32.reinterpret_i32 |
| 'f64.reinterpret_i64' | f64.reinterpret_i64 |
| 'i32.extend8_s' | i32.extend8_s |
| 'i32.extend16_s' | i32.extend16_s |
| 'i64.extend8_s' | i64.extend8_s |
| 'i64.extend16_s' | i64.extend16_s |
| 'i64.extend32_s' | i64.extend32_s |
6.5.9 Vector Instructions

Vector memory instructions have optional offset and alignment immediates, like the memory instructions.

\[
\text{plaininstr}_I ::= \ldots
\]

- `v128.load' `memarg16` \(\Rightarrow v128\).load \(m\)
- `v128.load8x8_s' `memarg8` \(\Rightarrow v128\).load8x8_s \(m\)
- `v128.load8x8_u' `memarg8` \(\Rightarrow v128\).load8x8_u \(m\)
- `v128.load16x4_s' `memarg8` \(\Rightarrow v128\).load16x4_s \(m\)
- `v128.load16x4_u' `memarg8` \(\Rightarrow v128\).load16x4_u \(m\)
- `v128.load32x2_s' `memarg8` \(\Rightarrow v128\).load32x2_s \(m\)
- `v128.load32x2_u' `memarg8` \(\Rightarrow v128\).load32x2_u \(m\)
- `v128.load8_splat' `memarg1` \(\Rightarrow v128\).load8 \(s\)\(\text{plat} \) \(m\)
- `v128.load16_splat' `memarg2` \(\Rightarrow v128\).load16 \(s\)\(\text{plat} \) \(m\)
- `v128.load32_splat' `memarg4` \(\Rightarrow v128\).load32 \(s\)\(\text{plat} \) \(m\)
- `v128.load64_splat' `memarg8` \(\Rightarrow v128\).load64 \(s\)\(\text{plat} \) \(m\)
- `v128.load32_zero' `memarg4` \(\Rightarrow v128\).load32 \(z\) \(m\)
- `v128.load64_zero' `memarg8` \(\Rightarrow v128\).load64 \(z\) \(m\)
- `v128.store' `memarg16` \(\Rightarrow v128\).store \(m\)
- `v128.load8_lane' `memarg1 laneidx\(u\)8` \(\Rightarrow v128\).load8 \(l\)\(aneidx\) \(m\) \(laneidx\)
- `v128.load16_lane' `memarg2 laneidx\(u\)8` \(\Rightarrow v128\).load16 \(l\)\(aneidx\) \(m\) \(laneidx\)
- `v128.load32_lane' `memarg4 laneidx\(u\)8` \(\Rightarrow v128\).load32 \(l\)\(aneidx\) \(m\) \(laneidx\)
- `v128.load64_lane' `memarg8 laneidx\(u\)8` \(\Rightarrow v128\).load64 \(l\)\(aneidx\) \(m\) \(laneidx\)
- `v128.store8_lane' `memarg1 laneidx\(u\)8` \(\Rightarrow v128\).store8 \(l\)\(aneidx\) \(m\) \(laneidx\)
- `v128.store16_lane' `memarg2 laneidx\(u\)8` \(\Rightarrow v128\).store16 \(l\)\(aneidx\) \(m\) \(laneidx\)
- `v128.store32_lane' `memarg4 laneidx\(u\)8` \(\Rightarrow v128\).store32 \(l\)\(aneidx\) \(m\) \(laneidx\)
- `v128.store64_lane' `memarg8 laneidx\(u\)8` \(\Rightarrow v128\).store64 \(l\)\(aneidx\) \(m\) \(laneidx\)

Vector constant instructions have a mandatory shape descriptor, which determines how the following values are parsed.

\[
\begin{align*}
\text{v128.const' 'i8x16' (}\text{n:16}\text{)} & \Rightarrow \text{v128.const bytes}^{1}_{128} (\text{bytes8}(\text{n})^{16}) \\
\text{v128.const' 'i16x8' (}\text{n:16}\text{)} & \Rightarrow \text{v128.const bytes}^{1}_{128} (\text{bytes16}(\text{n})^{8}) \\
\text{v128.const' 'i32x4' (}\text{n:32}\text{)} & \Rightarrow \text{v128.const bytes}^{1}_{128} (\text{bytes32}(\text{n})^{4}) \\
\text{v128.const' 'i64x2' (}\text{n:64}\text{)} & \Rightarrow \text{v128.const bytes}^{1}_{128} (\text{bytes64}(\text{n})^{2}) \\
\text{v128.const' 'f32x4' (}\text{z:f32}\text{)} & \Rightarrow \text{v128.const bytes}^{1}_{128} (\text{bytes32}(\text{z})^{4}) \\
\text{v128.const' 'f64x2' (}\text{z:f64}\text{)} & \Rightarrow \text{v128.const bytes}^{1}_{128} (\text{bytes64}(\text{z})^{2}) \\
\text{'i8x16.shuffle' (}\text{laneidx\(u\)8}\text{)} & \Rightarrow \text{i8x16.shuffle laneidx}^{16} \\
\text{'i8x16.swizzle'} & \Rightarrow \text{i8x16.swizzle} \\
\text{'i16x8.splat'} & \Rightarrow \text{i16x8.splat} \\
\text{'i32x4.splat'} & \Rightarrow \text{i32x4.splat} \\
\text{'i64x2.splat'} & \Rightarrow \text{i64x2.splat} \\
\text{'f32x4.splat'} & \Rightarrow \text{f32x4.splat} \\
\text{'f64x2.splat'} & \Rightarrow \text{f64x2.splat}
\end{align*}
\]
`i8x16.extract_lane_s` laneidx:u8  ➞  i8x16.extract_lane_s laneidx
`i8x16.extract_lane_u` laneidx:u8  ➞  i8x16.extract_lane_u laneidx
`i8x16.replace_lane` laneidx:u8  ➞  i8x16.replace_lane
`i16x8.extract_lane_s` laneidx:u8  ➞  i16x8.extract_lane_s laneidx
`i16x8.extract_lane_u` laneidx:u8  ➞  i16x8.extract_lane_u laneidx
`i16x8.replace_lane` laneidx:u8  ➞  i16x8.replace_lane
`i32x4.extract_lane` laneidx:u8  ➞  i32x4.extract_lane
`i32x4.replace_lane` laneidx:u8  ➞  i32x4.replace_lane
`i64x2.extract_lane` laneidx:u8  ➞  i64x2.extract_lane
`i64x2.replace_lane` laneidx:u8  ➞  i64x2.replace_lane
`f32x4.extract_lane` laneidx:u8  ➞  f32x4.extract_lane
`f32x4.replace_lane` laneidx:u8  ➞  f32x4.replace_lane
`f64x2.extract_lane` laneidx:u8  ➞  f64x2.extract_lane
`f64x2.replace_lane` laneidx:u8  ➞  f64x2.replace_lane

`i8x16.eq`  ➞  i8x16.eq
`i8x16.ne`  ➞  i8x16.ne
`i8x16.lt_s`  ➞  i8x16.lt_s
`i8x16.lt_u`  ➞  i8x16.lt_u
`i8x16.gt_s`  ➞  i8x16.gt_s
`i8x16.gt_u`  ➞  i8x16.gt_u
`i8x16.le_s`  ➞  i8x16.le_s
`i8x16.le_u`  ➞  i8x16.le_u
`i8x16.ge_s`  ➞  i8x16.ge_s
`i8x16.ge_u`  ➞  i8x16.ge_u

`i16x8.eq`  ➞  i16x8.eq
`i16x8.ne`  ➞  i16x8.ne
`i16x8.lt_s`  ➞  i16x8.lt_s
`i16x8.lt_u`  ➞  i16x8.lt_u
`i16x8.gt_s`  ➞  i16x8.gt_s
`i16x8.gt_u`  ➞  i16x8.gt_u
`i16x8.le_s`  ➞  i16x8.le_s
`i16x8.le_u`  ➞  i16x8.le_u
`i16x8.ge_s`  ➞  i16x8.ge_s
`i16x8.ge_u`  ➞  i16x8.ge_u

`i32x4.eq`  ➞  i32x4.eq
`i32x4.ne`  ➞  i32x4.ne
`i32x4.lt_s`  ➞  i32x4.lt_s
`i32x4.lt_u`  ➞  i32x4.lt_u
`i32x4.gt_s`  ➞  i32x4.gt_s
`i32x4.gt_u`  ➞  i32x4.gt_u
`i32x4.le_s`  ➞  i32x4.le_s
`i32x4.le_u`  ➞  i32x4.le_u
`i32x4.ge_s`  ➞  i32x4.ge_s
`i32x4.ge_u`  ➞  i32x4.ge_u

`i64x2.eq`  ➞  i64x2.eq
`i64x2.ne`  ➞  i64x2.ne
`i64x2.lt_s`  ➞  i64x2.lt_s
`i64x2.gt_s`  ➞  i64x2.gt_s
`i64x2.le_s`  ➞  i64x2.le_s
`i64x2.ge_s`  ➞  i64x2.ge_s
6.5. Instructions

`f32x4.eq` | `f32x4.eq`
---|---
`f32x4.ne` | `f32x4.ne`
`f32x4.lt` | `f32x4.lt`
`f32x4.gt` | `f32x4.gt`
`f32x4.le` | `f32x4.le`
`f32x4.ge` | `f32x4.ge`

`f64x2.eq` | `f64x2.eq`
---|---
`f64x2.ne` | `f64x2.ne`
`f64x2.lt` | `f64x2.lt`
`f64x2.gt` | `f64x2.gt`
`f64x2.le` | `f64x2.le`
`f64x2.ge` | `f64x2.ge`

`v128.not` | `v128.not`
---|---
`v128.and` | `v128.and`
`v128.andnot` | `v128.andnot`
`v128.or` | `v128.or`
`v128.xor` | `v128.xor`
`v128.bitselect` | `v128.bitselect`
`v128.any_true` | `v128.any_true`

`i8x16.abs` | `i8x16.abs`
---|---
`i8x16.neg` | `i8x16.neg`
`i8x16.all_true` | `i8x16.all_true`
`i8x16.bitmask` | `i8x16.bitmask`
`i8x16.narrow_i16x8_s` | `i8x16.narrow_i16x8_s`
`i8x16.narrow_i16x8_u` | `i8x16.narrow_i16x8_u`
`i8x16.shl` | `i8x16.shl`
`i8x16.shr_s` | `i8x16.shr_s`
`i8x16.shr_u` | `i8x16.shr_u`
`i8x16.add` | `i8x16.add`
`i8x16.add_sat_s` | `i8x16.add_sat_s`
`i8x16.add_sat_u` | `i8x16.add_sat_u`
`i8x16.sub` | `i8x16.sub`
`i8x16.sub_sat_s` | `i8x16.sub_sat_s`
`i8x16.sub_sat_u` | `i8x16.sub_sat_u`
`i8x16.min_s` | `i8x16.min_s`
`i8x16.min_u` | `i8x16.min_u`
`i8x16.max_s` | `i8x16.max_s`
`i8x16.max_u` | `i8x16.max_u`
`i8x16.avgr_u` | `i8x16.avgr_u`
`i8x16.popcnt` | `i8x16.popcnt`
| i32x4.abs' | i32x4.abs |
| i32x4.neg' | i32x4.neg |
| i32x4.all_true' | i32x4.all_true |
| i32x4.bitmask' | i32x4.bitmask |
| i32x4.narrow_i32x4_u' | i32x4.narrow_i32x4_u |
| i32x4.extend_low_i8x16_u' | i32x4.extend_low_i8x16_u |
| i32x4.extend_high_i8x16_u' | i32x4.extend_high_i8x16_u |
| i32x4.extend_high_i16x8_u' | i32x4.extend_high_i16x8_u |
| i32x4.shl' | i32x4.shl |
| i32x4.shr_s' | i32x4.shr_s |
| i32x4.shr_u' | i32x4.shr_u |
| i32x4.add' | i32x4.add |
| i32x4.add_sat_s' | i32x4.add_sat_s |
| i32x4.add_sat_u' | i32x4.add_sat_u |
| i32x4.sub' | i32x4.sub |
| i32x4.sub_sat_s' | i32x4.sub_sat_s |
| i32x4.sub_sat_u' | i32x4.sub_sat_u |
| i32x4.mul' | i32x4.mul |
| i32x4.min_s' | i32x4.min_s |
| i32x4.min_u' | i32x4.min_u |
| i32x4.max_s' | i32x4.max_s |
| i32x4.max_u' | i32x4.max_u |
| i32x4.avgr_u' | i32x4.avgr_u |
| i32x4.q15mulr_sat_s' | i32x4.q15mulr_sat_s |
| i32x4.extmul_low_i8x16_s' | i32x4.extmul_low_i8x16_s |
| i32x4.extmul_low_i8x16_u' | i32x4.extmul_low_i8x16_u |
| i32x4.extmul_high_i8x16_u' | i32x4.extmul_high_i8x16_u |
| i32x4.extadd_pairwise_i8x16_s' | i32x4.extadd_pairwise_i8x16_s |
| i32x4.extadd_pairwise_i8x16_u' | i32x4.extadd_pairwise_i8x16_u |
| i32x4.dot_i16x8_s' | i32x4.dot_i16x8_s |
| i32x4.extmul_low_i16x8_s' | i32x4.extmul_low_i16x8_s |
| i32x4.extmul_high_i16x8_s' | i32x4.extmul_high_i16x8_s |
| i32x4.extmul_low_i16x8_u' | i32x4.extmul_low_i16x8_u |
| i32x4.extmul_high_i16x8_u' | i32x4.extmul_high_i16x8_u |
6.5. Instructions

- `i64x2.abs` ➔ i64x2.abs
- `i64x2.neg` ➔ i64x2.neg
- `i64x2.all_true` ➔ i64x2.all_true
- `i64x2.bitmask` ➔ i64x2.bitmask
- `i64x2.extend_low_i32x4_s` ➔ i64x2.extend_low_i32x4_s
- `i64x2.extend_high_i32x4_s` ➔ i64x2.extend_high_i32x4_s
- `i64x2.extend_low_i32x4_u` ➔ i64x2.extend_low_i32x4_u
- `i64x2.extend_high_i32x4_u` ➔ i64x2.extend_high_i32x4_u
- `i64x2.shl` ➔ i64x2.shl
- `i64x2.shr_s` ➔ i64x2.shr_s
- `i64x2.shr_u` ➔ i64x2.shr_u
- `i64x2.add` ➔ i64x2.add
- `i64x2.sub` ➔ i64x2.sub
- `i64x2.mul` ➔ i64x2.mul
- `i64x2.extmul_low_i32x4_s` ➔ i64x2.extmul_low_i32x4_s
- `i64x2.extmul_high_i32x4_s` ➔ i64x2.extmul_high_i32x4_s
- `i64x2.extmul_low_i32x4_u` ➔ i64x2.extmul_low_i32x4_u
- `i64x2.extmul_high_i32x4_u` ➔ i64x2.extmul_high_i32x4_u

- `f32x4.abs` ➔ f32x4.abs
- `f32x4.neg` ➔ f32x4.neg
- `f32x4.sqrt` ➔ f32x4.sqrt
- `f32x4.add` ➔ f32x4.add
- `f32x4.sub` ➔ f32x4.sub
- `f32x4.mul` ➔ f32x4.mul
- `f32x4.div` ➔ f32x4.div
- `f32x4.min` ➔ f32x4.min
- `f32x4.max` ➔ f32x4.max
- `f32x4.pmin` ➔ f32x4.pmin
- `f32x4.pmax` ➔ f32x4.pmax

- `f64x2.abs` ➔ f64x2.abs
- `f64x2.neg` ➔ f64x2.neg
- `f64x2.sqrt` ➔ f64x2.sqrt
- `f64x2.add` ➔ f64x2.add
- `f64x2.sub` ➔ f64x2.sub
- `f64x2.mul` ➔ f64x2.mul
- `f64x2.div` ➔ f64x2.div
- `f64x2.min` ➔ f64x2.min
- `f64x2.max` ➔ f64x2.max
- `f64x2.pmin` ➔ f64x2.pmin
- `f64x2.pmax` ➔ f64x2.pmax

- `i32x4.trunc_sat_f32x4_s` ➔ i32x4.trunc_sat_f32x4_s
- `i32x4.trunc_sat_f32x4_u` ➔ i32x4.trunc_sat_f32x4_u
- `i32x4.trunc_sat_f64x2_s_zero` ➔ i32x4.trunc_sat_f64x2_s_zero
- `i32x4.trunc_sat_f64x2_u_zero` ➔ i32x4.trunc_sat_f64x2_u_zero
- `f32x4.convert_i32x4_s` ➔ f32x4.convert_i32x4_s
- `f32x4.convert_i32x4_u` ➔ f32x4.convert_i32x4_u
- `f64x2.convert_low_i32x4_s` ➔ f64x2.convert_low_i32x4_s
- `f64x2.convert_low_i32x4_u` ➔ f64x2.convert_low_i32x4_u
- `f32x4.demote_f64x2_zero` ➔ f32x4.demote_f64x2_zero
- `f64x2.promote_low_f32x4` ➔ f64x2.promote_low_f32x4
6.5.10 Folded Instructions

Instructions can be written as S-expressions by grouping them into folded form. In that notation, an instruction is wrapped in parentheses and optionally includes nested folded instructions to indicate its operands.

In the case of block instructions, the folded form omits the ‘end’ delimiter. For if instructions, both branches have to be wrapped into nested S-expressions, headed by the keywords ‘then’ and ‘else’.

The set of all phrases defined by the following abbreviations recursively forms the auxiliary syntactic class foldedinstr. Such a folded instruction can appear anywhere a regular instruction can.

\[
\begin{align*}
\text{`(`plaininstr foldedinstr `)`) } & \equiv \text{ foldedinstr } \text{plaininstr} \\
\text{`(`block label blocktype instr `)`} & \equiv \text{ `block label blocktype instr `end`} \\
\text{`(`loop label blocktype instr `)`} & \equiv \text{ `loop label blocktype instr `end`} \\
\text{`(`if label blocktype foldedinstr `(``then` instr `)` `(``else` instr `)` `?` `)`} & \equiv \text{ foldedinstr `if` label blocktype instr `else` (instr `)` `end`}
\end{align*}
\]

**Note:** For example, the instruction sequence

\[
\text{(local.get $x) (i32.const 2) i32.add (i32.const 3) i32.mul}
\]

can be folded into

\[
\text{(i32.mul (i32.add (local.get $x) (i32.const 2)) (i32.const 3))}
\]

Folded instructions are solely syntactic sugar, no additional syntactic or type-based checking is implied.

6.5.11 Expressions

Expressions are written as instruction sequences. No explicit ‘end’ keyword is included, since they only occur in bracketed positions.

\[
\text{expr} := (in:instr)^* \Rightarrow \text{in \text{end}}
\]
6.6 Modules

6.6.1 Indices

Indices can be given either in raw numeric form or as symbolic identifiers when bound by a respective construct. Such identifiers are looked up in the suitable space of the identifier context $I$.

\[
\begin{align*}
typeidx_{I} & ::= x:u32 \Rightarrow x \\
funcidx_{I} & ::= x:u32 \Rightarrow x \\
tableidx_{I} & ::= x:u32 \Rightarrow x \\
memidx_{I} & ::= x:u32 \Rightarrow x \\
globalidx_{I} & ::= x:u32 \Rightarrow x \\
elemidx_{I} & ::= x:u32 \Rightarrow x \\
dataidx_{I} & ::= x:u32 \Rightarrow x \\
localidx_{I} & ::= l:u32 \Rightarrow l \\
labelidx_{I} & ::= l:u32 \Rightarrow l
\end{align*}
\]

6.6.2 Types

Type definitions can bind a symbolic type identifier.

\[
type ::= ('\text{\texttt{type}} id\ ft:\text{functype}') \Rightarrow ft
\]

6.6.3 Type Uses

A type use is a reference to a type definition. It may optionally be augmented by explicit inlined parameter and result declarations. That allows binding symbolic identifiers to name the local indices of parameters. If inline declarations are given, then their types must match the referenced function type.

\[
typeuse_{I} ::= ('\text{\texttt{type}} x:typeidx_{I})' \Rightarrow x,I' \\
\quad (\text{if } I.\text{typedefs}[x] = [t_1^n] \rightarrow [t_2^n] \land I' = \{\text{locals } (\epsilon)^n\}) \\
\quad | ('\text{\texttt{type}} x:typeidx_{I})' (t_1:\text{param})^* (t_2:\text{result})^* \Rightarrow x,I' \\
\quad (\text{if } I.\text{typedefs}[x] = [t_1] \rightarrow [t_2] \land I' = \{\text{locals id(\text{param})}^*\} \text{ well-formed})
\]

The synthesized attribute of a typeuse is a pair consisting of both the used type index and the updated identifier context including possible parameter identifiers. The following auxiliary function extracts optional identifiers from parameters:

\[
id('\text{\texttt{param}} id^* ... ') = id^n
\]

Note: Both productions overlap for the case that the function type is \([\ ] \rightarrow [\ ]\). However, in that case, they also produce the same results, so that the choice is immaterial.

The well-formedness condition on $I'$ ensures that the parameters do not contain duplicate identifier.
Abbreviations

A typeuse may also be replaced entirely by inline parameter and result declarations. In that case, a type index is automatically inserted:

\[(t_1:\text{param})* (t_2:\text{result})* \equiv \text{`('type' } x \text{') param* result*} \]

where \(x\) is the smallest existing type index whose definition in the current module is the function type \([t_1] \rightarrow [t_2]\]. If no such index exists, then a new type definition of the form

\text{`('type' `('func' param* result*') ')')} \]

is inserted at the end of the module.

Abbreviations are expanded in the order they appear, such that previously inserted type definitions are reused by consecutive expansions.

6.6.4 Imports

The descriptors in imports can bind a symbolic function, table, memory, or global identifier.

\[
\text{import}_I \ ::= \text{`('import' mod:name nm:name d:importdesc}_I \text{'')'} \quad \Rightarrow \{\text{module mod, name nm, desc } d\} \\
\text{importdesc}_I \ ::= \text{`('func' id? } x, I':\text{typeuse}_I \text{'')'} \quad \Rightarrow \text{func } x \\
| \text{`('table' id? } tt:tabletype \text{'')'} \quad \Rightarrow \text{table } tt \\
| \text{`('memory' id? mt:memtype \text{'')'} \quad \Rightarrow \text{mem } mt \\
| \text{`('global' id? gt:globaltype \text{'')'} \quad \Rightarrow \text{global } gt}
\]

Abbreviations

As an abbreviation, imports may also be specified inline with function, table, memory, or global definitions; see the respective sections.

6.6.5 Functions

Function definitions can bind a symbolic function identifier, and local identifiers for its parameters and locals.

\[
\text{func}_I \ ::= \text{`('func' id? } x, I':\text{typeuse}_I (t:\text{local})* (in:instr}_I)* \text{')'} \quad \Rightarrow \{\text{type } x, \text{locals } t^*, \text{body } \text{in}* \text{end}\} \\
\text{(if } I'' = I' \oplus \{\text{locals id'(local)* } \text{well-formed}\} \quad \text{well-formed}) \\
\text{local} \ ::= \text{`('local' id? } t:valtype \text{'')'} \quad \Rightarrow \text{ } t
\]

The definition of the local identifier context \(I''\) uses the following auxiliary function to extract optional identifiers from locals:

\[
\text{id}('\text{local'} id? \ldots \text{'')} = id^2
\]

Note: The well-formedness condition on \(I''\) ensures that parameters and locals do not contain duplicate identifiers.
Abbreviations

Multiple anonymous locals may be combined into a single declaration:

```
('local valtype ') ≡ ('local valtype ')
```

Functions can be defined as imports or exports inline:

```
('func id' ('import name1 name2 ')' typeuse ')') ≡
('import name1 name2 ('func id' typeuse ')')

('func id' ('export name ') ... ')') ≡
('export name ('func id')') ('func id' ... ')
```

Note: The latter abbreviation can be applied repeatedly, if “...” contains additional export clauses. Consequently, a function declaration can contain any number of exports, possibly followed by an import.

6.6.6 Tables

Table definitions can bind a symbolic table identifier.

```
table₁ ::= ('table id tt:tabletype ')' ⇒ {type tt}
```

Abbreviations

An element segment can be given inline with a table definition, in which case its offset is 0 and the limits of the table type are inferred from the length of the given segment:

```
('table id reftype ('elem expr:vec(elemexpr) ')')' ≡
('table id n n reftype ')

('elem ('table id ') ('i32.const ' 0 ') vec(elemexpr) ')
(id id' ≠ id ∧ id' = id' ∨ id = id ∧ id' fresh)
```

```
('table id reftype ('elem x:vec(funcidx) ')')' ≡
('table id n n reftype ')

('elem ('table id ') ('i32.const ' 0 ') vec(funcidx) ')
(id id' ≠ id ∧ id' = id' ∨ id = id ∧ id' fresh)
```

Tables can be defined as imports or exports inline:

```
('table id ('import name1 name2 ') tabletype ')')' ≡
('import name1 name2 ('table id tabletype ')')

('export name ' ...')') ≡
('export name ('table id ')') ('table id' ... ')
(id id' ≠ id ∧ id' = id' ∨ id = id ∧ id' fresh)
```

Note: The latter abbreviation can be applied repeatedly, if “...” contains additional export clauses. Consequently, a table declaration can contain any number of exports, possibly followed by an import.
6.6.7 Memories

Memory definitions can bind a symbolic memory identifier.

\[ \text{mem}_f ::= \left( \text{`memory'} \; \text{id}^2 \; \text{mt}:\text{memtype} \right) \Rightarrow \{ \text{type} \; \text{mt} \} \]

**Abbreviations**

A data segment can be given inline with a memory definition, in which case its offset is 0 the limits of the memory type are inferred from the length of the data, rounded up to page size:

\[ \left( \text{`memory'} \; \text{id} \right) \left( \text{`data'} \; b^n: \text{datastring} \right) \Rightarrow \left( \text{memory'} \; \text{id} \right) \left( \text{data'} \; \text{id} \right) \]

\[ \left( \text{`data'} \; \left( \text{`memory'} \; \text{id'} \right) \left( \text{`datastring} \right) \left( \text{id} \right) \right) \]

\[ \left( \text{id} \neq \epsilon \land \text{id} \lor \text{id}^2 = \epsilon \land \text{id} \; \text{fresh}, \text{m} = \text{ceil}(n/64\text{Ki}) \right) \]

Memories can be defined as **imports** or **exports** inline:

\[ \left( \text{`memory'} \; \text{id} \right) \left( \text{`import'} \; \text{name}_1 \; \text{name}_2 \right) \left( \text{`memory'} \; \text{mt} \right) \]

\[ \left( \text{`import'} \; \text{name}_1 \; \text{name}_2 \left( \text{`memory'} \; \text{id} \right) \left( \text{`memorytype} \right) \right) \]

\[ \left( \text{`memory'} \; \text{id} \right) \left( \text{`export'} \; \text{name} \right) \left( \text{`memory'} \; \text{id} \right) \left( \text{`memorytype} \right) \]

\[ \left( \text{`export'} \; \text{name} \left( \text{`memory'} \; \text{id} \right) \left( \text{`memorytype} \right) \right) \]

\[ \left( \text{id} \neq \epsilon \land \text{id} \lor \text{id}^2 = \epsilon \land \text{id} \; \text{fresh} \right) \]

**Note:** The latter abbreviation can be applied repeatedly, if “...” contains additional export clauses. Consequently, a memory declaration can contain any number of exports, possibly followed by an import.

6.6.8 Globals

Global definitions can bind a symbolic global identifier.

\[ \text{global}_f ::= \left( \text{`global'} \; \text{id} \; gt: \text{globaltype} \; e: \text{expr} \right) \Rightarrow \{ \text{type} \; \text{gt}, \text{init} \; e \} \]

**Abbreviations**

Globals can be defined as **imports** or **exports** inline:

\[ \left( \text{`global'} \; \text{id} \right) \left( \text{`import'} \; \text{name}_1 \; \text{name}_2 \right) \left( \text{`globaltype} \right) \]

\[ \left( \text{`import'} \; \text{name}_1 \; \text{name}_2 \left( \text{`global'} \; \text{id} \right) \left( \text{`globaltype} \right) \right) \]

\[ \left( \text{`global'} \; \text{id} \right) \left( \text{`export'} \; \text{name} \right) \left( \text{`globaltype} \right) \]

\[ \left( \text{`export'} \; \text{name} \left( \text{`global'} \; \text{id} \right) \left( \text{`globaltype} \right) \right) \]

\[ \left( \text{id} \neq \epsilon \land \text{id} \lor \text{id}^2 = \epsilon \land \text{id} \; \text{fresh} \right) \]

**Note:** The latter abbreviation can be applied repeatedly, if “...” contains additional export clauses. Consequently, a global declaration can contain any number of exports, possibly followed by an import.
6.6.9 Exports

The syntax for exports mirrors their \textit{abstract syntax} directly.

\begin{verbatim}
export_f ::= \textquoteleft\textquoteleft export \textquoteleft\textquoteleft nm.name d:exportdesc_f \textquoteleft\textquoteright\textquoteleft \Rightarrow \{ name nm, desc d \}
exportdesc_f ::= \textquoteleft\textquoteleft func \textquoteleft\textquoteleft x:funcidx_f \textquoteleft\textquoteright\textquoteleft \Rightarrow func x
| \textquoteleft\textquoteleft table \textquoteleft\textquoteleft x:tableidx_f \textquoteleft\textquoteright\textquoteleft \Rightarrow table x
| \textquoteleft\textquoteleft memory \textquoteleft\textquoteleft x:memidx_f \textquoteleft\textquoteright\textquoteleft \Rightarrow mem x
| \textquoteleft\textquoteleft global \textquoteleft\textquoteleft x:globalidx_f \textquoteleft\textquoteright\textquoteleft \Rightarrow global x
\end{verbatim}

\textbf{Abbreviations}

As an abbreviation, exports may also be specified inline with \textit{function}, \textit{table}, \textit{memory}, or \textit{global} definitions; see the respective sections.

6.6.10 Start Function

A \textit{start function} is defined in terms of its index.

\begin{verbatim}
start_f ::= \textquoteleft\textquoteleft start \textquoteleft\textquoteleft x:funcidx_f \textquoteleft\textquoteright\textquoteleft \Rightarrow \{ func x \}
\end{verbatim}

\textbf{Note:} At most one start function may occur in a module, which is ensured by a suitable side condition on the \textit{module} grammar.

6.6.11 Element Segments

Element segments allow for an optional \textit{table index} to identify the table to initialize.

\begin{verbatim}
elem_f ::= \textquoteleft\textquoteleft elem \textquoteleft\textquoteleft id\textquoteleft\textquoteleft et, y*):elemlist_f \textquoteleft\textquoteright\textquoteleft \Rightarrow \{ type et, init y*, mode passive \}
| \textquoteleft\textquoteleft elem \textquoteleft\textquoteleft id\textquoteleft\textquoteleft x:tableuse_f \textquoteleft\textquoteleft\textquoteleft\textquoteleft offset \textquoteleft\textquoteleft e:expr_f \textquoteleft\textquoteright\textquoteleft \textquoteleft\textquoteleft et, y*):elemlist_f \textquoteleft\textquoteright\textquoteleft \Rightarrow \{ type et, init y*, mode active \{ table x, offset e \} \}
| \textquoteleft\textquoteleft elem \textquoteleft\textquoteleft id\textquoteleft\textquoteleft \textquoteleft declare \textquoteleft\textquoteleft et, y\textquoteleft\textquoteleft):elemlist_f \textquoteleft\textquoteright\textquoteleft \Rightarrow \{ type et, init y*, mode declarative \}
elemlist_f ::= t:reftype y*:vec(elemexpr_f) \Rightarrow (type t, init y*)
elemexpr_f ::= \textquoteleft\textquoteleft item \textquoteleft\textquoteleft e:expr_f \textquoteleft\textquoteright\textquoteleft \Rightarrow e
tableuse_f ::= \textquoteleft\textquoteleft table \textquoteleft\textquoteleft x:tableidx_f \textquoteleft\textquoteright\textquoteleft \Rightarrow x
\end{verbatim}

\textbf{Abbreviations}

As an abbreviation, a single instruction may occur in place of the offset of an active element segment or as an element expression:

\begin{verbatim}
\textquoteleft\textquoteleft instr \textquoteleft\textquoteright\textquoteleft \equiv \textquoteleft\textquoteleft\textquoteleft offset \textquoteleft\textquoteleft instr \textquoteleft\textquoteright\textquoteleft
\textquoteleft\textquoteleft instr \textquoteleft\textquoteright\textquoteleft \equiv \textquoteleft\textquoteleft\textquoteleft item \textquoteleft\textquoteleft instr \textquoteleft\textquoteright\textquoteleft
\end{verbatim}

Also, the element list may be written as just a sequence of \textit{function indices}:

\begin{verbatim}
\textquoteleft\textquoteleft func \textquoteleft\textquoteleft vec(funcidx_f) \equiv \textquoteleft\textquoteleft func\textquoteleft\textquoteleft vec(\textquoteleft\textquoteleft ref.func \textquoteleft\textquoteleft funcidx_f \textquoteleft\textquoteright\textquoteright)
\end{verbatim}

A table use can be omitted, defaulting to 0. Furthermore, for backwards compatibility with earlier versions of WebAssembly, if the table use is omitted, the `\textit{func}` keyword can be omitted as well.

\begin{verbatim}
\textquoteleft\textquoteleft elem \textquoteleft\textquoteleft id\textquoteleft\textquoteleft \textquoteleft\textquoteleft offset \textquoteleft\textquoteleft expr_f \textquoteleft\textquoteright\textquoteleft vec(funcidx_f) \textquoteleft\textquoteright\textquoteleft \equiv \textquoteleft\textquoteleft\textquoteleft\textquoteleft elem \textquoteleft\textquoteleft id\textquoteleft\textquoteleft \textquoteleft\textquoteleft\textquoteleft table \textquoteleft\textquoteleft 0 \textquoteleft\textquoteright\textquoteleft \textquoteleft\textquoteleft\textquoteleft\textquoteleft offset \textquoteleft\textquoteleft expr_f \textquoteleft\textquoteright\textquoteleft
\end{verbatim}

As another abbreviation, element segments may also be specified inline with \textit{table} definitions; see the respective section.
6.6.12 Data Segments

Data segments allow for an optional memory index to identify the memory to initialize. The data is written as a string, which may be split up into a possibly empty sequence of individual string literals.

\[
data_I ::= \left(\text{`data' } \text{id} \? b^* : \text{datastring }\right)
\Rightarrow \{\text{init } b^*, \text{mode passive}\}
\]

\[
datastring ::= (b^*: \text{string})^* \Rightarrow \text{concat}((b^*)^*)
\]

\[
\text{memuse}_I ::= \left(\text{`memory' } \text{x:memidx}_I \right) \Rightarrow x
\]

**Note:** In the current version of WebAssembly, the only valid memory index is 0 or a symbolic memory identifier resolving to the same value.

**Abbreviations**

As an abbreviation, a single instruction may occur in place of the offset of an active data segment:

\[
\text{`instr'} \equiv \left(\text{`offset' instr'}\right)
\]

Also, a memory use can be omitted, defaulting to 0.

\[
\epsilon \equiv \left(\text{`memory' } 0 \right)
\]

As another abbreviation, data segments may also be specified inline with memory definitions; see the respective section.

6.6.13 Modules

A module consists of a sequence of fields that can occur in any order. All definitions and their respective bound identifiers scope over the entire module, including the text preceding them.

A module may optionally bind an identifier that names the module. The name serves a documentary role only.

**Note:** Tools may include the module name in the name section of the binary format.

\[
\text{module} ::= \left(\text{`module' } \text{id} (m: \text{modulefield}_I)^* \right) \Rightarrow \bigoplus m^*
\]

\[
\text{modulefield}_I ::= ty: \text{type} \Rightarrow \{\text{types } ty\}
\]

The following restrictions are imposed on the composition of modules: \(m_1 \oplus m_2\) is defined if and only if

- \(m_1.\text{start} = \epsilon \lor m_2.\text{start} = \epsilon\)
- \(m_1.\text{funcs} = m_1.\text{tables} = m_1.\text{mems} = m_1.\text{globals} = \epsilon \lor m_2.\text{imports} = \epsilon\)
The first condition ensures that there is at most one start function. The second condition enforces that all imports must occur before any regular definition of a function, table, memory, or global, thereby maintaining the ordering of the respective index spaces.

The well-formedness condition on $I$ in the grammar for module ensures that no namespace contains duplicate identifiers.

The definition of the initial identifier context $I$ uses the following auxiliary definition which maps each relevant definition to a singular context with one (possibly empty) identifier:

$$
\begin{align*}
  \text{idc('"type' id' ft:functype '"')} & = \{\text{types (id')},\, \text{typedefs ft}\} \\
  \text{idc('"func' id' ... '"')} & = \{\text{funcs (id')}\} \\
  \text{idc('"table' id' ... '"')} & = \{\text{tables (id')}\} \\
  \text{idc('"memory' id' ... '"')} & = \{\text{mems (id')}\} \\
  \text{idc('"global' id' ... '"')} & = \{\text{globals (id')}\} \\
  \text{idc('"elem' id' ... '"')} & = \{\text{elem (id')}\} \\
  \text{idc('"data' id' ... '"')} & = \{\text{data (id')}\} \\
  \text{idc('"import' ... ('"func' id' ... '"') ... '"')} & = \{\text{funcs (id')}\} \\
  \text{idc('"import' ... ('"table' id' ... '"') ... '"')} & = \{\text{tables (id')}\} \\
  \text{idc('"import' ... ('"memory' id' ... '"') ... '"')} & = \{\text{mems (id')}\} \\
  \text{idc('"import' ... ('"global' id' ... '"') ... '"')} & = \{\text{globals (id')}\} \\
  \text{idc(' ... ')} & = \{}
\end{align*}
$$

**Abbreviations**

In a source file, the toplevel (module ...) surrounding the module body may be omitted.

$$
\text{modulefield}^* \equiv ('"\text{module' modulefield}^* ')}
$$
7.1 Embedding

A WebAssembly implementation will typically be *embedded* into a *host* environment. An *embedder* implements the connection between such a host environment and the WebAssembly semantics as defined in the main body of this specification. An embedder is expected to interact with the semantics in well-defined ways.

This section defines a suitable interface to the WebAssembly semantics in the form of entry points through which an embedder can access it. The interface is intended to be complete, in the sense that an embedder does not need to reference other functional parts of the WebAssembly specification directly.

**Note:** On the other hand, an embedder does not need to provide the host environment with access to all functionality defined in this interface. For example, an implementation may not support *parsing* of the *text format*.

7.1.1 Types

In the description of the embedder interface, syntactic classes from the *abstract syntax* and the *runtime’s abstract machine* are used as names for variables that range over the possible objects from that class. Hence, these syntactic classes can also be interpreted as types.

For numeric parameters, notation like \( n : \text{u32} \) is used to specify a symbolic name in addition to the respective value range.

7.1.2 Errors

Failure of an interface operation is indicated by an auxiliary syntactic class:

\[
\text{error} ::= \text{error}
\]

In addition to the error conditions specified explicitly in this section, implementations may also return errors when specific *implementation limitations* are reached.

**Note:** Errors are abstract and unspecific with this definition. Implementations can refine it to carry suitable classifications and diagnostic messages.
7.1.3 Pre- and Post-Conditions

Some operations state pre-conditions about their arguments or post-conditions about their results. It is the embedder’s responsibility to meet the pre-conditions. If it does, the post conditions are guaranteed by the semantics.

In addition to pre- and post-conditions explicitly stated with each operation, the specification adopts the following conventions for runtime objects (store, moduleinst, externval, addresses):

- Every runtime object passed as a parameter must be valid per an implicit pre-condition.
- Every runtime object returned as a result is valid per an implicit post-condition.

Note: As long as an embedder treats runtime objects as abstract and only creates and manipulates them through the interface defined here, all implicit pre-conditions are automatically met.

7.1.4 Store

store_init() : store

1. Return the empty store.

\[
\text{store_init}() = \{ \text{funcs } \epsilon, \text{ mems } \epsilon, \text{ tables } \epsilon, \text{ globals } \epsilon \}
\]

7.1.5 Modules

module_decode(byte*) : module | error

1. If there exists a derivation for the byte sequence byte* as a module according to the binary grammar for modules, yielding a module m, then return m.

2. Else, return error.

\[
\begin{align*}
\text{module_decode}(b^*) &= m & (\text{if } \text{module } \xrightarrow{\text{}} m:b^*) \\
\text{module_decode}(b^*) &= \text{error} & (\text{otherwise})
\end{align*}
\]

module_parse(char*) : module | error

1. If there exists a derivation for the source char* as a module according to the text grammar for modules, yielding a module m, then return m.

2. Else, return error.

\[
\begin{align*}
\text{module_parse}(c^*) &= m & (\text{if } \text{module } \xrightarrow{\text{}} m:c^*) \\
\text{module_parse}(c^*) &= \text{error} & (\text{otherwise})
\end{align*}
\]
module\_validate(module) : error

1. If module is valid, then return nothing.
2. Else, return error.

\[
\begin{align*}
\text{module\_validate}(m) &= \epsilon \quad \text{(if } \vdash m : \text{extertype}^* \rightarrow \text{extertype}'^* ) \\
\text{module\_validate}(m) &= \text{error} \quad \text{(otherwise)}
\end{align*}
\]

module\_instantiate(store, module, externval\*) : (store, moduleinst | error)

1. Try instantiating module in store with external values externval\* as imports:
   a. If it succeeds with a module instance moduleinst, then let result be moduleinst.
   b. Else, let result be error.
2. Return the new store paired with result.

\[
\begin{align*}
\text{module\_instantiate}(S, m, ev\*) &= (S', F.\text{module}) \quad \text{(if } \text{instantiate}(S, m, ev\*) \leftarrow *S'; F; \epsilon) \\
\text{module\_instantiate}(S, m, ev\*) &= (S', \text{error}) \quad \text{(if } \text{instantiate}(S, m, ev\*) \leftarrow *S'; F; \text{trap})
\end{align*}
\]

Note: The store may be modified even in case of an error.

module\_imports(module) : (name, name, extertype)*

1. Pre-condition: module is valid with external import types extertype* and external export types extertype'*.
2. Let import* be the imports module.imports.
3. Assert: the length of import* equals the length of extertype*.
4. For each import\_i in import* and corresponding extertype\_i in extertype*, do:
   a. Let result\_i be the triple (import\_i.module, import\_i.name, extertype\_i).
5. Return the concatenation of all result\_i, in index order.
6. Post-condition: each extertype\_i is valid.

\[
\text{module\_imports}(m) = (im.\text{module}, im.\text{name}, extertype)^* \\
\quad \text{(if } im^* = m.\text{imports} \land \vdash m : \text{extertype}^* \rightarrow \text{extertype}'^*)
\]

module\_exports(module) : (name, extertype)*

1. Pre-condition: module is valid with external import types extertype* and external export types extertype'*.
2. Let export* be the exports module.exports.
3. Assert: the length of export* equals the length of extertype'*.
4. For each export\_i in export* and corresponding extertype\_i in extertype', do:
   a. Let result\_i be the pair (export\_i.name, extertype\_i).
5. Return the concatenation of all result\_i, in index order.
6. Post-condition: each extertype\_i is valid.
module_exports(m) = \( (\text{ex.name, extertype'})^* \) (if \( \text{ex}^* = m.\text{exports} \land \vdash m : \text{extertype}^* \rightarrow \text{extertype}'^* \) )

### 7.1.6 Module Instances

\( \text{instance_export(moduleinst, name)} : \text{external} \mid \text{error} \)

1. Assert: due to validity of the module instance \( \text{moduleinst} \), all its export names are different.
2. If there exists an \( \text{exportinst}_i \) in \( \text{moduleinst.exports} \) such that \( \text{name exportinst}_i \).name equals \( \text{name} \), then:
   a. Return the external value \( \text{exportinst}_i \).value.
3. Else, return \( \text{error} \).

\[
\begin{align*}
\text{instance_export}(m, \text{name}) & = m.\text{exports}[i].\text{value} & (\text{if } m.\text{exports}[i].\text{name} = \text{name}) \\
\text{instance_export}(m, \text{name}) & = \text{error} & (\text{otherwise})
\end{align*}
\]

### 7.1.7 Functions

\( \text{func_alloc(store, functype, hostfunc)} : (\text{store, funcaddr}) \)

1. Pre-condition: \( \text{functype} \) is \( \text{valid} < \text{valid - functype} > \).
2. Let \( \text{funcaddr} \) be the result of \text{allocating a host function} in \text{store} with \text{function type functype} and host function code \text{hostfunc}.
3. Return the new store paired with \( \text{funcaddr} \).

\[
\text{func_alloc}(S, ft, code) = (S', a) \quad (\text{if } \text{allochostfunc}(S, ft, code) = S', a)
\]

**Note:** This operation assumes that \( \text{hostfunc} \) satisfies the pre- and post-conditions required for a function instance with type \( \text{functype} \).

Regular (non-host) function instances can only be created indirectly through module instantiation.

\( \text{func_type(store, funcaddr)} : \text{functype} \)

1. Return \( S.\text{funcs}[a].\text{type} \).
2. Post-condition: the returned function type is \text{valid}.

\[
\text{func_type}(S, a) = S.\text{funcs}[a].\text{type}
\]
func_invoke(store, funcaddr, val*) : (store, val* | error)

1. Try invoking the function funcaddr in store with values val* as arguments:
   a. If it succeeds with values val'* as results, then let result be val'*.
   b. Else it has trapped, hence let result be error.
2. Return the new store paired with result.

\[
\begin{align*}
\text{func_invoke}(S, a, v^*) &= (S', v'^*) & \text{(if } \text{invoke}(S, a, v^*) \mapsto S'; F; v'^*) \\
\text{func_invoke}(S, a, v^*) &= (S', \text{error}) & \text{(if } \text{invoke}(S, a, v^*) \mapsto S'; F; \text{trap})
\end{align*}
\]

**Note:** The store may be modified even in case of an error.

### 7.1.8 Tables

table_alloc(store, tabletype) : (store, tableaddr, ref)

1. Pre-condition: tabletype is valid < valid − tabletype >.
2. Let tableaddr be the result of allocating a table in store with table type tabletype and initialization value ref.
3. Return the new store paired with tableaddr.

\[
\begin{align*}
\text{table_alloc}(S, tt, r) &= (S', a) & \text{(if } \text{alloctable}(S, tt, r) = S', a)
\end{align*}
\]

table_type(store, tableaddr) : tabletype

1. Return S.tables[a].type.
2. Post-condition: the returned table type is valid < valid − tabletype >.

\[
\begin{align*}
\text{table_type}(S, a) &= S.\text{tables}[a].\text{type}
\end{align*}
\]

table_read(store, tableaddr, i : u32) : ref | error

1. Let ti be the table instance store.tables[tableaddr].
2. If i is larger than or equal to the length of ti.elem, then return error.
3. Else, return the reference value ti.elem[i].

\[
\begin{align*}
\text{table_read}(S, a, i) &= r & \text{(if } S.\text{tables}[a].\text{elem}[i] = r) \\
\text{table_read}(S, a, i) &= \text{error} & \text{(otherwise)}
\end{align*}
\]

table_write(store, tableaddr, i : u32, ref) : store | error

1. Let ti be the table instance store.tables[tableaddr].
2. If i is larger than or equal to the length of ti.elem, then return error.
3. Replace ti.elem[i] with the reference value ref.
4. Return the updated store.

\[
\begin{align*}
\text{table_write}(S, a, i, r) &= S' & \text{(if } S' = S \text{ with } \text{tables}[a].\text{elem}[i] = r) \\
\text{table_write}(S, a, i, r) &= \text{error} & \text{(otherwise)}
\end{align*}
\]

7.1. Embedding
table_size(store, tableaddr) : u32

1. Return the length of store.tables[tableaddr].elem.

\[\text{table_size}(S, a) = n \quad (\text{if } |S{.}\text{tables}[a].\text{elem}| = n)\]

table_grow(store, tableaddr, n : u32, ref) : store | error

1. Try growing the table instance store.tables[tableaddr] by n elements with initialization value ref:
   a. If it succeeds, return the updated store.
   b. Else, return error.

\[\begin{align*}
\text{table_grow}(S, a, n, r) &= S' \quad (\text{if } S' = S \text{ with } \text{tables}[a] = \text{growtable}(S{.}\text{tables}[a], n, r)) \\
\text{table_grow}(S, a, n, r) &= \text{error} \quad (\text{otherwise})
\end{align*}\]

7.1.9 Memories

mem_alloc(store, memtype) : (store, memaddr)

1. Pre-condition: memtype is valid < valid – memtype >.
2. Let memaddr be the result of allocating a memory in store with memory type memtype.
3. Return the new store paired with memaddr.

\[\text{mem_alloc}(S, mt) = (S', a) \quad (\text{if } \text{allocmem}(S, mt) = S', a)\]

mem_type(store, memaddr) : memtype

1. Return S.mems[a].type.
2. Post-condition: the returned memory type is valid < valid – memtype >.

\[\text{mem_type}(S, a) = S{.}\text{mems}[a].\text{type}\]

mem_read(store, memaddr, i : u32) : byte | error

1. Let mi be the memory instance store.mems[memaddr].
2. If i is larger than or equal to the length of mi.data, then return error.
3. Else, return the byte mi.data[i].

\[\begin{align*}
\text{mem_read}(S, a, i) &= b \quad (\text{if } S{.}\text{mems}[a].\text{data}[i] = b) \\
\text{mem_read}(S, a, i) &= \text{error} \quad (\text{otherwise})
\end{align*}\]
mem_write(store, memaddr, i : u32, byte) : store | error

1. Let mi be the memory instance store.mems[memaddr].
2. If u32 is larger than or equal to the length of mi.data, then return error.
3. Replace mi.data[i] with byte.
4. Return the updated store.

\[
\text{mem_write}(S, a, i, b) = \begin{cases} 
S' & \text{if } S' = S \text{ with } \text{mems}[a].data[i] = b \\
\text{error} & \text{(otherwise)}
\end{cases}
\]

mem_size(store, memaddr) : u32

1. Return the length of store.mems[memaddr].data divided by the page size.

\[
\text{mem_size}(S, a) = n \quad (\text{if } |S.\text{mems}[a].data| = n \cdot 64 \text{ Ki})
\]

mem_grow(store, memaddr, n : u32) : store | error

1. Try growing the memory instance store.mems[memaddr] by n pages:
   a. If it succeeds, return the updated store.
   b. Else, return error.

\[
\text{mem_grow}(S, a, n) = \begin{cases} 
S' & \text{if } S' = S \text{ with } \text{mems}[a] = \text{growmem}(S.\text{mems}[a], n) \\
\text{error} & \text{(otherwise)}
\end{cases}
\]

### 7.1.10 Globals

global_alloc(store, globaltype, val) : (store, globaladdr)

1. Pre-condition: globaltype is valid < valid − globaltype >.
2. Let globaladdr be the result of allocating a global in store with global type globaltype and initialization value val.
3. Return the new store paired with globaladdr.

\[
\text{global_alloc}(S, gt, v) = \begin{cases} 
(S', a) & \text{if allocglobal}(S, gt, v) = S', a
\end{cases}
\]

global_type(store, globaladdr) : globaltype

1. Return S.globals[a].type.
2. Post-condition: the returned global type is valid < valid − globaltype >.

\[
\text{global_type}(S, a) = S.\text{globals}[a].\text{type}
\]
global\_read\((\text{store}, \text{globaladdr})\) : val

1. Let \(gi\) be the global instance \(\text{store}.\text{globals}[\text{globaladdr}]\).
2. Return the value \(gi.\text{value}\).

\[
global\_read(S, a) = v \quad \text{(if } S.\text{globals}[a].\text{value} = v)\]

global\_write\((\text{store}, \text{globaladdr}, \text{val})\) : store | error

1. Let \(gi\) be the global instance \(\text{store}.\text{globals}[\text{globaladdr}]\).
2. Let \(mut\ t\) be the structure of the global type \(gi.\text{type}\).
3. If \(mut\) is not \(\text{var}\), then return \(\text{error}\).
4. Replace \(gi.\text{value}\) with the value \(\text{val}\).
5. Return the updated store.

\[
global\_write(S, a, v) = S' \quad \text{(if } S.\text{globals}[a].\text{type} = \text{var}\ t \land S' = S \text{ with} \text{globals}[a].\text{value} = v)\]
\[
global\_write(S, a, v) = \text{error} \quad \text{(otherwise)}\]

### 7.2 Implementation Limitations

Implementations typically impose additional restrictions on a number of aspects of a WebAssembly module or execution. These may stem from:

- physical resource limits,
- constraints imposed by the embedder or its environment,
- limitations of selected implementation strategies.

This section lists allowed limitations. Where restrictions take the form of numeric limits, no minimum requirements are given, nor are the limits assumed to be concrete, fixed numbers. However, it is expected that all implementations have “reasonably” large limits to enable common applications.

**Note:** A conforming implementation is not allowed to leave out individual features. However, designated subsets of WebAssembly may be specified in the future.

### 7.2.1 Syntactic Limits

#### Structure

An implementation may impose restrictions on the following dimensions of a module:

- the number of types in a module
- the number of functions in a module, including imports
- the number of tables in a module, including imports
- the number of memories in a module, including imports
- the number of globals in a module, including imports
- the number of element segments in a module
- the number of data segments in a module
• the number of imports to a module
• the number of exports from a module
• the number of parameters in a function type
• the number of results in a function type
• the number of parameters in a block type
• the number of results in a block type
• the number of locals in a function
• the size of a function body
• the size of a structured control instruction
• the number of structured control instructions in a function
• the nesting depth of structured control instructions
• the number of label indices in a br_table instruction
• the length of an element segment
• the length of a data segment
• the length of a name
• the range of characters in a name

If the limits of an implementation are exceeded for a given module, then the implementation may reject the validation, compilation, or instantiation of that module with an embedder-specific error.

Note: The last item allows embedders that operate in limited environments without support for Unicode\(^{47}\) to limit the names of imports and exports to common subsets like ASCII\(^{48}\).

### Binary Format

For a module given in binary format, additional limitations may be imposed on the following dimensions:

• the size of a module
• the size of any section
• the size of an individual function’s code
• the number of sections

### Text Format

For a module given in text format, additional limitations may be imposed on the following dimensions:

• the size of the source text
• the size of any syntactic element
• the size of an individual token
• the nesting depth of folded instructions
• the length of symbolic identifiers
• the range of literal characters allowed in the source text

---

47 https://www.unicode.org/versions/latest/
48 https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d
7.2.2 Validation

An implementation may defer validation of individual functions until they are first invoked. If a function turns out to be invalid, then the invocation, and every consecutive call to the same function, results in a trap.

Note: This is to allow implementations to use interpretation or just-in-time compilation for functions. The function must still be fully validated before execution of its body begins.

7.2.3 Execution

Restrictions on the following dimensions may be imposed during execution of a WebAssembly program:

- the number of allocated module instances
- the number of allocated function instances
- the number of allocated table instances
- the number of allocated memory instances
- the number of allocated global instances
- the size of a table instance
- the size of a memory instance
- the number of frames on the stack
- the number of labels on the stack
- the number of values on the stack

If the runtime limits of an implementation are exceeded during execution of a computation, then it may terminate that computation and report an embedder-specific error to the invoking code.

Some of the above limits may already be verified during instantiation, in which case an implementation may report exceedance in the same manner as for syntactic limits.

Note: Concrete limits are usually not fixed but may be dependent on specifics, interdependent, vary over time, or depend on other implementation- or embedder-specific situations or events.

7.3 Validation Algorithm

The specification of WebAssembly validation is purely declarative. It describes the constraints that must be met by a module or instruction sequence to be valid.

This section sketches the skeleton of a sound and complete algorithm for effectively validating code, i.e., sequences of instructions. (Other aspects of validation are straightforward to implement.)

In fact, the algorithm is expressed over the flat sequence of opcodes as occurring in the binary format, and performs only a single pass over it. Consequently, it can be integrated directly into a decoder.

The algorithm is expressed in typed pseudo code whose semantics is intended to be self-explanatory.
7.3.1 Data Structures

Types are representable as an enumeration.

```plaintext
type val_type = I32 | I64 | F32 | F64 | V128 | Funcref | Externref

func is_num(t : val_type | Unknown) : bool =
    return t = I32 || t = I64 || t = F32 || t = F64 || t = Unknown

func is_vec(t : val_type | Unknown) : bool =
    return t = V128 || t = Unknown

func is_ref(t : val_type | Unknown) : bool =
    return t = Funcref || t = Externref || t = Unknown
```

The algorithm uses two separate stacks: the value stack and the control stack. The former tracks the types of operand values on the stack, the latter surrounding structured control instructions and their associated blocks.

```plaintext
type val_stack = stack(val_type | Unknown)

type ctrl_stack = stack(ctrl_frame)

type ctrl_frame = {
    opcode : opcode
    start_types : list(val_type)
    end_types : list(val_type)
    height : nat
    unreachable : bool
}
```

For each value, the value stack records its value type, or Unknown when the type is not known.

For each entered block, the control stack records a control frame with the originating opcode, the types on the top of the operand stack at the start and end of the block (used to check its result as well as branches), the height of the operand stack at the start of the block (used to check that operands do not underflow the current block), and a flag recording whether the remainder of the block is unreachable (used to handle stack-polymorphic typing after branches).

For the purpose of presenting the algorithm, the operand and control stacks are simply maintained as global variables:

```plaintext
var vals : val_stack
var ctrls : ctrl_stack
```

However, these variables are not manipulated directly by the main checking function, but through a set of auxiliary functions:

```plaintext
func push_val(type : val_type | Unknown) =
    vals.push(type)

func pop_val() : val_type | Unknown =
    if (vals.size() = ctrls[0].height && ctrls[0].unreachable) return Unknown
    error_if(vals.size() = ctrls[0].height)
    return vals.pop()

func pop_val(expect : val_type | Unknown) : val_type | Unknown =
    let actual = pop_val()
    error_if(actual /= expect && actual /= Unknown && expect /= Unknown)
    return actual
```

(continues on next page)
Pushing an operand value simply pushes the respective type to the value stack.

Popping an operand value checks that the value stack does not underflow the current block and then removes one type. But first, a special case is handled where the block contains no known values, but has been marked as unreachable. That can occur after an unconditional branch, when the stack is typed polymorphically. In that case, an unknown type is returned.

A second function for popping an operand value takes an expected type, which the actual operand type is checked against. The types may differ in case one of them is Unknown. The function returns the actual type popped from the stack.

Finally, there are accumulative functions for pushing or popping multiple operand types.

Note: The notation stack[i] is meant to index the stack from the top, so that, e.g., ctrls[0] accesses the element pushed last.

The control stack is likewise manipulated through auxiliary functions:

Pushing a control frame takes the types of the label and result values. It allocates a new frame record recording them along with the current height of the operand stack and marks the block as reachable.

Popping a frame first checks that the control stack is not empty. It then verifies that the operand stack contains the right types of values expected at the end of the exited block and pops them off the operand stack. Afterwards, it checks that the stack has shrunk back to its initial height.

The type of the label associated with a control frame is either that of the stack at the start or the end of the frame, determined by the opcode that it originates from.

Finally, the current frame can be marked as unreachable. In that case, all existing operand types are purged from the value stack, in order to allow for the stack-polymorphism logic in pop_val to take effect.

Note: Even with the unreachable flag set, consecutive operands are still pushed to and popped from the operand stack. That is necessary to detect invalid examples like (unreachable (i32.const i64.add)). However, a polymorphic
7.3.2 Validation of Opcode Sequences

The following function shows the validation of a number of representative instructions that manipulate the stack. Other instructions are checked in a similar manner.

Note: Various instructions not shown here will additionally require the presence of a validation context for checking uses of indices. That is an easy addition and therefore omitted from this presentation.

```rust
func validate(opcode) =
  switch (opcode)
    case (i32.add)
      pop_val(I32)
      pop_val(I32)
      push_val(I32)
    case (drop)
      pop_val()
    case (select)
      pop_val(I32)
      let t1 = pop_val()
      let t2 = pop_val()
      error_if(not ((is_num(t1) && is_num(t2)) || (is_vec(t1) && is_vec(t2))))
      error_if(t1 /= t2 && t1 /= Unknown && t2 /= Unknown)
      push_val(if (t1 = Unknown) t2 else t1)
    case (select t)
      pop_val(I32)
      pop_val(t)
      pop_val(t)
      push_val(t)
    case (unreachable)
      unreachable()
    case (block t1*->t2*)
      pop_vals([t1*])
      push_ctrl(block, [t1*], [t2*])
    case (loop t1*->t2*)
      pop_vals([t1*])
      push_ctrl(loop, [t1*], [t2*])
    case (if t1*->t2*)
      pop_val(I32)
      pop_vals([t1*])
      push_ctrl(if, [t1*], [t2*])
    case (end)
      let frame = pop_ctrl()
      push_vals(frame.end_types)
```

(continues on next page)

7.3. Validation Algorithm
case (else)
    let frame = pop_ctrl()
    error_if(frame.opcode !== if)
    push_ctrl(else, frame.start_types, frame.end_types)

case (br n)
    error_if(ctrls.size() < n)
    pop_vals(label_types(ctrls[n]))
    unreachable()

case (br_if n)
    error_if(ctrls.size() < n)
    pop_val(I32)
    pop_vals(label_types(ctrls[n]))
    push_vals(label_types(ctrls[n]))

case (br_table n* m)
    pop_val(I32)
    error_if(ctrls.size() < m)
    let arity = label_types(ctrls[m]).size()
        foreach (n in n*)
            error_if(ctrls.size() < n)
            error_if(label_types(ctrls[n]).size() !== arity)
            push_vals(pop_vals(label_types(ctrls[n])))
            pop_vals(label_types(ctrls[m]))
        unreachable()

Note: It is an invariant under the current WebAssembly instruction set that an operand of Unknown type is never duplicated on the stack. This would change if the language were extended with stack instructions like dup. Under such an extension, the above algorithm would need to be refined by replacing the Unknown type with proper type variables to ensure that all uses are consistent.

7.4 Custom Sections

This appendix defines dedicated custom sections for WebAssembly’s binary format. Such sections do not contribute to, or otherwise affect, the WebAssembly semantics, and like any custom section they may be ignored by an implementation. However, they provide useful meta data that implementations can make use of to improve user experience or take compilation hints.

Currently, only one dedicated custom section is defined, the name section.

7.4.1 Name Section

The name section is a custom section whose name string is itself ‘name’. The name section should appear only once in a module, and only after the data section.

The purpose of this section is to attach printable names to definitions in a module, which e.g. can be used by a debugger or when parts of the module are to be rendered in text form.

Note: All names are represented in Unicode encoded in UTF-8. Names need not be unique.

49 https://www.unicode.org/versions/latest/
Subsections

The data of a name section consists of a sequence of subsections. Each subsection consists of a
- a one-byte subsection id,
- the \( u32 \) size of the contents, in bytes,
- the actual contents, whose structure is depended on the subsection id.

\[
\begin{align*}
\text{namesec} & \quad ::= \quad \text{section}_0(\text{namedata}) \\
\text{namedata} & \quad ::= \quad n:\text{name} \\
& \quad \quad \quad \quad \quad \text{(if } n = \text{’name’}) \\
& \quad \quad \text{modulenamesubsec}? \\
& \quad \quad \text{funcnamesubsec}? \\
& \quad \quad \text{localnamesubsec}? \\
\text{namesubsection}_N(B) & \quad ::= \quad N:\text{byte size}:u32 \ B \quad \text{(if } size = ||B||) \\
\end{align*}
\]

The following subsection ids are used:

<table>
<thead>
<tr>
<th>Id</th>
<th>Subsection</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>module name</td>
</tr>
<tr>
<td>1</td>
<td>function names</td>
</tr>
<tr>
<td>2</td>
<td>local names</td>
</tr>
</tbody>
</table>

Each subsection may occur at most once, and in order of increasing id.

Name Maps

A name map assigns names to indices in a given index space. It consists of a vector of index/name pairs in order of increasing index value. Each index must be unique, but the assigned names need not be.

\[
\begin{align*}
\text{namemap} & \quad ::= \quad \text{vec(nameassoc)} \\
\text{nameassoc} & \quad ::= \quad \text{idx name} \\
\end{align*}
\]

An indirect name map assigns names to a two-dimensional index space, where secondary indices are grouped by primary indices. It consists of a vector of primary index/name map pairs in order of increasing index value, where each name map in turn maps secondary indices to names. Each primary index must be unique, and likewise each secondary index per individual name map.

\[
\begin{align*}
\text{indirectnamemap} & \quad ::= \quad \text{vec(indirectnameassoc)} \\
\text{indirectnameassoc} & \quad ::= \quad \text{idx namemap} \\
\end{align*}
\]

Module Names

The module name subsection has the id 0. It simply consists of a single name that is assigned to the module itself.

\[
\text{modulenamesubsec} \quad ::= \quad \text{namesubsection}_0(\text{name})
\]
Function Names

The function name subsection has the id 1. It consists of a name map assigning function names to function indices.

\[
\text{funcnamesubsec ::= namesubsection}_1(\text{namemap})
\]

Local Names

The local name subsection has the id 2. It consists of an indirect name map assigning local names to local indices grouped by function indices.

\[
\text{localnamesubsec ::= namesubsection}_2(\text{indirectnamemap})
\]

7.5 Soundness

The type system of WebAssembly is sound, implying both type safety and memory safety with respect to the WebAssembly semantics. For example:

- All types declared and derived during validation are respected at runtime; e.g., every local or global variable will only contain type-correct values, every instruction will only be applied to operands of the expected type, and every function invocation always evaluates to a result of the right type (if it does not trap or diverge).

- No memory location will be read or written except those explicitly defined by the program, i.e., as a local, a global, an element in a table, or a location within a linear memory.

- There is no undefined behavior, i.e., the execution rules cover all possible cases that can occur in a valid program, and the rules are mutually consistent.

Soundness also is instrumental in ensuring additional properties, most notably, encapsulation of function and module scopes: no locals can be accessed outside their own function and no module components can be accessed outside their own module unless they are explicitly exported or imported.

The typing rules defining WebAssembly validation only cover the static components of a WebAssembly program. In order to state and prove soundness precisely, the typing rules must be extended to the dynamic components of the abstract runtime, that is, the store, configurations, and administrative instructions.\(^{50}\)

7.5.1 Results

Results can be classified by result types as follows.

\[\text{val}^*\]

- For each value \(\text{val}_i\) in \(\text{val}^*\):
  - The value \(\text{val}_i\) is valid with some value type \(t_i\).
- Let \(t^*\) be the concatenation of all \(t_i\).
- Then the result is valid with result type \([t^*]\).

\[
(S \vdash \text{val} : t)^* \\
S \vdash \text{val}^* : [t^*]
\]

\(^{50}\) The formalization and theorems are derived from the following article: Andreas Haas, Andreas Rossberg, Derek Schuff, Ben Titzer, Dan Gohman, Luke Wagner, Alon Zakai, JF Bastien, Michael Holman. Bringing the Web up to Speed with WebAssembly\(^{51}\). Proceedings of the 38th ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI 2017). ACM 2017.

\(^{51}\) https://dl.acm.org/citation.cfm?doid=3062341.3062363
Results \texttt{trap}

- The result is valid with \textit{result type} $[t^*]$, for any sequence $t^*$ of \textit{value types}.

\[
S \vdash \text{trap} : [t^*]
\]

7.5.2 Store Validity

The following typing rules specify when a runtime store $S$ is valid. A valid store must consist of \textit{function}, \textit{table}, \textit{memory}, \textit{global}, and \textit{module} instances that are themselves valid, relative to $S$.

To that end, each kind of instance is classified by a respective \textit{function}, \textit{table}, \textit{memory}, or \textit{global} type. Module instances are classified by \textit{module contexts}, which are regular \textit{contexts} repurposed as module types describing the \textit{index spaces} defined by a module.

\textbf{Store $S$}

- Each \textit{function instance} $\text{funcinst}_i$ in $S$.\textit{funcs} must be \textit{valid} with some \textit{function type} $\text{functype}_i$.
- Each \textit{table instance} $\text{tableinst}_i$ in $S$.\textit{tables} must be \textit{valid} with some \textit{table type} $\text{tabletype}_i$.
- Each \textit{memory instance} $\text{meminst}_i$ in $S$.\textit{mems} must be \textit{valid} with some \textit{memory type} $\text{memtype}_i$.
- Each \textit{global instance} $\text{globalinst}_i$ in $S$.\textit{globals} must be \textit{valid} with some \textit{global type} $\text{globaltype}_i$.
- Each \textit{element instance} $\text{eleminst}_i$ in $S$.\textit{elems} must be \textit{valid}.
- Each \textit{data instance} $\text{datainst}_i$ in $S$.\textit{datas} must be \textit{valid}.
- Then the store is valid.

\[
(S \vdash \text{funcinst}_i : \text{functype}_i)^*,
(S \vdash \text{tableinst}_i : \text{tabletype}_i)^*,
(S \vdash \text{meminst}_i : \text{memtype}_i)^*,
(S \vdash \text{globalinst}_i : \text{globaltype}_i)^*,
(S \vdash \text{eleminst}_i \text{ok})^*,
(S \vdash \text{datainst}_i \text{ok})^*
\]

\[
S = \{\text{funcs funcinst}^*, \text{tables tableinst}^*, \text{mems meminst}^*, \text{globals globalinst}^*, \text{elems eleminst}^*, \text{datas datainst}^*\}
\]

\[
S \vdash S \text{ok}
\]

\textbf{Function Instances} $\{\text{type functype}, \text{module moduleinst}, \text{code func}\}$

- The \textit{function type} $\text{functype}$ must be \textit{valid}.
- The \textit{module instance} $\text{moduleinst}$ must be \textit{valid} with some \textit{context} $C$.
- Under \textit{context} $C$, the \textit{function} $\text{func}$ must be \textit{valid} with \textit{function type} $\text{functype}$.
- Then the function instance is valid with \textit{function type} $\text{functype}$.

\[
\vdash \text{functype ok} \quad S \vdash \text{moduleinst} : C \quad C \vdash \text{func} : \text{functype}
\]

\[
S \vdash \{\text{type functype}, \text{module moduleinst}, \text{code func}\} : \text{functype}
\]
Host Function Instances \(\{\text{type functype, hostcode hf}\}\)

- The \textit{function type} \textit{functype} must be \textit{valid}.
- Let \(\lfloor t_1^* \rfloor \rightarrow \lfloor t_2^* \rfloor\) be the \textit{function type} \textit{functype}.
- For every \textit{valid store} \(S_1\) \textit{extending} \(S\) and every sequence \(\text{val}^*\) of \textit{values} whose \textit{types} coincide with \(t_1^*\):
  - Executing \(hf\) in store \(S_1\) with arguments \(\text{val}^*\) has a non-empty set of possible outcomes.
  - For every element \(R\) of this set:
    - Either \(R\) must be \(\bot\) (i.e., divergence).
    - Or \(R\) consists of a \textit{valid store} \(S_2\) \textit{extending} \(S_1\) and a \textit{result} \(\text{result}\) whose \textit{type} coincides with \(\lfloor t_2^* \rfloor\).
- Then the function instance is valid with \textit{function type} \textit{functype}.
  \[
  \forall S_1, \text{val}^*, \vdash S_1 \text{ ok} \wedge \vdash S \leq S_1 \wedge S_1 \vdash \text{val}^* : \lfloor t_1^* \rfloor \implies \exists hf(S_1; \text{val}^*), R = \bot \lor
  \vdash R \in hf(S_1; \text{val}^*), R = \top \\
  \vdash \lfloor t_1^* \rfloor \rightarrow \lfloor t_2^* \rfloor \text{ ok}
  \frac{\exists S_2, \text{result}, \vdash S_2 \text{ ok} \wedge \vdash S_1 \leq S_2 \wedge S_2 \vdash \text{result} : \lfloor t_2^* \rfloor \wedge R = (S_2; \text{result})}{S \vdash \{\text{type } \lfloor t_1^* \rfloor \rightarrow \lfloor t_2^* \rfloor, \text{hostcode } hf\} : \lfloor t_1^* \rfloor \rightarrow \lfloor t_2^* \rfloor}
  \]

\textbf{Note:} This rule states that, if appropriate pre-conditions about store and arguments are satisfied, then executing the host function must satisfy appropriate post-conditions about store and results. The post-conditions match the ones in the \textit{execution rule} for invoking host functions.

Any store under which the function is invoked is assumed to be an extension of the current store. That way, the function itself is able to make sufficient assumptions about future stores.

Table Instances \(\{\text{type (limits } t\text{), elem ref}^*\}\)

- The \textit{table type} \textit{limits} \(t\) must be \textit{valid}.
- The length of \(\text{ref}^*\) must equal \textit{limits}.\textit{min}.
- For each \textit{reference} \(\text{ref}_i\) in the table’s elements \(\text{ref}^n\):
  - The \textit{reference} \(\text{ref}_i\) must be \textit{valid} with \textit{reference type} \(t\).
- Then the table instance is valid with \textit{table type} \textit{limits} \(t\).
  \[
  \vdash \text{limits } t \text{ ok} \quad n = \text{limits}.\text{min} \quad (S \vdash \text{ref} : t)^n
  \frac{}{S \vdash \{\text{type (limits } t\text{), elem ref}^n\} : \text{limits } t}
  \]

Memory Instances \(\{\text{type limits, data } b^*\}\)

- The \textit{memory type} \(\{\text{min } n, \text{max } m^3\}\) must be \textit{valid}.
- The length of \(b^*\) must equal \textit{limits}.\textit{min} multiplied by the \textit{page size} \(64\text{ Ki}\).
- Then the memory instance is valid with \textit{memory type} \textit{limits}.
  \[
  \vdash \text{limits } ok \quad n = \text{limits}.\text{min} \cdot 64\text{ Ki}
  \frac{}{S \vdash \{\text{type limits, data } b^n\} : \text{limits } t}
  \]
Global Instances \{ \text{type (mut } t\text{), value } val \}

- The global type \text{mut } t must be valid.
- The value \text{val } must be valid with value type \text{t}.
- Then the global instance is valid with global type \text{mut } t.

\[
\frac{\vdash \text{mut } t \text{ ok} \quad S \vdash \text{val} : \text{t}}{S \vdash \{} \text{type (mut } t\text{), value } val \} : \text{mut } t
\]

Element Instances \{ \text{elem } fa}^* \}

- For each reference \text{ref}_i in the elements \text{ref}^n:
  - The reference \text{ref}_i must be valid with reference type \text{t}.
- Then the table instance is valid.

\[
\frac{S \vdash \text{ref} : \text{t}}{S \vdash \{} \text{type } t, \text{elem } \text{ref}^* \} \text{ ok}
\]

Data Instances \{ \text{data } b}^* \}

- The data instance is valid.

\[
S \vdash \{} \text{data } b^* \} \text{ ok}
\]

Export Instances \{ \text{name name, value } externval \}

- The external value \text{externval} must be valid with some external type \text{externtype}.
- Then the export instance is valid.

\[
S \vdash \text{externval} : \text{externtype} \\
S \vdash \{} \text{name name, value } \text{externval} \} \text{ ok}
\]

Module Instances \text{moduleinst}\]

- Each function type \text{func}\text{type}_i in \text{moduleinst.types} must be valid.
- For each function address \text{funcaddr}_i in \text{moduleinst.funcaddrs}, the external value \text{func} \text{funcaddr}_i must be valid with some external type \text{func}\text{type}_i.
- For each table address \text{tableaddr}_i in \text{moduleinst.tableaddrs}, the external value \text{table} \text{tableaddr}_i must be valid with some external type \text{table}\text{tabletype}_i.
- For each memory address \text{memaddr}_i in \text{moduleinst.memaddrs}, the external value \text{mem} \text{memaddr}_i must be valid with some external type \text{mem}\text{memtype}_i.
- For each global address \text{globaladdr}_i in \text{moduleinst.globaladdrs}, the external value \text{global} \text{globaladdr}_i must be valid with some external type \text{global}\text{globaltype}_i.
- For each element address \text{elemaddr}_i in \text{moduleinst.elemaaddr}, the element instance \text{S.elems}[elemaddr_i] must be valid.
- For each data address \text{dataaddr}_i in \text{moduleinst.dataaddrs}, the data instance \text{S.datas}[dataaddr_i] must be valid.

7.5. Soundness
• Each export instance \(\text{exportinst}_i\) in \(\text{moduleinst.exports}\) must be valid.

• For each export instance \(\text{exportinst}_i\) in \(\text{moduleinst.exports}\), the name \(\text{exportinst}_i\).name must be different from any other name occurring in \(\text{moduleinst.exports}\).

• Let \(\text{functype}^*\) be the concatenation of all \(\text{functype}_i\) in order.

• Let \(\text{tabletype}^*\) be the concatenation of all \(\text{tabletype}_i\) in order.

• Let \(\text{memtype}^*\) be the concatenation of all \(\text{memtype}_i\) in order.

• Let \(\text{globaltype}^*\) be the concatenation of all \(\text{globaltype}_i\) in order.

• Then the module instance is valid with context

\[
\{\text{types functype}^*, \text{funcs functype}^*, \text{tables tabletype}^*, \text{mems memtype}^*, \text{globals globaltype}^*\}.
\]

\[
\begin{align*}
(S \vdash \text{func} \text{funcaddr} : \text{func functype})^* & \quad (S \vdash \text{table} \text{tableaddr} : \text{table tabletype})^* \\
(S \vdash \text{mem} \text{memaddr} : \text{mem memtype})^* & \quad (S \vdash \text{global} \text{globaladdr} : \text{global globaltype})^* \\
(S \vdash S.\text{elems}[\text{elemaddr}] \text{ok})^* & \quad (S \vdash S.\text{datas}[\text{dataaddr}] \text{ok})^* \\
(S \vdash \text{exportinst ok})^* & \quad (\text{exportinst.name} \text{disjoint})
\end{align*}
\]

\[
S \vdash \{\text{types functype}^*, \text{funcaddrs funcaddr}^*, \text{tableaddrs tableaddr}^*, \text{memaddrs memaddr}^*, \text{globaladdrs globaladdr}^*, \text{elemaddrs elemaddr}^*, \text{dataaddrs dataaddr}^*, \text{exports exportinst}^*\} : \{\text{types functype}^*, \text{funcs functype}^*, \text{tables tabletype}^*, \text{mems memtype}^*, \text{globals globaltype}^*\}
\]

### 7.5.3 Configuration Validity

To relate the WebAssembly type system to its execution semantics, the typing rules for instructions must be extended to configurations \(S; T\), which relates the store to execution threads.

Configurations and threads are classified by their result type. In addition to the store \(S\), threads are typed under a return type \(\text{resulttype}^*\), which controls whether and with which type a return instruction is allowed. This type is absent (\(\epsilon\)) except for instruction sequences inside an administrative frame instruction.

Finally, frames are classified with frame contexts, which extend the module contexts of a frame’s associated module instance with the locals that the frame contains.

#### Configurations \(S; T\)

- The store \(S\) must be valid.

- Under no allowed return type, the thread \(T\) must be valid with some result type \([t^*]\).

- Then the configuration is valid with the result type \([t^*]\).

\[
\begin{align*}
\vdash S \text{ok} & \quad S;\epsilon \vdash T : [t^*] \\
\vdash S; T : [t^*]
\end{align*}
\]
Threads $F; instr^*$

- Let $resulttype^?$ be the current allowed return type.
- The frame $F$ must be valid with a context $C$.
- Let $C'$ be the same context as $C$, but with return set to $resulttype^?$.
- Under context $C'$, the instruction sequence $instr^*$ must be valid with some type $[] \rightarrow [t^*]$.
- Then the thread is valid with the result type $[t^*]$.

$$
S \vdash F : C \quad S; C, return resulttype^? \vdash instr^* : [] \rightarrow [t^*] \\
S; resulttype^? \vdash F; instr^* : [t^*]
$$

Frames $\{locals\, val^*,\, module\, moduleinst\}$

- The module instance $moduleinst$ must be valid with some module context $C$.
- Each value $val_i$ in $val^*$ must be valid with some value type $t_i$.
- Let $t^*$ the concatenation of all $t_i$ in order.
- Let $C'$ be the same context as $C$, but with the value types $t^*$ prepended to the locals vector.
- Then the frame is valid with frame context $C'$.

$$
S \vdash moduleinst : C \quad (S \vdash val : t)^* \\
S \vdash \{locals\, val^*,\, module\, moduleinst\} : (C; locals\, t^*)
$$

7.5.4 Administrative Instructions

Typing rules for administrative instructions are specified as follows. In addition to the context $C$, typing of these instructions is defined under a given store $S$. To that end, all previous typing judgements $C \vdash prop$ are generalized to include the store, as in $S; C \vdash prop$, by implicitly adding $S$ to all rules – $S$ is never modified by the pre-existing rules, but it is accessed in the extra rules for administrative instructions given below.

**trap**

- The instruction is valid with type $[t_1^*] \rightarrow [t_2^*]$, for any sequences of value types $t_1^*$ and $t_2^*$.

$$
S; C \vdash trap : [t_1^*] \rightarrow [t_2^*]
$$

**ref.extern externaddr**

- The instruction is valid with type $[] \rightarrow [externref]$.

$$
S; C \vdash ref.extern\, externaddr : [] \rightarrow [externref]
$$
ref funcaddr

- The external function value func funcaddr must be valid with external function type func functype.
- Then the instruction is valid with type $[] \rightarrow [\text{funcref}]$.

\[
S \vdash \text{func} \text{funcaddr} : \text{funcfunctype} \\
S; C \vdash \text{ref} \text{funcaddr} : [] \rightarrow [\text{funcref}]
\]

invoke funcaddr

- The external function value func funcaddr must be valid with external function type func($t_1^* \rightarrow t_2^*$).
- Then the instruction is valid with type $[t_1^*] \rightarrow [t_2^*]$.

\[
S \vdash \text{func} \text{funcaddr} : \text{func} [t_1^*] \rightarrow [t_2^*] \\
S; C \vdash \text{invoke} \text{funcaddr} : [t_1^*] \rightarrow [t_2^*]
\]

label$_n${instr$_0^*$} instr* end

- The instruction sequence instr$_0^*$ must be valid with some type $[t_1^*] \rightarrow [t_2^*]$.
- Let $C'$ be the same context as $C$, but with the result type $[t_2^*]$ prepended to the labels vector.
- Under context $C'$, the instruction sequence instr* must be valid with type $[] \rightarrow [t_2^*]$.
- Then the compound instruction is valid with type $[] \rightarrow [t_2^*]$.

\[
S; C \vdash \text{instr}_0^* : [t_1^*] \rightarrow [t_2^*] \\
S; C, \text{labels} [t_1^*] \vdash \text{instr}^* : [] \rightarrow [t_2^*] \\
S; C \vdash \text{label}_n \{ \text{instr}_0^* \} \text{instr}^* \text{end} : [] \rightarrow [t_2^*]
\]

frame$_n${$F$} instr* end

- Under the return type $[t_1^*]$, the thread $F$; instr* must be valid with result type $[t_1^*]$.
- Then the compound instruction is valid with type $[] \rightarrow [t_1^*]$.

\[
S; [t_1^n] \vdash F; \text{instr}^* : [t_1^n] \\
S; C \vdash \text{frame}_n \{ F \} \text{instr}^* \text{end} : [] \rightarrow [t_1^n]
\]

7.5.5 Store Extension

Programs can mutate the store and its contained instances. Any such modification must respect certain invariants, such as not removing allocated instances or changing immutable definitions. While these invariants are inherent to the execution semantics of WebAssembly instructions and modules, host functions do not automatically adhere to them. Consequently, the required invariants must be stated as explicit constraints on the invocation of host functions. Soundness only holds when the embedder ensures these constraints.

The necessary constraints are codified by the notion of store extension: a store state $S'$ extends state $S$, written $S \preceq S'$, when the following rules hold.

Note: Extension does not imply that the new store is valid, which is defined separately above.
Store $S$

- The length of $S.funcs$ must not shrink.
- The length of $S.tables$ must not shrink.
- The length of $S.mems$ must not shrink.
- The length of $S.globals$ must not shrink.
- The length of $S.elems$ must not shrink.
- The length of $S.datas$ must not shrink.
- For each function instance $funcinst_1$ in the original $S.funcs$, the new function instance must be an extension of the old.
- For each table instance $tableinst_1$ in the original $S.tables$, the new table instance must be an extension of the old.
- For each memory instance $meminst_1$ in the original $S.mems$, the new memory instance must be an extension of the old.
- For each global instance $globalinst_1$ in the original $S.globals$, the new global instance must be an extension of the old.
- For each element instance $eleminst_1$ in the original $S.elems$, the new global instance must be an extension of the old.
- For each data instance $datainst_1$ in the original $S.datas$, the new global instance must be an extension of the old.

\[
\begin{align*}
S_1.funcs &= funcinst_1^* & S_2.funcs &= funcinst_1^* \text{funcinst}_2^* \quad (\vdash funcinst_1 \leq funcinst_1^*)^* \\
S_1.tables &= tableinst_1^* & S_2.tables &= tableinst_1^* \text{tableinst}_2^* \quad (\vdash tableinst_1 \leq tableinst_1^*)^* \\
S_1.mems &= meminst_1^* & S_2.mems &= meminst_1^* \text{meminst}_2^* \quad (\vdash meminst_1 \leq meminst_1^*)^* \\
S_1.globals &= globalinst_1^* & S_2.globals &= globalinst_1^* \text{globalinst}_2^* \quad (\vdash globalinst_1 \leq globalinst_1^*)^* \\
S_1.elems &= eleminst_1^* & S_2.elems &= eleminst_1^* \text{eleminst}_2^* \quad (\vdash eleminst_1 \leq eleminst_1^*)^* \\
S_1.datas &= datainst_1^* & S_2.datas &= datainst_1^* \text{datainst}_2^* \quad (\vdash datainst_1 \leq datainst_1^*)^*
\end{align*}
\]

\[\vdash S_1 \leq S_2\]

Function Instance $funcinst$

- A function instance must remain unchanged.

\[\vdash funcinst \leq funcinst\]

Table Instance $tableinst$

- The table type $tableinst$.type must remain unchanged.
- The length of $tableinst$.elem must not shrink.

\[n_1 \leq n_2 \quad \vdash \{\text{type } tt, \text{elem } (fa_1)_{n_1}\} \leq \{\text{type } tt, \text{elem } (fa_2)_{n_2}\}\]
Memory Instance \textit{meminst}

- The \textit{memory type} \textit{meminst.type} must remain unchanged.
- The length of \textit{meminst.data} must not shrink.

\[
\begin{align*}
& n_1 \leq n_2 \\
\vdash & \{\text{type } mt, \text{data } b_1^n \} \preceq \{\text{type } mt, \text{data } b_2^n \}
\end{align*}
\]

Global Instance \textit{globalinst}

- The \textit{global type} \textit{globalinst.type} must remain unchanged.
- Let \textit{mut t} be the structure of \textit{globalinst.type}.
- If \textit{mut} is \textit{const}, then the \textit{value} \textit{globalinst.value} must remain unchanged.

\[
\begin{align*}
&\text{mut} = \text{var} \lor \text{val}_1 = \text{val}_2 \\
\vdash & \{\text{type (mut t)}, \text{value val}_1 \} \preceq \{\text{type (mut t)}, \text{value val}_2 \}
\end{align*}
\]

Element Instance \textit{eleminst}

- The vector \textit{eleminst.elem} must either remain unchanged or shrink to length 0.

\[
\begin{align*}
& f_a^*_1 = f_a^*_2 \lor f_a^*_2 = \epsilon \\
\vdash & \{\text{elem } f_a^*_1 \} \preceq \{\text{elem } f_a^*_2 \}
\end{align*}
\]

Data Instance \textit{datainst}

- The vector \textit{datainst.data} must either remain unchanged or shrink to length 0.

\[
\begin{align*}
& b_1^* = b_2^* \lor b_2^* = \epsilon \\
\vdash & \{\text{data } b_1^* \} \preceq \{\text{data } b_2^* \}
\end{align*}
\]

7.5.6 Theorems

Given the definition of \textit{valid configurations}, the standard soundness theorems hold.\textsuperscript{52}

\textbf{Theorem (Preservation).} If a \textit{configuration} \textit{S; T} is \textit{valid} with \textit{result type} \textit{[t*]} (i.e., \text{\vdash} \textit{S; T : [t*]}), and steps to \textit{S'; T'} (i.e., \textit{S; T} \iff \textit{S'; T'}), then \textit{S'; T'} is a \textit{valid configuration} with the same \textit{result type} (i.e., \text{\vdash} \textit{S'; T' : [t*]}). Furthermore, \textit{S'} is an \textit{extension} of \textit{S} (i.e., \text{\vdash} \textit{S \preceq S'}).

A \textit{terminal thread} is one whose sequence of \textit{instructions} is a \textit{result}. A \textit{terminal configuration} is a \textit{configuration} whose thread is terminal.

\textbf{Theorem (Progress).} If a \textit{configuration} \textit{S; T} is \textit{valid} (i.e., \text{\vdash} \textit{S; T : [t*]}) for some \textit{result type} \textit{[t*]}, then either it is terminal, or it can step to some \textit{configuration} \textit{S'; T'} (i.e., \textit{S; T} \iff \textit{S'; T'}). From Preservation and Progress the soundness of the WebAssembly type system follows directly.

\textbf{Corollary (Soundness).} If a \textit{configuration} \textit{S; T} is \textit{valid} (i.e., \text{\vdash} \textit{S; T : [t*]}) for some \textit{result type} \textit{[t*]}, then it either diverges or takes a finite number of steps to reach a \textit{terminal configuration} \textit{S'; T'} (i.e., \textit{S; T} \iff * \textit{S'; T'}) that is \textit{valid} with the same \textit{result type} (i.e., \text{\vdash} \textit{S'; T' : [t*]}) and where \textit{S'} is an \textit{extension} of \textit{S} (i.e., \text{\vdash} \textit{S \preceq S'}).

\textsuperscript{52} A machine-verified version of the formalization and soundness proof is described in the following article: Conrad Watt. \textit{Mechanising and Verifying the WebAssembly Specification}.\textsuperscript{53} Proceedings of the 7th ACM SIGPLAN Conference on Certified Programs and Proofs (CPP 2018), ACM 2018.

\textsuperscript{53} https://dl.acm.org/citation.cfm?id=3167082
In other words, every thread in a valid configuration either runs forever, traps, or terminates with a result that has the expected type. Consequently, given a *valid store*, no computation defined by *instantiation* or *invocation* of a valid module can “crash” or otherwise (mis)behave in ways not covered by the *execution* semantics given in this specification.

### 7.6 Change History

Since the original release 1.0 of the WebAssembly specification, a number of proposals for extensions have been integrated. The following sections provide an overview of what has changed.

#### 7.6.1 Release 2.0

**Sign extension instructions**

Added new numeric instructions for performing sign extension within integer representations\(^5\).

- New numeric instructions: `inn.extendN_s`

**Non-trapping float-to-int conversions**

Added new conversion instructions that avoid trapping when converting a floating-point number to an integer\(^5\).

- New numeric instructions: `inn.trunc_sat_fmm_sx`

**Multiple values**

Generalized the result type of blocks and functions to allow for multiple values; in addition, introduced the ability to have block parameters\(^5\).

- *Function types* allow more than one result
- *Block types* can be arbitrary function types

**Reference types**

Added `funcref` and `externref` as new value types and respective instructions\(^5\).

- New value types: reference types `funcref` and `externref`
- New reference instructions: `ref.null`, `ref.func`, `ref.is_null`
- Enrich parametric instruction: `select` with optional type immediate
- New declarative form of `element segment`\(^5\)

---

Table instructions

Added instructions to directly access and modify tables\(^2\).

- **Table types** allow any reference type as element type
- New **table instructions**: table.get, table.set, table.size, table.grow

Multiple tables

Added the ability to use multiple tables per module\(^2\).

- **Modules** may define, import, and export multiple tables
- **Table instructions** take a table index immediate: table.get, table.set, table.size, table.grow, call_indirect
- **Element segments** take a table index

Bulk memory and table instructions

Added instructions that modify ranges of memory or table entries\(^758\)

- New **memory instructions**: memory.fill, memory.init, memory.copy, data.drop
- New **table instructions**: table.fill, table.init, table.copy, elem.drop
- New passive form of **data segment**
- New passive form of **element segment**
- New **data count section** in binary format
- Active data and element segments boundaries are no longer checked at compile time but may trap instead

Vector instructions

Added vector type and instructions that manipulate multiple numeric values in parallel (also known as **SIMD**, single instruction multiple data)\(^59\)

- New **value type**: v128
- New **memory instructions**: v128.load, v128.load\(\mathsf{N}\_sx\), v128.load\(\mathsf{N}\_sx\), v128.load\(\mathsf{N}\_splat\), v128.load\(\mathsf{N}\_lane\), v128.store, v128.store\(\mathsf{N}\_lane\)
- New constant **vector instruction**: v128.const
- New unary **vector instructions**: v128.not, \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{abs}\), \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{neg}\), \(i_8\times16\).popcnt, \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{abs}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{neg}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{sqrt}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{ceil}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{floor}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{trunc}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{nearest}\)
- New binary **vector instructions**: v128.and, v128.andnot, v128.or, v128 xor, \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{add}\), \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{sub}\), \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{mul}\), \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{add}\_\mathsf{sat}\_sx\), \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{sub}\_\mathsf{sat}\_sx\), \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{min}\_sx\), \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{max}\_sx\), \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{shl}\), \(\mathsf{v}_\mathsf{v}\mathsf{v}\mathsf{M}\_\mathsf{shr}\_sx\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{add}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{extmul}\_\mathsf{half}\_i\mathsf{v}_\mathsf{v}\mathsf{M}\_f\_sx\), \(i_{16}\times8\).q15mul\(\mathsf{r}\_\mathsf{sat}\_s\), \(i_{32}\times4\).dot\(\mathsf{i}_{16}\times8\_s\), \(i_{16}\times8\).extadd\(\mathsf{>_}\_3\_i\times\mathsf{e}_{16}\_s\), \(i_{32}\times4\).extadd\(\mathsf{>_}\_3\_i\times\mathsf{e}_{16}\_s\), \(i\times8\).avgr\_\(u\), \(i\times8\).avgr\_\(u\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{sub}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{mul}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{div}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{min}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{max}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{pmin}\), \(f_\mathsf{v}_\mathsf{v}\mathsf{M}\_\mathsf{pmax}\)
- New ternary **vector instruction**: v128.bitselect
- New test **vector instructions**: v128.any_true, v128.all_true

\(^58\) https://github.com/WebAssembly/spec/tree/main/proposals/bulk-memory-operations/
\(^59\) https://github.com/WebAssembly/spec/tree/main/proposals/simd/
• New conversion vector instructions: i32x4.trunc_sat_f32x4 sx, i32x4.trunc_sat_f64x2 sx_zero, f32x4.convert_i32x4 sx, f32x4.demote_f64x2_zero, f64x2.convert_low_i32x4 sx, f64x2.promote_low_f32x4

• New lane access vector instructions: iN x M.extract_lane sx, iN x M.replace_lane, fN x M.extract_lane, fN x M.replace_lane

• New lane splitting/combining vector instructions: iN x M.extend_half iN x M' sx, i8x16.narrow_i16x8 sx, i16x8.narrow_i32x4 sx

• New byte reordering vector instructions: i8x16.shuffle, i8x16.swizzle

• New injection/projection vector instructions: iN x M.splat, fN x M.splat, iN x M.bitmask
Symbols

: abstract syntax
  administrative instruction, 62

A

abbreviations, 156
abstract syntax, 5, 131, 155, 190
  block type, 18, 28
  byte, 7
  data, 22, 50
  data address, 58
  data index, 20
  data instance, 60
  element, 22, 49
  element address, 58
  element index, 20
  element instance, 60
  element mode, 22
  export, 23, 50
  export instance, 60
  expression, 19, 46, 119
  external type, 11, 29
  external value, 60
  floating-point number, 7
  frame, 61
  function, 21, 47
  function address, 58
  function index, 20
  function instance, 59
  function type, 10, 28
  global, 22, 48
  global address, 58
  global index, 20
  global instance, 60
  global type, 11, 29
  grammar, 5
  host address, 58
  import, 23, 51
  instruction, 11–13, 16–18, 32–34, 37, 38, 40, 43, 87, 89, 90, 97, 99, 104, 113
  integer, 7
  label, 61
  label index, 20
  limits, 10, 28
  local, 21
  local index, 20
  memory, 21, 48
  memory address, 58
  memory index, 20
  memory instance, 59
  memory type, 10, 29
  module, 20, 52
  module instance, 59
  mutability, 11
  name, 8
  notation, 5
  number type, 9
  reference type, 9
  result, 58
  result type, 10
  signed integer, 7
  start function, 23, 50
  store, 58
  table, 21, 48
  table address, 58
  table index, 20
  table instance, 59
  table type, 11, 29
  type, 9
  type definition, 21
  type index, 20
  uninterpreted integer, 7
  unsigned integer, 7
  value, 6, 57
  value type, 10
  vector, 6, 8
  activation, 61
  active, 22, 22
  address, 58, 97, 99, 104, 113, 121
  data, 58
  element, 58
  function, 58
  global, 58
  host, 58
  memory, 58
  table, 58
  administrative instruction, 202, 203
<table>
<thead>
<tr>
<th>Page References</th>
<th>Indexes</th>
</tr>
</thead>
<tbody>
<tr>
<td>: abstract syntax, 62</td>
<td>vector type, 134</td>
</tr>
<tr>
<td>administrative instructions, 62</td>
<td>bit, 65</td>
</tr>
<tr>
<td>algorithm, 192</td>
<td>bit width, 7, 9, 64, 104</td>
</tr>
<tr>
<td>allocation, 58, 121, 184, 192</td>
<td>block, 18, 43, 113, 117, 136, 163, 207</td>
</tr>
<tr>
<td>arithmetic NaN, 7</td>
<td>type, 18</td>
</tr>
<tr>
<td>ASCII, 157, 158, 160</td>
<td>block context, 63</td>
</tr>
<tr>
<td>B</td>
<td>block type, 18, 28, 43, 136</td>
</tr>
<tr>
<td>binary format, 8, 131, 184, 191, 192, 196</td>
<td>abstract syntax, 18</td>
</tr>
<tr>
<td>block type, 136</td>
<td>binary format, 136</td>
</tr>
<tr>
<td>byte, 133</td>
<td>validation, 28</td>
</tr>
<tr>
<td>custom section, 148</td>
<td>Boolean, 3, 65, 66</td>
</tr>
<tr>
<td>data, 151</td>
<td>bottom type, 31</td>
</tr>
<tr>
<td>data count, 151</td>
<td>branch, 18, 43, 63, 113, 136, 163</td>
</tr>
<tr>
<td>data index, 147</td>
<td>byte, 7, 8, 22, 50, 59, 60, 66, 122, 131, 133, 151, 160, 178, 179, 188, 200, 201</td>
</tr>
<tr>
<td>element, 150</td>
<td>abstract syntax, 7</td>
</tr>
<tr>
<td>element index, 147</td>
<td>binary format, 133</td>
</tr>
<tr>
<td>export, 149</td>
<td>text format, 160</td>
</tr>
<tr>
<td>expression, 147</td>
<td>C</td>
</tr>
<tr>
<td>floating-point number, 133</td>
<td>call, 61, 62, 117</td>
</tr>
<tr>
<td>function, 149, 150</td>
<td>canonical NaN, 7</td>
</tr>
<tr>
<td>function index, 147</td>
<td>changes, 207</td>
</tr>
<tr>
<td>function type, 135</td>
<td>character, 2, 8, 157, 157, 158, 160, 190, 191</td>
</tr>
<tr>
<td>global, 149</td>
<td>text format, 157</td>
</tr>
<tr>
<td>global index, 147</td>
<td>closure, 59</td>
</tr>
<tr>
<td>global type, 136</td>
<td>code, 11, 191</td>
</tr>
<tr>
<td>grammar, 131</td>
<td>section, 150</td>
</tr>
<tr>
<td>import, 148</td>
<td>code section, 150</td>
</tr>
<tr>
<td>instruction, 136–138, 141</td>
<td>comment, 157, 158</td>
</tr>
<tr>
<td>integer, 133</td>
<td>concepts, 3</td>
</tr>
<tr>
<td>label index, 147</td>
<td>configuration, 56, 63, 202, 206</td>
</tr>
<tr>
<td>limits, 135</td>
<td>constant, 19, 22, 47, 57</td>
</tr>
<tr>
<td>local, 150</td>
<td>context, 25, 31, 37, 38, 40, 43, 52, 152, 201–203</td>
</tr>
<tr>
<td>local index, 147</td>
<td>control instruction, 18</td>
</tr>
<tr>
<td>memory, 149</td>
<td>control instructions, 43, 113, 136, 163</td>
</tr>
<tr>
<td>memory index, 147</td>
<td>custom section, 148, 196</td>
</tr>
<tr>
<td>memory type, 135</td>
<td>binary format, 148</td>
</tr>
<tr>
<td>module, 152</td>
<td>D</td>
</tr>
<tr>
<td>mutability, 136</td>
<td>data, 19–21, 22, 50, 52, 62, 123, 151, 152, 178–180, 190</td>
</tr>
<tr>
<td>name, 133</td>
<td>abstract syntax, 22</td>
</tr>
<tr>
<td>notation, 131</td>
<td>address, 58</td>
</tr>
<tr>
<td>number type, 134</td>
<td>binary format, 151</td>
</tr>
<tr>
<td>reference type, 134</td>
<td>index, 20</td>
</tr>
<tr>
<td>result type, 135</td>
<td>instance, 60</td>
</tr>
<tr>
<td>section, 147</td>
<td>section, 151</td>
</tr>
<tr>
<td>signed integer, 133</td>
<td>segment, 22, 50, 151, 178, 179</td>
</tr>
<tr>
<td>start function, 150</td>
<td>text format, 178, 179</td>
</tr>
<tr>
<td>table, 149</td>
<td>validation, 50</td>
</tr>
<tr>
<td>table index, 147</td>
<td>data address, 59, 123</td>
</tr>
<tr>
<td>table type, 135</td>
<td>abstract syntax, 58</td>
</tr>
<tr>
<td>type, 134</td>
<td>data count, 151</td>
</tr>
<tr>
<td>type index, 147</td>
<td>binary format, 151</td>
</tr>
<tr>
<td>type section, 148</td>
<td>section, 151</td>
</tr>
<tr>
<td>uninterpreted integer, 133</td>
<td>data count section, 151</td>
</tr>
<tr>
<td>unsigned integer, 133</td>
<td>data index, 20, 22, 147, 175</td>
</tr>
<tr>
<td>value, 132</td>
<td>value type, 135</td>
</tr>
<tr>
<td>value type, 135</td>
<td>vector, 132</td>
</tr>
</tbody>
</table>
Index 213
abstract syntax, 10
binary format, 135
text format, 161
validation, 28

global, 11, 17, 19, 20, 22, 23, 48, 52, 60, 123, 125, 149, 152, 178, 180, 189, 190
abstract syntax, 22
address, 58
binary format, 149
export, 23
import, 23
index, 20
instance, 60
mutability, 11
section, 149
text format, 178
type, 11
validation, 48
global address, 59, 60, 97, 120, 123, 125, 149, 152, 178, 180, 189, 190
abstract syntax, 58
global index, 17, 20, 22, 23, 37, 50, 97, 125, 137, 147, 149, 164, 175, 178
abstract syntax, 20
binary format, 147
text format, 175
global instance, 58, 59, 60, 97, 123, 125, 189, 192, 199, 200, 204, 206
abstract syntax, 60
global section, 149
global type, 11, 12, 22, 23, 25, 29, 31, 48, 51, 120, 123, 136, 148, 149, 162, 176, 178, 189, 199, 200
abstract syntax, 11
binary format, 136
text format, 162
validation, 29
global type, 25
grammar notation, 5, 131, 155
grow, 124

H
host, 2, 183
address, 58
host address, 57
abstract syntax, 58
host function, 59, 118, 122, 186, 199

I
identifier, 155, 156, 175–178, 180, 191
identifier context, 156, 180
identifiers, 160
text format, 160
IEEE 754, 2, 3, 7, 9, 66, 74

implementation, 183, 190
implementation limitations, 190
import, 2, 11, 20–22, 23, 47, 51, 52, 120, 125, 148, 152, 176–178, 180, 185, 190
abstract syntax, 23
binary format, 148
section, 148
text format, 176–178
validation, 51
import section, 148
index, 20, 23, 50, 59, 147, 149, 156, 162, 175–178, 197
data, 20
element, 20
function, 20
global, 20
label, 20
local, 20
memory, 20
table, 20
type, 20
index space, 20, 23, 25, 156, 197
instance, 59, 127
data, 60
element, 60
export, 60
function, 59
global, 60
memory, 59
module, 59
table, 59
instantiation, 4, 9, 23, 127, 185, 206
instantiation. module, 25
instruction, 3, 10, 11, 19, 31, 46, 59–63, 87, 117, 136, 162, 190, 192, 202, 204, 207, 208
abstract syntax, 11–13, 16–18
binary format, 136–138, 141
execution, 87, 89, 90, 97, 99, 104, 113
text format, 163–165, 168
validation, 32–34, 37, 38, 40, 43
instruction sequence, 46, 117
instructions, 208
integer, 3, 7, 8, 9, 12, 57, 64–66, 99, 104, 133, 159, 207
abstract syntax, 7
binary format, 133
signed, 7
text format, 159
uninterpreted, 7
unsigned, 7
invocation, 4, 59, 129, 186, 206

K
keyword, 157

L
label, 18, 43, 61, 62, 63, 113, 117, 136, 163, 192, 204
abstract syntax, 61
index, 20
label index, 18, 20, 43, 113, 136, 147, 162, 163, 175
abstract syntax, 20
binary format, 147
text format, 162, 175

lane, 8
LEB128, 133, 136
lexical format, 157
limits, 10, 11, 21, 28–31, 99, 104, 122, 124, 135, 162, 200
abstract syntax, 10
binary format, 135
memory, 10
table, 11
text format, 162
validation, 28

linear memory, 3
little endian, 17, 66, 133
local, 17, 20, 21, 47, 61, 150, 176, 190, 198, 203
abstract syntax, 21
binary format, 150
index, 20
text format, 176
local index, 17, 20, 21, 37, 47, 97, 137, 147, 164, 175, 198
abstract syntax, 20
binary format, 147
text format, 175

M
magnitude, 7
matching, 30, 125
memory, 3, 9, 10, 17, 20, 21, 22, 23, 48, 50, 52, 59, 60, 62, 66, 122, 124, 125, 149, 151, 152, 177–180, 188, 190, 208
abstract syntax, 21
address, 58
binary format, 149
data, 22, 50, 151, 178, 179
export, 23
import, 23
index, 20
instance, 59
limits, 10
section, 149
text format, 177
type, 10
validation, 48
memory address, 59, 60, 104, 120, 122, 124, 125, 188
abstract syntax, 58
memory index, 17, 20, 21–23, 40, 50, 104, 125, 137, 147, 149, 151, 165, 175, 178, 179
abstract syntax, 20
binary format, 147
text format, 175
memory instance, 58, 59, 59, 62, 104, 122, 124, 125, 188, 192, 199, 200, 204, 205
abstract syntax, 59
memory instruction, 17, 40, 104, 137, 165
memory section, 149
memory type, 10, 11, 21, 23, 25, 29, 31, 48, 51, 59, 120, 122, 135, 148, 149, 162, 176, 177, 188, 199, 200
abstract syntax, 10
binary format, 135
text format, 162
validation, 29
module, 2, 3, 20, 25, 52, 58, 59, 125, 127, 129, 131, 152, 180, 184, 186, 190–192, 197, 206
abstract syntax, 20
binary format, 152
instance, 59
text format, 180
validation, 52
module instance, 59, 61, 121, 125, 129, 185, 186, 192, 201, 203
abstract syntax, 59
module instruction, 63
mutability, 11, 11, 22, 29, 31, 60, 120, 123, 136, 162, 200, 206
abstract syntax, 11
binary format, 136
global, 11
text format, 162

N
name, 2, 8, 23, 50, 51, 59, 60, 133, 148, 149, 160, 176–178, 190, 196, 201
abstract syntax, 8
binary format, 133
text format, 160
name map, 197
name section, 180, 196
NaN, 7, 64, 75, 87
arithmetic, 7
canonical, 7
payload, 7
notation, 5, 131, 155
abstract syntax, 5
binary format, 131
text format, 155
null, 9, 16
number, 13, 57
type, 9
number type, 9, 10, 57, 134, 135, 161
abstract syntax, 9
binary format, 134
text format, 161
numeric instruction, 12, 32, 87, 138, 165
numeric vectors, 8, 13, 66

O
offset, 19
opcode, 136, 192, 195
operand, 11
operand stack, 11, 31
P
page size, 10, 17, 21, 59, 135, 162, 178
parameter, 10, 20, 190
parametric instruction, 16, 137, 164
parametric instructions, 37, 97
passive, 22, 22
payload, 7
phases, 4
polymorphism, 31, 37, 43, 136, 162, 178
portability, 1
preservation, 206
progress, 206
R
reduction rules, 56
reference, 9, 16, 57, 89, 99, 201, 207
type, 9
reference instruction, 16, 137, 164
reference instructions, 33, 89
reference type, 9, 10, 11, 29, 33, 57, 99, 134, 135, 161, 162, 207
abstract syntax, 9
binary format, 134
text format, 161
result, 10, 58, 186, 190, 198
abstract syntax, 58
type, 10
result type, 10, 10, 25, 46, 113, 135, 136, 161, 163, 198, 202, 204, 207
abstract syntax, 10
binary format, 135
resulttype, 25
rewrite rule, 156
rounding, 74
runtime, 57
S
S-expression, 155, 173
section, 147, 152, 191, 196
binary format, 147
code, 150
custom, 148
data, 151
data count, 151
element, 150
export, 149
function, 149
global, 149
import, 148
memory, 149
name, 180
start, 150
table, 149
type, 148
security, 2
segment, 62
shape, 66
sign, 66
signed integer, 7, 66, 133, 159
abstract syntax, 7
binary format, 133
text format, 159
significand, 7, 66
SIMD, 8, 9, 13, 208
soundness, 198, 206
source text, 157, 157, 191
stack, 55, 61, 129, 192
stack machine, 11
stack type, 18
start function, 20, 23, 50, 52, 150, 152, 179, 180
abstract syntax, 23
binary format, 150
section, 150
text format, 179
validation, 50
start section, 150
store, 55, 58, 58, 60, 61, 63, 87, 97, 99, 104, 113, 118, 120, 121, 127, 129, 184, 186–189, 199, 202–204
abstract syntax, 58
store extension, 204
string, 160
text format, 160
structured control, 18, 43, 113, 136, 163
structured control instruction, 190
T
table, 3, 9, 11, 17, 18, 20, 21, 22, 23, 48, 49, 52, 59, 60, 62, 122, 124, 125, 149, 152, 177, 180, 187, 190, 207, 208
abstract syntax, 21
address, 58
binary format, 149
element, 22, 49, 150, 177, 179
export, 23
import, 23
index, 20
instance, 59
limits, 10, 11
section, 149
text format, 177
type, 11
validation, 48
table address, 59, 60, 99, 113, 120, 122, 124, 125, 187
abstract syntax, 58
table index, 17, 20, 21–23, 38, 49, 50, 99, 125, 137, 147, 149, 150, 164, 175, 177–179, 208
abstract syntax, 20
binary format, 147
text format, 175
table instance, 58, 59, 59, 62, 99, 113, 122, 124, 125, 187, 192, 199, 200, 204, 205
abstract syntax, 59
table instruction, 17, 38, 99, 137, 164
table section, 149
Index

U

Unicode, 2, 8, 133, 155, 157, 160, 190
unicode, 191
Unicode UTF-8, 196
uninterpreted integer, 7, 66, 133, 159
abstract syntax, 7
binary format, 133
text format, 159
unsigned integer, 7, 66, 133, 159
abstract syntax, 7
binary format, 133
text format, 159
unwinding, 18
UTF-8, 2, 8, 133, 155, 160

V

validation, 4, 9, 25, 87, 120, 184, 191, 192
block type, 28
data, 50
element, 49
export, 50
expression, 46
external type, 29
function type, 28
global, 48
global type, 29
import, 51
instruction, 32–34, 37, 38, 40, 43
limits, 28
memory, 48
memory type, 29
module, 52
start function, 50
table, 48
table type, 29
validity, 206
valtype, 25
value, 3, 6, 12, 13, 22, 31, 57, 58, 60, 63, 64, 87, 97,
99, 104, 120, 123, 129, 132, 158, 186, 189,
192, 198, 200, 203, 206
abstract syntax, 6, 57
binary format, 132
external, 60
text format, 158
type, 10
value type, 10, 10–13, 16, 18, 21, 25, 29, 31, 37,
47, 57, 87, 104, 120, 123, 135–137, 161, 162,
164, 192, 198, 203, 207, 208
abstract syntax, 10
binary format, 135
text format, 161
variable instruction, 17
variable instructions, 37, 97, 137, 164
vector, 6, 10, 18, 22, 43, 113, 132, 136, 157, 163
abstract syntax, 6, 8
binary format, 132
text format, 157
vector instruction, 13, 34, 90, 141, 168
vector number, 57
vector type, 9, 10, 57, 134, 161, 208
binary format, 134
text format, 161
version, 152

W
white space, 157, 158